Topologies of Three-Phase Rectifier with Near Sinusoidal Input Currents – A Comparative Analysis of Power Quality Indices using FFT and DWT

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Abstract - The traditional method used to detect and localize power quality disturbances by Fast Fourier Transform (FFT) can provide only amplitude - frequency information, but looses time information. In evaluating power supply quality, the FFT-based indices present different limitations. This paper proposes a set of four indices based on Discrete Wavelet Transform (DWT), such as total harmonic distortion, equivalent distortion index, K-factor, and crest factor that can be also applied for non-stationary signals analysis, in unbalanced three-phase systems with non-sinusoidal conditions. In the comparative analysis of Power Quality Indices (PQIs) definitions, these two instruments were used for the three-phase power systems with non-linear loads: three rectifier topologies with near sinusoidal input currents (RNSIC).

Keywords—Discrete Wavelet Transform (DWT), data acquisition system, Power Quality Indices (PQIs), rectifier with near sinusoidal currents (RNSIC), harmonics.

I. Introduction

In recent years, electric power quality has been receiving great interest due to the increased usage of nonlinear loads in the power systems area [1-5].

The power systems can suffer from different waveform distortions caused by multiple power quality disturbances, which can be classified as stationary (with time unchangeable characteristics) or non-stationary (with time changeable characteristics), sinusoidal or non-sinusoidal, respectively [6-13], [33-35].

The Power Quality Indices (PQIs) evaluation based on the Fast Fourier Transform (FFT) involves limitations as high computational effort, time related information loss, and spectral leakage, therefore new approaches are necessary. Definitions that are more accurate can be obtained using Discrete Wavelet Transform (DWT), which provides representations for any non-stationary waveforms, preserves time-frequency information, and reduces the computational effort [14-17].

To reduce the current harmonics, many electric power applications use uncontrolled three-phase rectifiers with near sinusoidal input currents, called RNSICs, which offer high dynamic performances, and allow getting near sinusoidal input currents. Such a modern rectifier is the AC-to-DC converter,

which comprises three input inductors, a three-phase bridge rectifier with diodes, and commutation capacitors that enhance the input currents and the power factors.

This paper proposes a comparative analysis of PQIs definitions based on Discrete Wavelet Transform (DWT), and on Fast Fourier Transform (FFT) based on IEEE Standard 1459-2010 [18, 19] for unbalanced nonlinear three-phase systems in sinusoidal regime using three rectifier topologies with near sinusoidal input currents (RNSIC). The generated waveforms are captured with a data acquisition system, which contains voltage and current Hall transducers.

II. THE PQIS DEFINITIONS BASED ON FFT/DWT IN THE THREE-PHASE SYSTEMS CASE

A precise detection and analysis of power quality information can be realized by applying the wavelet theory. The method presented in this study involves a layer-by-layer decomposition, choice of suitable sampling frequency and suitable mother wavelet, and a harmonic real-time tracking [12].

A. IEEE Standard three-phase definitions

In the case of non-sinusoidal regime, PQIs definitions can be deduced considering the balanced three-phase system as three single-phase systems [18, 19].

1. Total Harmonic Distortion

Based on the effective voltage and current, the equivalent total harmonic distortion can be calculated as follows:

$$THD_{eV} = \frac{V_{eH}}{V_{el}} \tag{1}$$

$$V_{eH} = \sqrt{\frac{V_{RSH}^2 + V_{STH}^2 + V_{TRH}^2}{9}}, V_{el} = \sqrt{\frac{V_{RSI}^2 + V_{STI}^2 + V_{TRI}^2}{9}}.$$
 (2)

where V_{RS} , V_{ST} and V_{TR} are the line-to-line voltage for phases R, S and T, while the subscripts I and H represent the fundamental component and the non-fundamental harmonic components, respectively [20, 21].

The equivalent total harmonic distortion in the current case is defined as:

$$THD_{e1} = \frac{I_{eH}}{I_{e1}}$$
 (3)

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$$I_{eH} = \sqrt{\frac{I_{RH}^2 + I_{SH}^2 + I_{TH}^2}{3}}, \quad I_{el} = \sqrt{\frac{I_{R1}^2 + I_{S1}^2 + I_{T1}^2}{3}}. \quad (4)$$

where I_R , I_S and I_T are the line currents for phases R, S and T[20].

2. Equivalent Distortion Index

When the fundamental component does not exist, it can be used the equivalent distortion index calculated as:

$$DIN_{eV} = \frac{V_{eH}}{V_e}, DIN_{e1} = \frac{I_{eH}}{I_e}$$
 (5)

$$V_e = \sqrt{\frac{V_{RS}^2 + V_{ST}^2 + V_{TR}^2}{9}} , I_e = \sqrt{\frac{I_R^2 + I_S^2 + I_T^2}{3}} .$$
 (6)

3. Transformer K-Factor

For each current phase, K-factor can be defined as a summation of harmonic currents generated by the non-linear load using the following algorithm:

$$I_{Rhar}^{2} = \sum_{h=1}^{\infty} h^{2} I_{Rh}^{2}, I_{Shar}^{2} = \sum_{h=1}^{\infty} h^{2} I_{Sh}^{2}, I_{Thar}^{2} = \sum_{h=1}^{\infty} h^{2} I_{Th}^{2}.$$
 (7)

The equivalent harmonic is represented by:

$$I_{ehar} = \sqrt{\frac{I_{Rhar}^2 + I_{Shar}^2 + I_{Thar}^2}{3}}.$$
 (8)

Transformer K-factor can be calculated using relation (9):

$$K = \frac{I_{\text{ehar}}}{I_{\text{e}}} \,. \tag{9}$$

4. Crest Factor

The crest factor that appears in three-phase system defines the impact on a waveform, and can be calculated as a ratio between the peak amplitude and RMS value of the current

$$CF_{R} = \frac{I_{R \text{ peak}}}{I_{R \text{ rms}}}, CF_{S} = \frac{I_{S \text{ peak}}}{I_{S \text{ rms}}}, CF_{T} = \frac{I_{T \text{ peak}}}{I_{T \text{ rms}}}.$$
 (10)

Generally, three-phase crest factor represents maximum value identified on each phase:

$$CF_{ov} = \max\{CF_R, CR_S, CF_T\}. \tag{11}$$

Three-phase PQIs definitions based on DWT

Total Harmonic Distortion

In the time-frequency domain, THD can be defined as [22]:

$$THD_{v}^{DWT} = \frac{V_{det}}{V_{ann}} = \frac{\sqrt{\sum_{j \ge j_{0}} V_{j}^{2}}}{V_{i_{0}}}, THD_{i}^{DWT} = \frac{I_{det}}{I_{ann}} = \frac{\sqrt{\sum_{j \ge j_{0}} I_{j}^{2}}}{I_{i_{0}}}. (12)$$

where $V_{j_0,x}$, $I_{j_0,x}$ represent the voltage and current RMS values of the lowest frequency band j_0 , called approximated voltage (V_{xapp}) and approximated current (I_{xapp}) for any phase x of the three-phase, respectively. The sets of voltage and current RMS values $\{V_{j,x}\}$ and $\{I_{j,x}\}$ of each frequency band or wavelet-level higher than or equal to the scaling level j_0 , are

termed detailed voltage (V_{xdet}) and detailed current (I_{xdet}) for any phase x respectively [17, 20].

2. Equivalent Distortion Index

The voltage and current equivalent total harmonic distortion can be formulated as:

$$DIN_{v}^{DWT} = \frac{V_{det}}{V_{e}} = \frac{\sqrt{\sum_{j \ge j_{0}} V_{j}^{2}}}{\sqrt{\sum_{j} V_{j}^{2}}}, \quad DIN_{i}^{DWT} = \frac{I_{det}}{I_{e}} = \frac{\sqrt{\sum_{j \ge j_{0}} I_{j}^{2}}}{\sqrt{\sum_{j} I_{j}^{2}}}.$$
 (13)

where V_e and I_e represent the effective RMS values for threephase voltages and currents.

3. Transformer K-Factor

This factor can be computed starting from the harmonic weighted RMS phase currents for any phase x as:

$$I_{x \text{ lev}}^{2} = \sum_{n=1}^{2^{j}-1} (I_{j}^{n})^{2} (I_{xj}^{n})^{2}$$
(14)

where 1_i^n is the center frequency of the sub-band of detail nand decomposition level j. The detail weighted effective RMS current, which can be defined as

$$I_{e}_{lev} = \sqrt{\frac{I_{R lev}^2 + I_{S lev}^2 + I_{T lev}^2}{3}}$$
 (15)

The K-factor is given by the ratio:

$$K^{DWT} = \frac{I_{e \text{ lev}}}{I_{e}}.$$
 (16)

Crest Factor

The phase crest factor is calculated using the relation:

$$CF_x^{DWT} = \frac{\max_{k} \left\{ i_{k,x} \right\}}{I_x} \text{ for } k = 0, 1,...,2^{N-j}-1$$
 (17)

where I_x is RMS current value. The phase crest factor indicates the impact of a waveform on each phase. For different values of j, different decomposition levels of the phase crest factors can be obtained.

The overall crest factor is computed as:
$$CF_{ov}^{DWT} = \max_{k} \left\{ CF_{x}^{DWT} \right\} \text{ for } x = R, S, T.$$
(18)

III. THE RECTIFIERS IN THREE-PHASE POWER SYSTEMS

In many power electronics applications for three-phase systems, with AC 50 or 60-Hz power supply, the alternative sinusoidal voltage must be converted in DC voltage. These applications are represented by the uninterruptible power supply from commutations (UPS systems), electrical drives with AC motors and static frequency convertors, which are using three-phase uncontrolled diode rectifiers [23-32].

A. The Rectifier with Near Sinusoidal Input Currents (RNSIC-1 topology)

The higher order harmonics deletion of the network current can be obtained not only by using the filtering systems (active or passive) or the PWM rectifiers, but also using a AC-DC

PEMC 2014 732 converter, which has practically sinusoidal input current, also known as RNSIC (Rectifier with Near Sinusoidal Input Currents) [23-31].

This rectifier is a classic type of three-phase bridge rectifier with parallel diode capacitors and serial inductors attached. It can be seen in Figure 1.

The capacitors C_I - C_6 are DC capacitors with the same C value. The inductances L_R , L_S and L_T have the same L value, and are connected on AC side. To obtain near sinusoidal drawn phase-currents i_R , i_S , and i_T , the relation between the two values L and C has to be [30-31]:

$$0.05 \le LC\omega^2 \le 0.10$$
 (19)

The functioning principle of this rectifier can be explained using the following steps contained in a third period of the network frequency $T=2\pi/\omega$ (ω is the angle frequency).

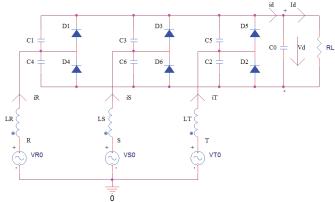


Figure 1. The RNSIC-1 electric scheme [23]

Two extreme situations of the RNSIC-1 convertor functioning can be discussed. In the first situation, if $R_L = 0$ (hence $V_d = 0$ and $\omega t_1 = 0$), the capacitors C_I - C_6 are short-circuited, and the angle $\varphi = +90^{\circ}$ is inductive. In this case, the phase currents are sinusoidal and have a I_{max} maximum amplitude [30].

In the second situation, if the V_d voltage is higher than $\sqrt{2} V_m / (1 - 2LC\omega^2)$ value, the diodes $D_I - D_6$ are no longer conductive and the $\varphi = -90^\circ$ angle is capacitive (hence $R_L = \infty$ and $\omega t_1 = \pi$). In this case, the phase currents i_R , i_S , and i_T are sinusoidal as well, and have a $\sqrt{2} I_{min}$ minimum value magnitude, also called holding current. The ratio I_{max}/I_{min} has the value [23]:

$$\frac{\left| \frac{I_{\text{max}}}{I_{\text{min}}} \right| = \frac{(1 - 2 L C \omega^{2})}{2 L C \omega^{2}} . \tag{20}$$

B. The Rectifier with Near Sinusoidal Input Currents (RNSIC-2 topology)

Next, it is presented a rectifier configuration with near sinusoidal input currents, and three capacitors C_1 - C_3 , which are connected on AC side (Figure 2). The supply voltages of these capacitors are limited to $\pm V_d$.

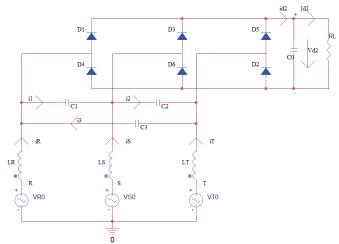


Figure 2. The RNSIC-2 electric scheme [30]

The φ angle variation between the phase voltage and phase current (V_{R0} and i_R) depends on the V_{d2} and V_{dmax} ratio.

For the higher load currents I_{d2} , with $0 \le \omega t \le \pi/3$, for both topologies, RNSIC-1 and RNSIC-2, the phase current waveforms and the conduction period of D_1 - D_6 diodes are identical.

A distinction can be observed at the small load currents I_{d2} , with $\pi/3 \le \omega t \le 2\pi/3$, when the diodes conduction periods are different: for the RNSIC-2 topology none or two diodes can be simultaneous in conduction [23].

C. The Rectifier with Near Sinusoidal Input Currents (RNSIC-3 topology)

This RNSIC topology is composed of three series inductors L_R , L_S , and L_T and commutation capacitors C_1 - C_3 (See Figure 3). For small currents, the RNSIC-3 is considered to be equivalent with RNSIC-1. Although it has a simple configuration, it has the largest DC current ripple [33].

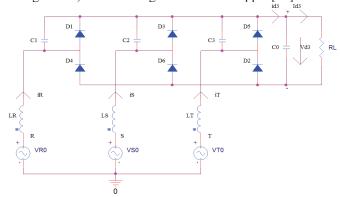


Figure 3. The RNSIC-3 electric scheme [33]

Such an active filter is placed in the network node, and serves multiple loads. This case does not absolutely exclude the appearance of some resonances due to both the presence of some capacitors bank, and the current harmonics. For the small nonlinear loads, it can be used so many RNSIC converters as many loads exist. Therefore, it is avoided the appearance of the current harmonics in different parts of the supply network.

IV. RESULTS AND ANALYSIS

This section presents the PQ indices calculation involving the three rectifier topologies with near sinusoidal input currents (RNSIC), and DWT and FFT comparative analysis of these values.

The experimental tests on a lab stand use the filtering capacitor C_0 of 4700 μ F, the inductance L_1 - L_3 equal with 16 mH, the CS411299 rectifier bridge with D_1 - D_6 diodes and the load resistor $R_L = 100 \Omega$. The AC input voltage magnitude V_{m1}

is equal to 325 V. The RNSIC converter configuration also contains the C_1 - C_6 capacitors (for RNSIC-1) or C_1 - C_3 capacitors (for RNSIC-2 and RNSIC-3), with 60 μ F value (See Figure 4).

The waveforms of voltage and current are obtained using a data acquisition system with linear/nonlinear loads composed of a Digital Signal Processor (DSP) board with high-speed 12 bit Analog to Digital Converters (ADC), which coupled to three-phase systems using the Hall transducers.

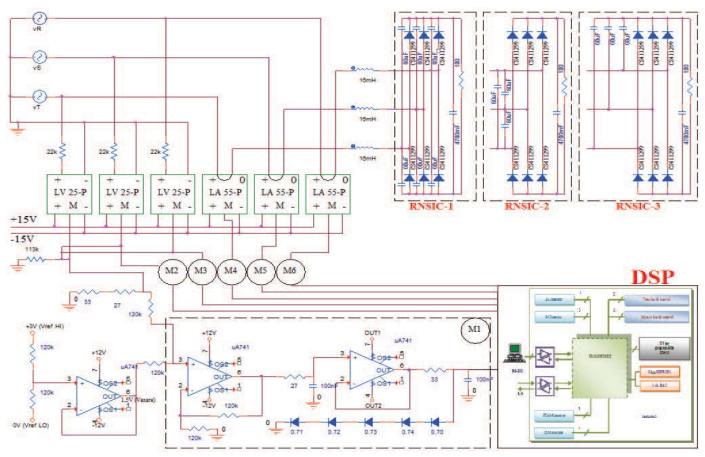


Figure 4. The data acquisition system with Hall transducers with RNSIC

The phase voltage waveforms V_R (CH2, 100 V/div.) and the phase current waveforms I_R (CH1, 10 A/div.) captured using the oscilloscope are shown in Figure 5. For a 50 Hz

frequency and 5 ms period it can be seen that the R phase current waveform is sinusoidal. The AC input voltage magnitude V_{ml} is equal to 325 V.

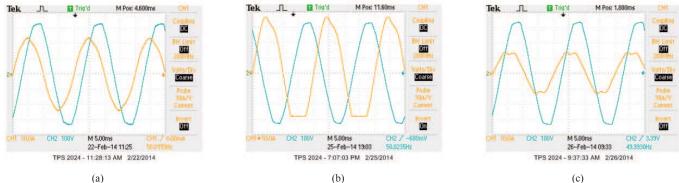
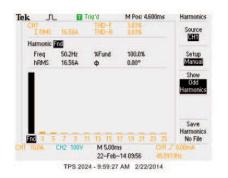
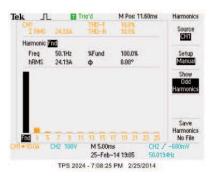


Figure 5. The current and voltage phase R waveforms of RNSIC with resistive load $R_L = 100 \Omega$: (a) RNSIC-1, (b) RNSIC-2 and (c) RNSIC-3

For RNSIC-1 the current harmonic components represent only 3.83% of the current fundamental harmonic, while for

RNSIC-2 and RNSIC-3, it represents 10.6 and 8.15, respectively (see Figure 6).





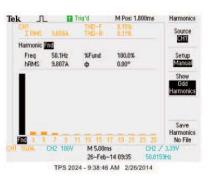


Figure 6. The spectrum of I_R phase current for RNSIC with resistive load $R_L = 100 \Omega$: (a) RNSIC-1, (b) RNSIC-2 and (c) RNSIC-3

A frequency domain analysis using FFT, and a time-frequency domain analysis using DWT have been carried out by Matlab software. The results are presented in Table I. The power quality indices were calculated for two resistive loads of $100~\Omega$, and $3.2~k\Omega$, respectively.

In wavelet analysis, it was used the 'db10' (Daubechies), which is considered the most suitable mother wavelet, due to its capacity of giving the least deviation between the calculated percentage energy of the harmonics and the percentage energy of the coefficients for the wavelet levels [11].

TABLE I. PQIS VALUES FOR RECTIFIER WITH NEAR SINUSOIDAL INPUT CURRENTS SYSTEMS

		Electric component	RNSIC-1		RNSIC-2		RNSIC-3	
	PQ index		FFT approach	DWT approach	FFT approach	DWT approach	FFT approach	DWT approach
$R_L = 100 \Omega,$ $V_{m1} = 325 V,$ $f = 50 Hz$	Equivalent Total Harmonic Distortion	THD_{v}	0.0183	0.0187	0.2101	0.2136	0.1523	0.1519
		THDi	0.0144	0.0148	0.1145	0.1158	0.0743	0.0741
	Distortion index	DIN_{v}	0.0184	0.0192	0.2045	0.2057	0.1411	0.1415
		DIN_i	0.0143	0.0146	0.1094	0.1103	0.0694	0.0703
	Transformer K-factor	K	1.9256	1.9386	3.1454	3.1422	2.6457	2.6430
	Phase crest factor	CF_R	1.72	1.74	1.84	1.84	1.79	1.80
		CF_S	1.74	1.75	1.86	1.87	1.82	1.79
		CF_T	1.73	1.72	1.89	1.88	1.80	1.81
	Overall crest factor	CF	1.74	1.75	1.89	1.88	1.82	1.81
$R_L = 3.2 \text{ k}\Omega$, $V_{\text{ml}} = 325 \text{ V}$, f = 50 Hz	Equivalent Total Harmonic Distortion	THD_{v}	0.2078	0.2085	0.2142	0.2139	0.2452	0.2454
		THD_i	0.1124	0.1128	0.1432	0.1437	0.2064	0.2061
	Distortion index	DIN_{v}	0.2034	0.2036	0.2103	0.2100	0.2447	0.2446
		DINi	0.1117	0.1121	0.1388	0.1391	0.2031	0.2033
	Transformer K-factor	K	3.4256	3.5467	3.5678	3.5680	3.7415	3.7411
	Phase crest factor	CF_R	1.79	1.82	1.81	1.83	1.92	1.93
		CF _S	1.92	1.95	1.92	1.91	1.95	1.96
		CF _T	1.87	1.90	1.90	1.91	1.97	1.98
	Overall crest factor	CF	1.92	1.95	1.92	1.91	1.97	1.98

V. CONCLUSION

This paper has proposed a comparative analysis of the power quality indices obtained with DWT and FFT using three rectifier topologies with near sinusoidal input currents.

The experimental results highlight the fact that DWT based definitions are more accurate, and that they can be used successfully for non-stationary voltage and current waveforms.

The values of FFT-based indices are very close to the values of DWT-based indices, which demonstrates the efficiency of the latter method.

Comparing the outcomes corresponding to the three rectifier topologies, it can be seen that for RNSIC-1, the current harmonic components percentage is smaller than the other two (RNSIC-2 and RNSIC-3), which means that the current waveform is sinusoidal.

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