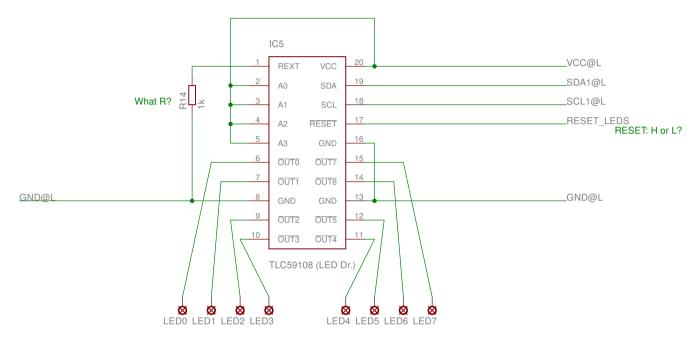
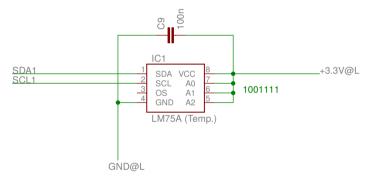
## Address 100 A3 A2 A1 A0 (RW) here 1001111

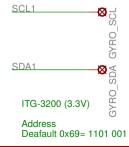


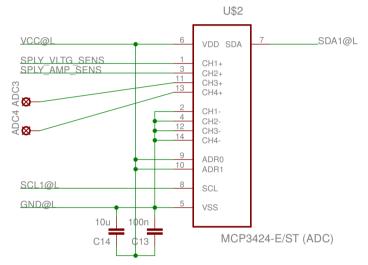
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pull-up resistors for I2C already in the RPI

The four most significant bits of the slave address are hard wired inside the LM75 and are "1001". The three least significant bits of the address are assigned to pins A2 –A0, and are set by connecting these pins to ground for a low, (0); or to aVS for a high, (1).

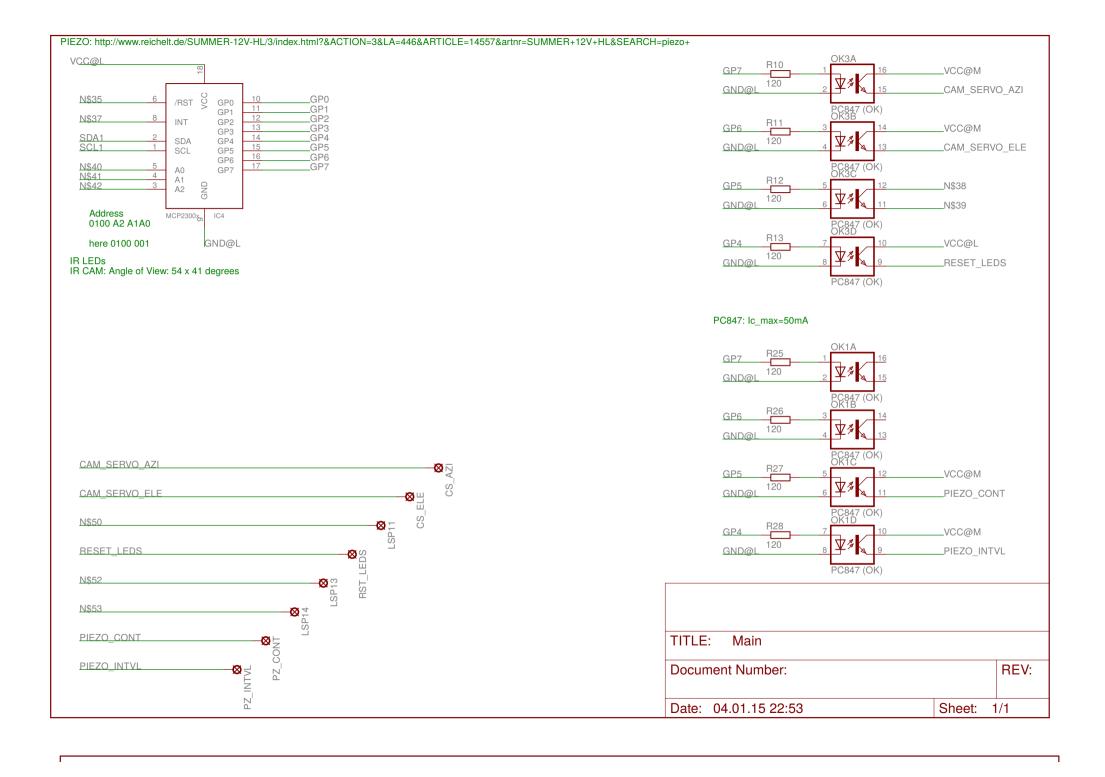






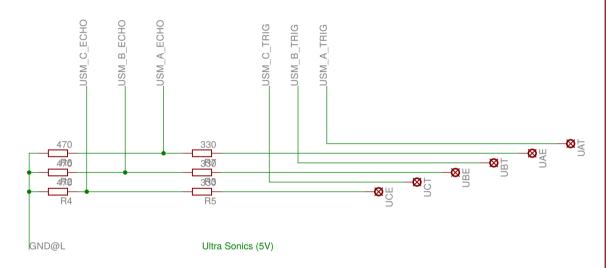
Address 1101 A2 A1 A0 used with floting/open, H and L here: 1101 110 (ADDR0=ADDR1=H)

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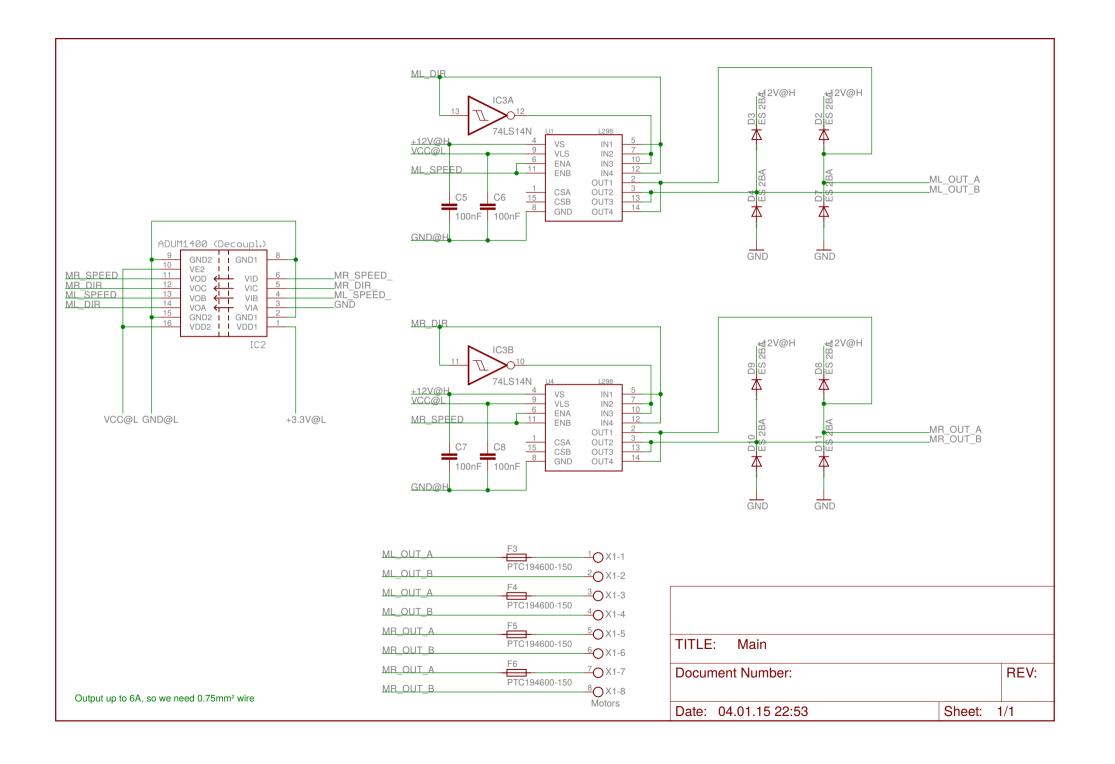
PIRs (5V)

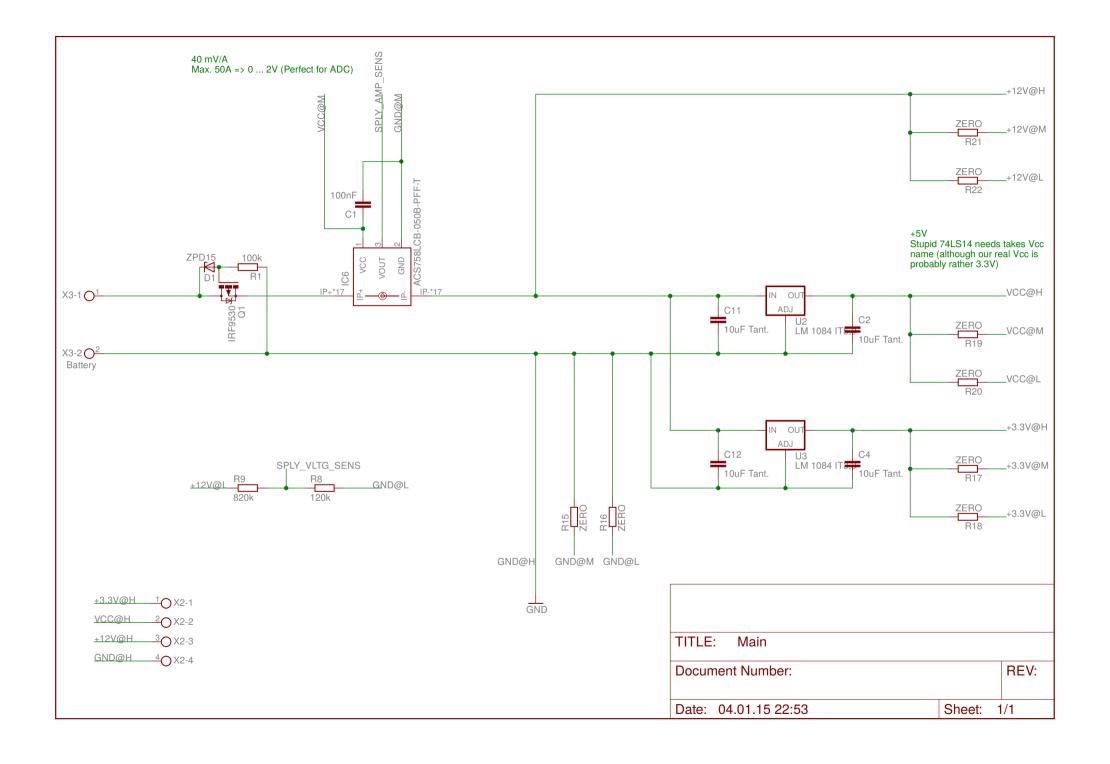


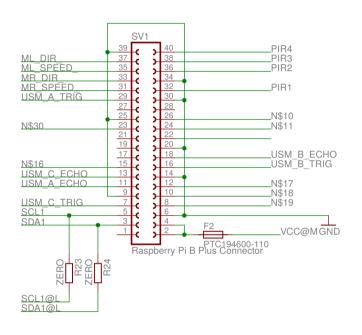
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Date: 04	4.01.15 22:53	Sheet: 1	/1