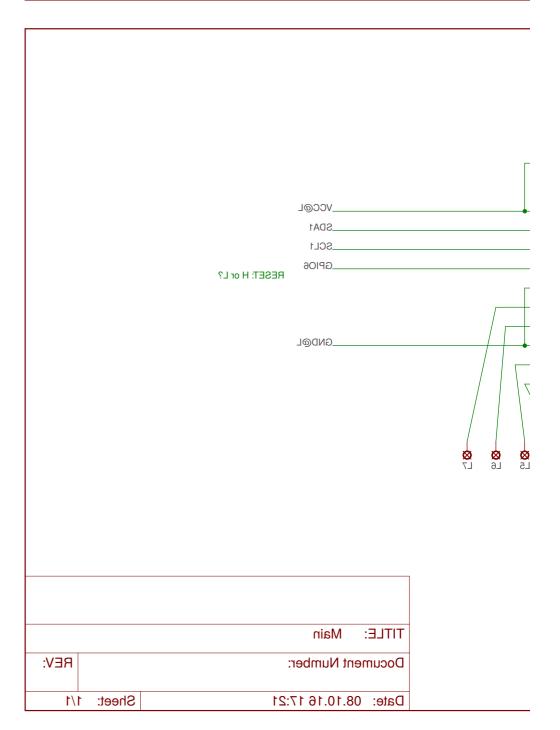
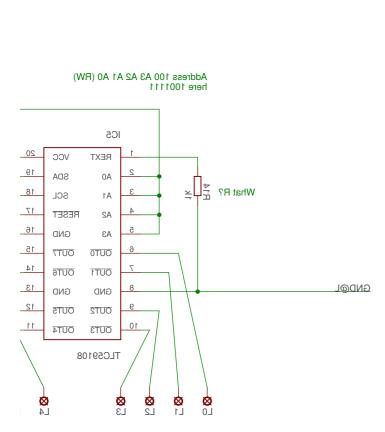
		Main	TITLE:
REV:		nent Number:	Docum
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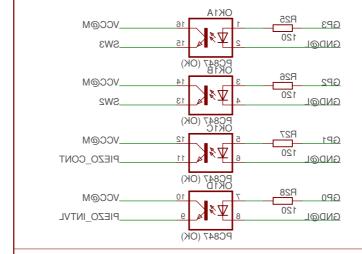
ITG-3200 (3.3V)

Address Deafault 0x69= 1101 001



LED Driver for 8 LEDs

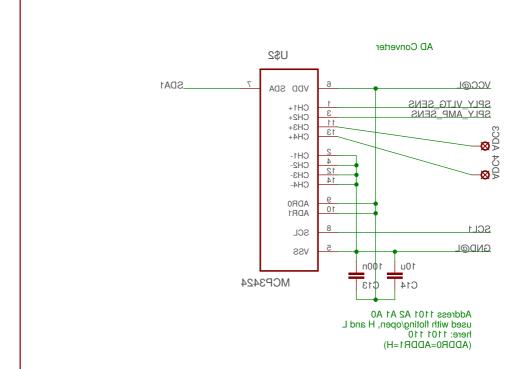




### TITLE: Main

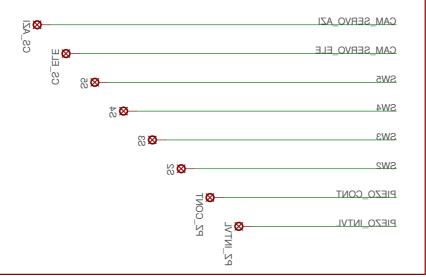
Document Number: REV:

Date: 08.10.16 17:21 Sheet: 1/1



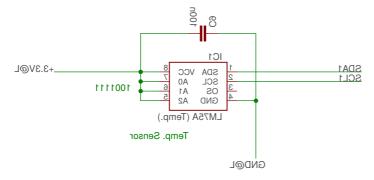
# IR CAM: Angle of View: 54 x 41 degrees

#### 8-Bit I/O Expander with Serial Interface



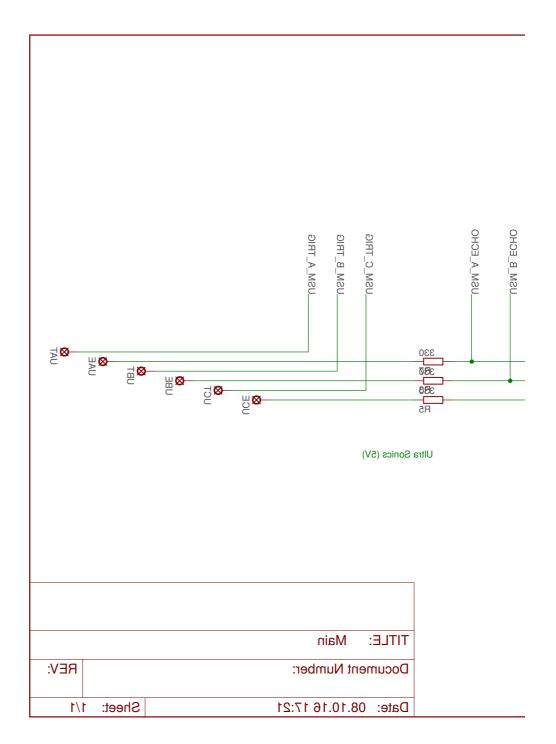
#### pull-up resistors for I2C already in the RPI

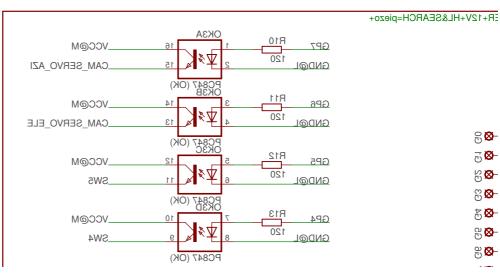
The four most significant bits of the slave address are hard wired inside the LM75 and are "1001". The three least significant bits of the address are assigned to pins A2 -A0, and are set by connecting these pins to ground for a low, (0); or to aVS for a high, (1).





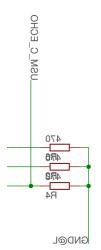
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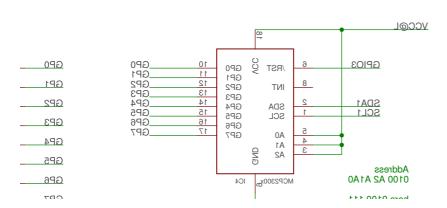


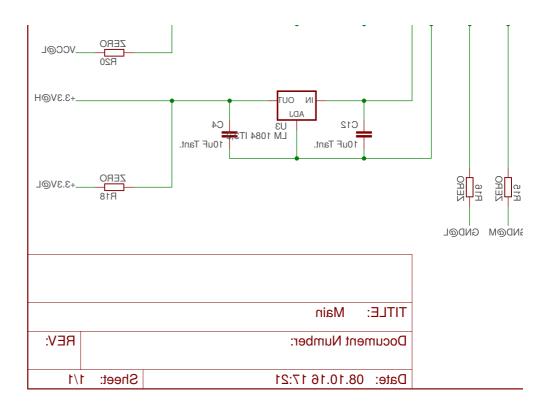


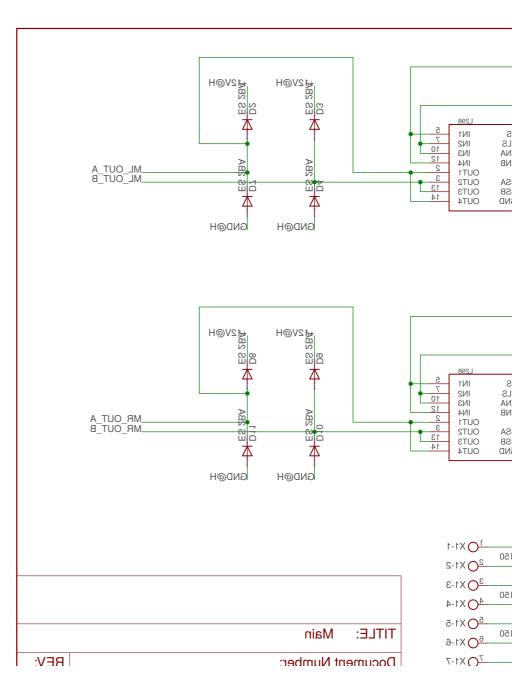
PIRs (5V)

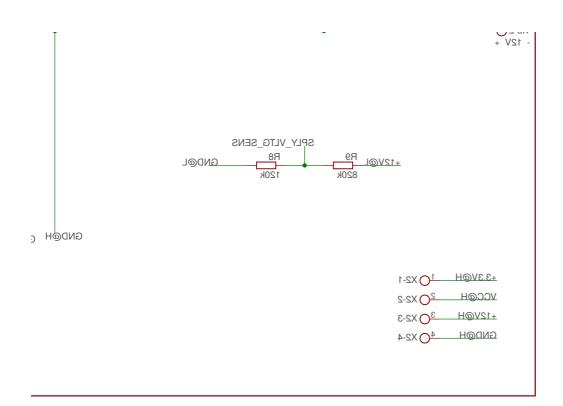


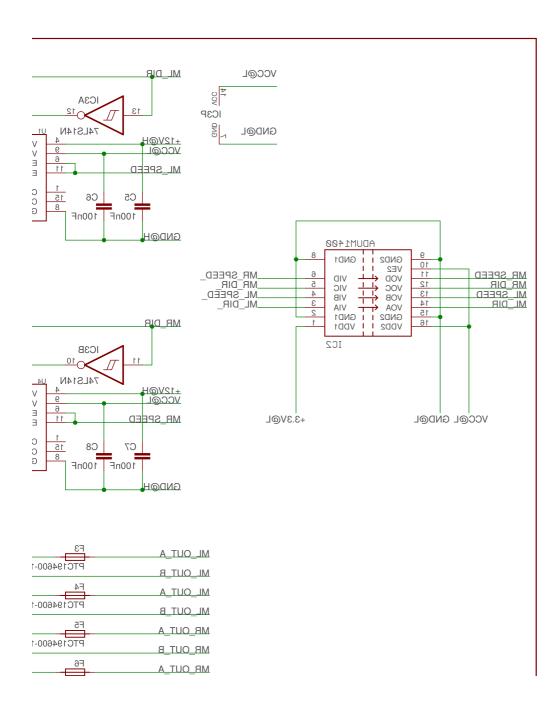
## PIEZO: http://www.reichelt.de/SUMMER-12V-HL/3/index.html?&ACTION=3&LA=446&ARTICLE=14557&artnr=SUMME











						PIR4 PIR3 PIR2 PIR1	
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