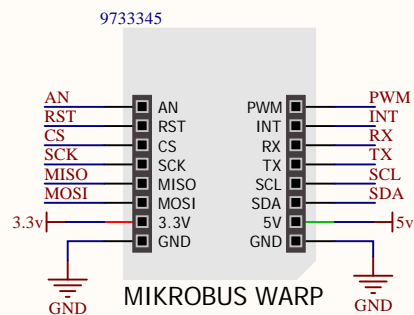
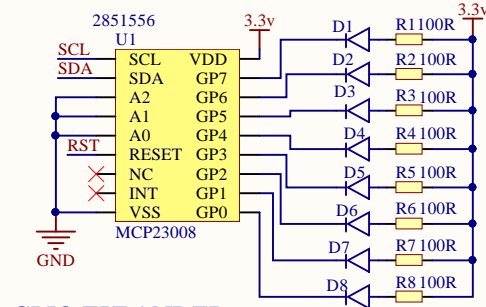


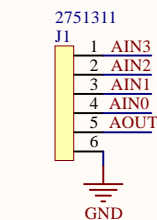
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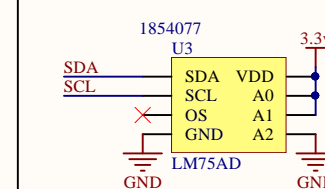
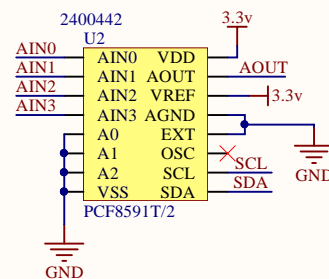
MIKROBUS WARP  
WaRP7 CONNECTOR



GPIO EXPANDER  
Address : 0x20

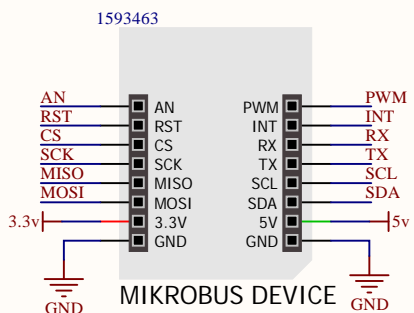


ADC/DAC  
Address : 0x48

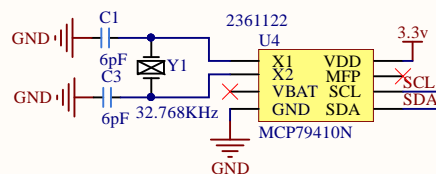


TEMPERATE SENSOR  
Address : 0x4B

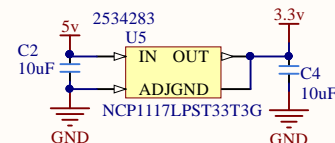
B



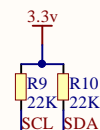
MIKROBUS DEVICE



RTC  
Address : 0x6F

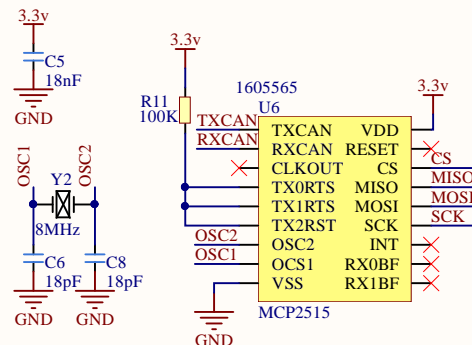


REGULATOR 3.3 V

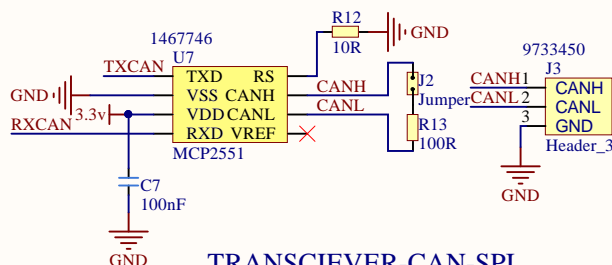


PULL UP IC

C



TRANSCIEVER-CAN-SPI



D

Title **EXPANSION BOARD WARP7**

Size: **A4**

Number: **1**

Revision: **2.0**

Design by  
OFFOUGA Joris  
MICLO Alois



Date: 22/02/2019 Time: 18:10:30

Sheet 1 of 1

File: C:\Users\alois\OneDrive\Documents\GitHub\Expansion-Board-WaRP7\Sheet1.SchDoc

1

2

3

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