

Title **EXPANSION BOARD WARP7**

Size: **A4**

Number: **1**

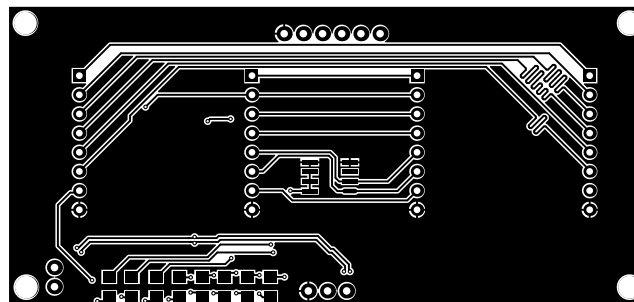
Revision: **2.2.0**

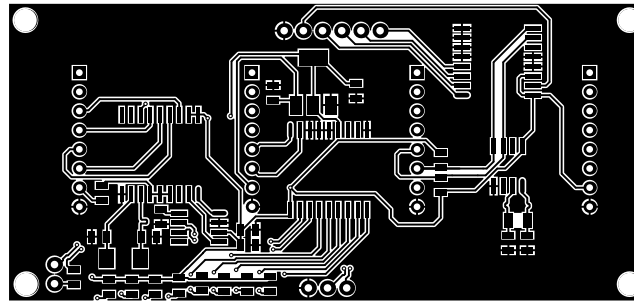
Design by
OFFOUGA Joris
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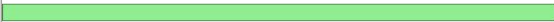







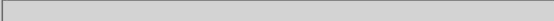







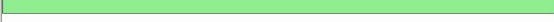
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File: C:\Users\alois\OneDrive\Documents\GitHub\Expansion-Board-WaRP7\Sheet1.SchDoc





Board Stack Report

Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3	  	Top Solder	Solder Resist	0,010mm	3,5
4	  	Top Layer	Copper	0,036mm	
5		Dielectric 1	FR-4	0,320mm	4,8
6	  	Bottom Layer	Copper	0,036mm	
7	  	Bottom Solder	Solder Resist	0,010mm	3,5
8		Bottom Overlay			
9		Bottom Paste			
	Height : 0,411mm				

