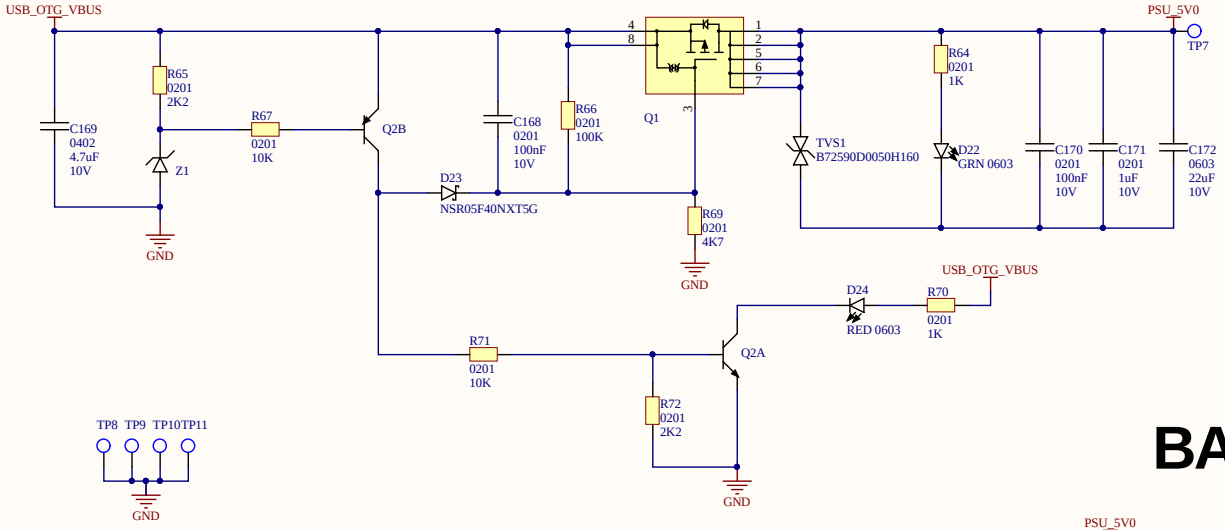
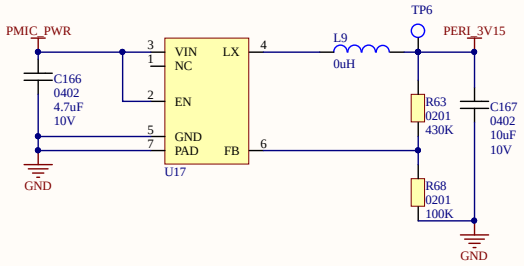
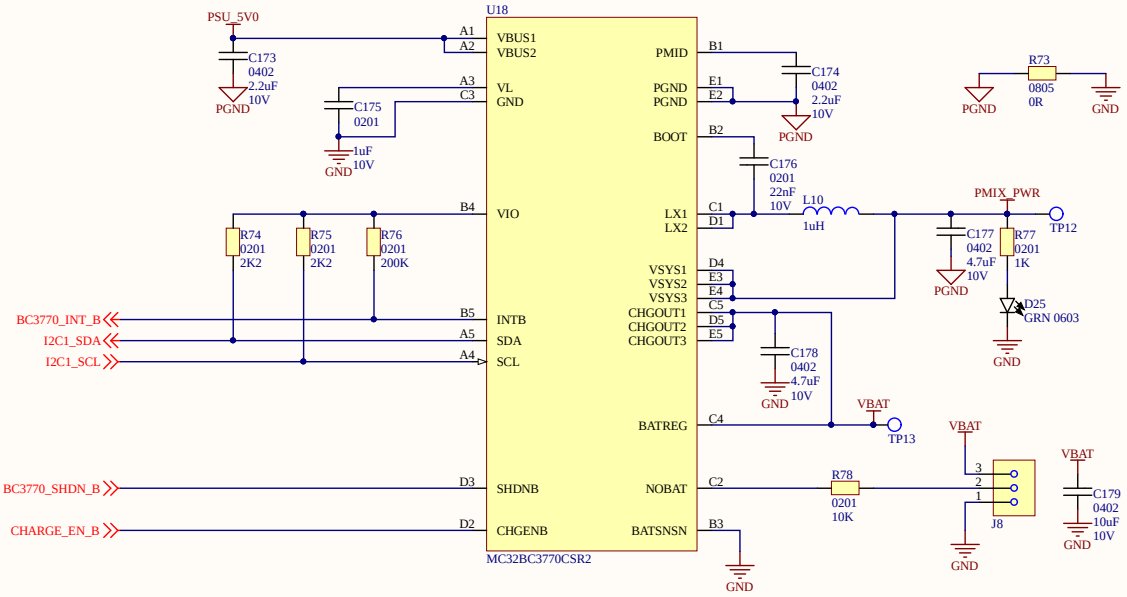
OVER VOLTAGE INDICATOR



PERI 3V15 (1A)

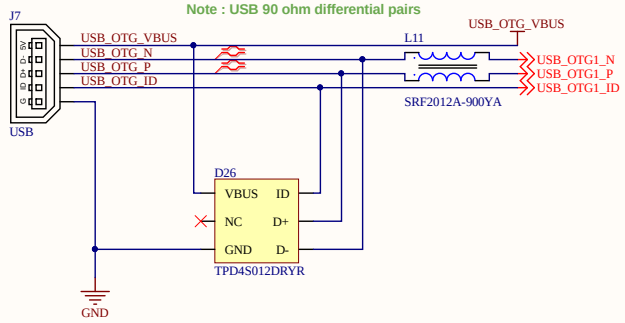


BATTERY CHARGE



Note: I2C slaver address : 92h for write , 93h for read.

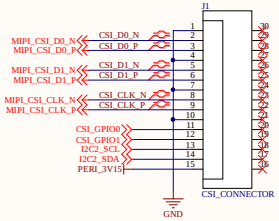
USB OTG



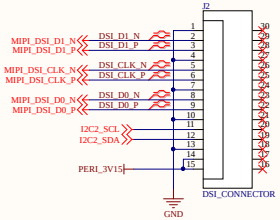
Title	*IMX7S_Design*		
Size: A3	Number:*	Revision:1.0	
Date: 14/08/2019	Time: 02:28:17	Sheet 1 of 8	
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_POWER.SchDoc			

Altium

CSI

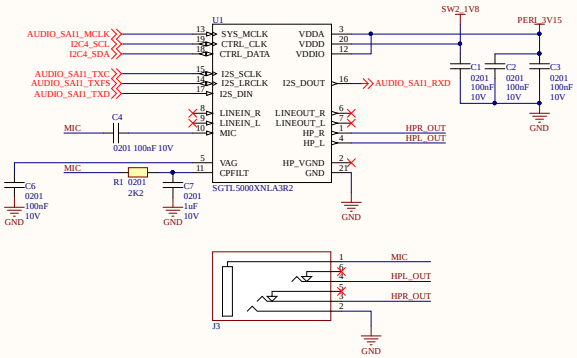


DSI

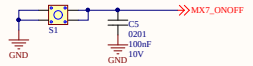


Note : DSI & CSI connectors are Raspberry device compatible

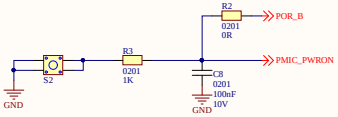
Audio



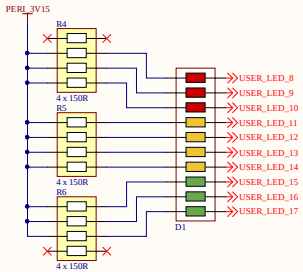
Power Botton



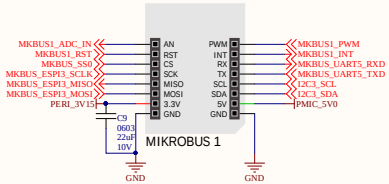
PMIC Power Botton

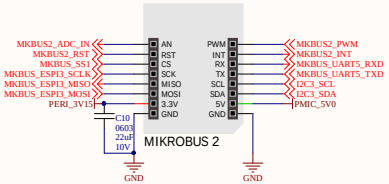


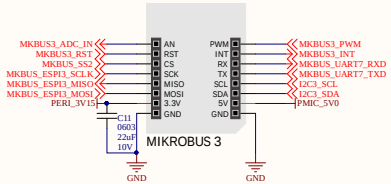
Bar Graph

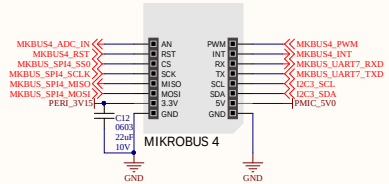


MIKROBUS



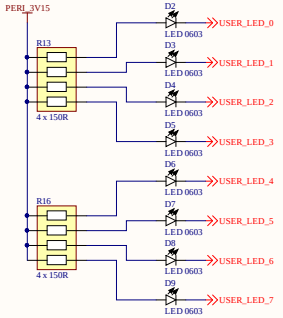




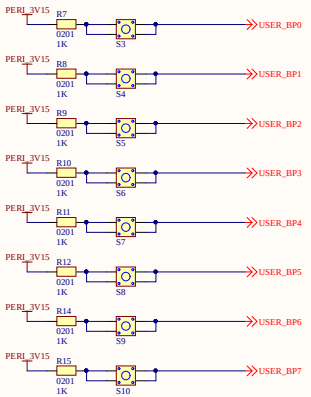


Note : SPI4 ON MKBUS 4, manual control of SS on MKBUS4

User LEDs



User Botton



Title *			
Size: A2	Number:*	Revision:*	
Date: 14/08/2019	Time: 02:28:17	Sheet * of *	
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_CONNECTORS_&_IHM.SchDoc			

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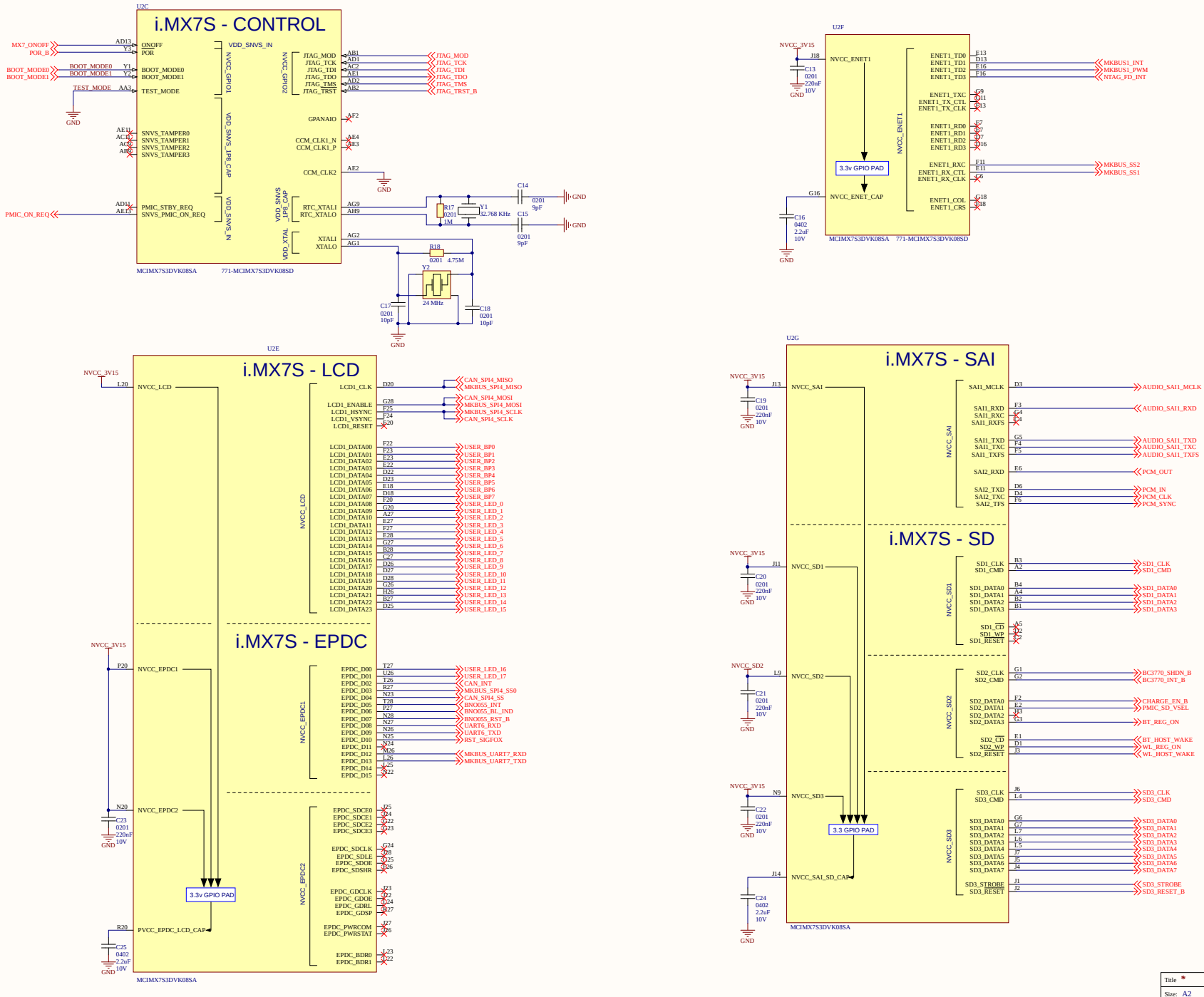
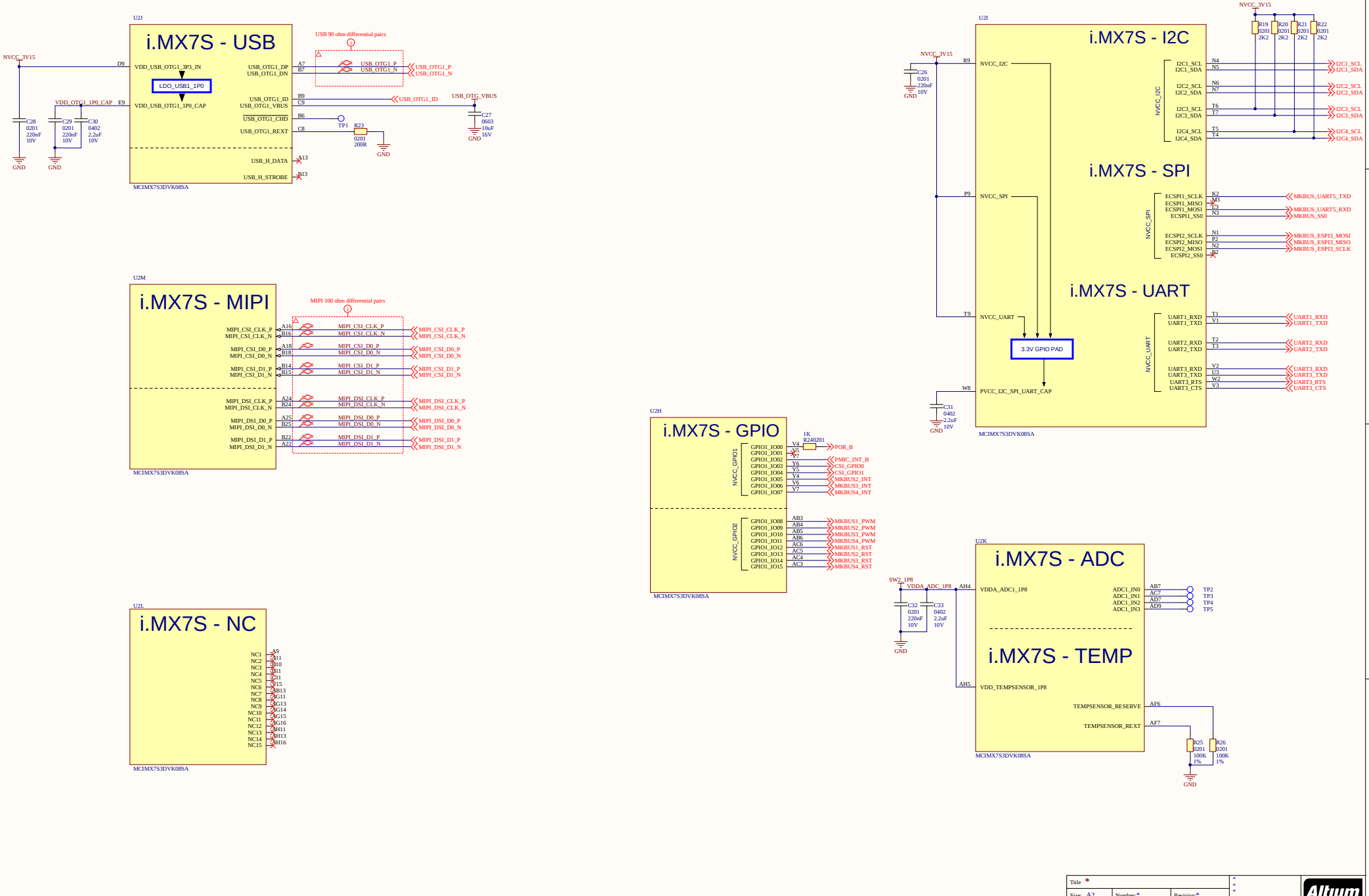
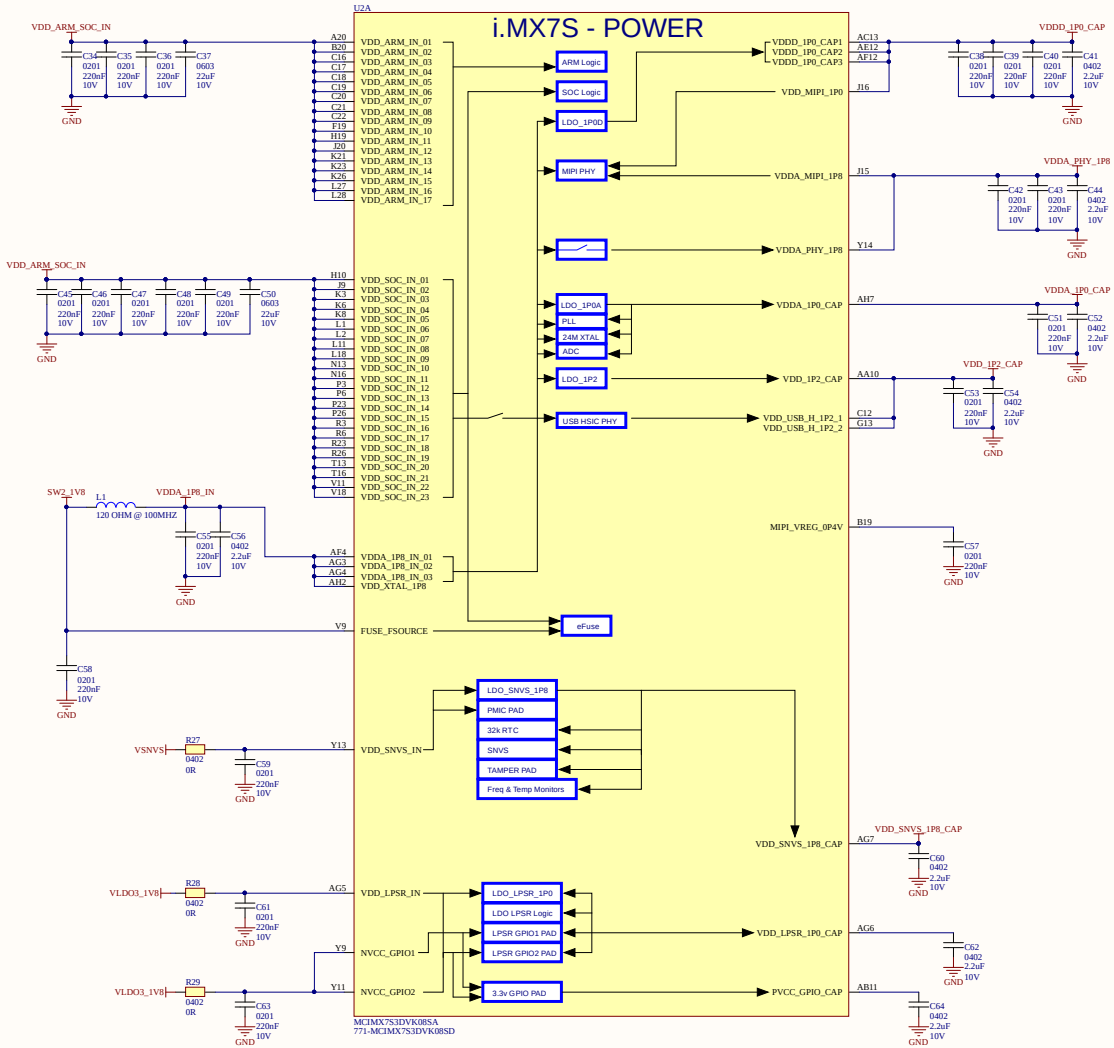
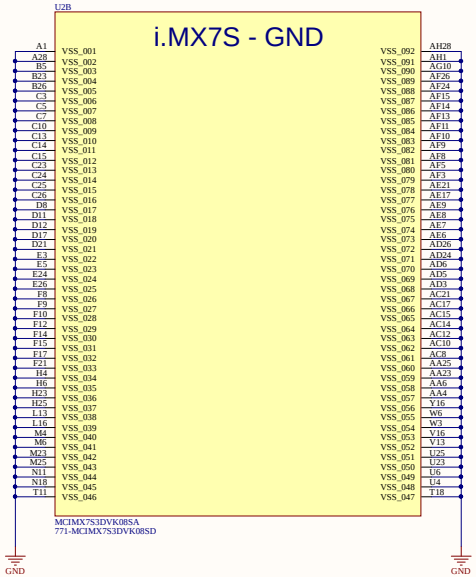

U2C
i.MX7S - CONTROL
MX7_ONOFF
POR_B
ONOFF
POR
BOOT_MODE0
BOOT_MODE1
TEST_MODE
SNVS_TAMPER0
SNVS_TAMPER1
SNVS_TAMPER2
SNVS_TAMPER3
PMIC_STBY_REQ
PMIC_ON_REQ
SNVS_PMIC_ON_REQ
VDD_SNVS_IN
JTAG_MOD
JTAG_TCK
JTAG_TDI
JTAG_TDO
JTAG_TMS
JTAG_TRST
GPANAIO
CCM_CLK1_N
CCM_CLK1_P
CCM_CLK2
RTC_XTALI
RTC_XTALO
XTALI
XTALO
MCIMX7S3DVK08SA
771-MCIMX7S3DVK08SD
U2F
NVCC_ENET1
NVCC_ENET_CAP
3.3v GPIO PAD
ENET1_TD0
ENET1_TD1
ENET1_TD2
ENET1_TD3
ENET1_TX_CTL
ENET1_TX_CLK
ENET1_RD0
ENET1_RD1
ENET1_RD2
ENET1_RD3
ENET1_RX_CTL
ENET1_RX_CLK
ENET1_COL
ENET1_CRS
MKBUS1_INT
MKBUS1_PWM
NTAG_FD_INT
MKBUS_SS2
MKBUS_SS1
U2E
i.MX7S - LCD
i.MX7S - EPDC
NVCC_LCD
LCD1_CLK
LCD1_ENABLE
LCD1_HSYNC
LCD1_VSYNC
LCD1_RESET
LCD1_DATA00
LCD1_DATA01
LCD1_DATA02
LCD1_DATA03
LCD1_DATA04
LCD1_DATA05
LCD1_DATA06
LCD1_DATA07
LCD1_DATA08
LCD1_DATA09
LCD1_DATA10
LCD1_DATA11
LCD1_DATA12
LCD1_DATA13
LCD1_DATA14
LCD1_DATA15
LCD1_DATA16
LCD1_DATA17
LCD1_DATA18
LCD1_DATA19
LCD1_DATA20
LCD1_DATA21
LCD1_DATA22
LCD1_DATA23
CAN_SPI4_MISO
MKBUS_SPI4_MISO
CAN_SPI4_MOSI
MKBUS_SPI4_MOSI
MKBUS_SPI4_SCLK
CAN_SPI4_SCLK
USER_BP0
USER_BP1
USER_BP2
USER_BP3
USER_BP4
USER_BP5
USER_BP6
USER_BP7
USER_LED_0
USER_LED_1
USER_LED_2
USER_LED_3
USER_LED_4
USER_LED_5
USER_LED_6
USER_LED_7
USER_LED_8
USER_LED_9
USER_LED_10
USER_LED_11
USER_LED_12
USER_LED_13
USER_LED_14
USER_LED_15
NVCC_EPDC1
NVCC_EPDC2
PVCC_EPDC_LCD_CAP
3.3v GPIO PAD
EPDC_D00
EPDC_D01
EPDC_D02
EPDC_D03
EPDC_D04
EPDC_D05
EPDC_D06
EPDC_D07
EPDC_D08
EPDC_D09
EPDC_D10
EPDC_D11
EPDC_D12
EPDC_D13
EPDC_D14
EPDC_D15
USER_LED_16
USER_LED_17
CAN_INT
MKBUS_SPI4_SS0
CAN_SPI4_SS
BNO055_INT
BNO055_BL_IND
BNO055_RST_B
UART6_RXD
UART6_TXD
RST_SIGFOX
MKBUS_UART7_RXD
MKBUS_UART7_TXD
EPDC_SDCE0
EPDC_SDCE1
EPDC_SDCE2
EPDC_SDCE3
EPDC_SDCLK
EPDC_SDLE
EPDC_SDOE
EPDC_SDSHR
EPDC_GDCLK
EPDC_GDOE
EPDC_GDRL
EPDC_GDSP
EPDC_PWRCOM
EPDC_PWRSTAT
EPDC_BDR0
EPDC_BDR1
MCIMX7S3DVK08SA
U2G
i.MX7S - SAI
i.MX7S - SD
NVCC_SAI
NVCC_SD1
NVCC_SD2
NVCC_SD3
NVCC_SAI_SD_CAP
3.3 GPIO PAD
SAI1_MCLK
SAI1_RXD
SAI1_RXC
SAI1_RXFS
SAI1_TXD
SAI1_TXC
SAI1_TXFS
SAI2_RXD
SAI2_TXD
SAI2_TXC
SAI2_TFS
AUDIO_SAI1_MCLK
AUDIO_SAI1_RXD
AUDIO_SAI1_TXD
AUDIO_SAI1_TXC
AUDIO_SAI1_TXFS
PCM_OUT
PCM_IN
PCM_CLK
PCM_SYNC
SD1_CLK
SD1_CMD
SD1_DATA0
SD1_DATA1
SD1_DATA2
SD1_DATA3
SD1_CD
SD1_WP
SD1_RESET
SD2_CLK
SD2_CMD
SD2_DATA0
SD2_DATA1
SD2_DATA2
SD2_DATA3
SD2_CD
SD2_WP
SD2_RESET
BC3770_SHDN_B
BC3770_INT_B
CHARGE_EN_B
PMIC_SD_VSEL
BT_REG_ON
BT_HOST_WAKE
WL_REG_ON
WL_HOST_WAKE
SD3_CLK
SD3_CMD
SD3_DATA0
SD3_DATA1
SD3_DATA2
SD3_DATA3
SD3_DATA4
SD3_DATA5
SD3_DATA6
SD3_DATA7
SD3_STROBE
SD3_RESET
SD3_RESET_B
Title *
Size: A2
Number:*
Revision:*
Date: 14/08/2019
Time: 02:28:17
Sheet * of *
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_CPU_INTERFACES_1.SchDoc
Altium


U2J
i.MX7S - USB
USB 90 ohm differential pairs
NVCC_3V15
VDD_USB_OTG1_3P3_IN
LDO_USB1_1P0
VDD_USB_OTG1_1P0_CAP
USB_OTG1_DP
USB_OTG1_DN
USB_OTG1_P
USB_OTG1_N
USB_OTG1_ID
USB_OTG1_VBUS
USB_OTG_VBUS
USB_OTG1_CHD
USB_OTG1_REXT
USB_H_DATA
USB_H_STROBE
MCIMX7S3DVK08SA
U2M
i.MX7S - MIPI
MIPI 100 ohm differential pairs
MIPI_CSI_CLK_P
MIPI_CSI_CLK_N
MIPI_CSI_D0_P
MIPI_CSI_D0_N
MIPI_CSI_D1_P
MIPI_CSI_D1_N
MIPI_DSI_CLK_P
MIPI_DSI_CLK_N
MIPI_DSI_D0_P
MIPI_DSI_D0_N
MIPI_DSI_D1_P
MIPI_DSI_D1_N
MCIMX7S3DVK08SA
U2L
i.MX7S - NC
NC1
NC2
NC3
NC4
NC5
NC6
NC7
NC8
NC9
NC10
NC11
NC12
NC13
NC14
NC15
MCIMX7S3DVK08SA
U2H
i.MX7S - GPIO
NVCC_GPIO1
NVCC_GPIO2
GPIO1_IO00
GPIO1_IO01
GPIO1_IO02
GPIO1_IO03
GPIO1_IO04
GPIO1_IO05
GPIO1_IO06
GPIO1_IO07
GPIO1_IO08
GPIO1_IO09
GPIO1_IO10
GPIO1_IO11
GPIO1_IO12
GPIO1_IO13
GPIO1_IO14
GPIO1_IO15
POR_B
PMIC_INT_B
CSI_GPIO0
CSI_GPIO1
MKBUS2_INT
MKBUS3_INT
MKBUS4_INT
MKBUS1_PWM
MKBUS2_PWM
MKBUS3_PWM
MKBUS4_PWM
MKBUS1_RST
MKBUS2_RST
MKBUS3_RST
MKBUS4_RST
MCIMX7S3DVK08SA
U2I
i.MX7S - I2C
i.MX7S - SPI
i.MX7S - UART
NVCC_I2C
NVCC_SPI
NVCC_UART
3.3V GPIO PAD
PVCC_I2C_SPI_UART_CAP
I2C1_SCL
I2C1_SDA
I2C2_SCL
I2C2_SDA
I2C3_SCL
I2C3_SDA
I2C4_SCL
I2C4_SDA
ECSPI1_SCLK
ECSPI1_MISO
ECSPI1_MOSI
ECSPI1_SS0
ECSPI2_SCLK
ECSPI2_MISO
ECSPI2_MOSI
ECSPI2_SS0
MKBUS_UART5_TXD
MKBUS_UART5_RXD
MKBUS_SS0
MKBUS_ESPI3_MOSI
MKBUS_ESPI3_MISO
MKBUS_ESPI3_SCLK
UART1_RXD
UART1_TXD
UART2_RXD
UART2_TXD
UART3_RXD
UART3_TXD
UART3_RTS
UART3_CTS
MCIMX7S3DVK08SA
U2K
i.MX7S - ADC
i.MX7S - TEMP
VDDA_ADC_1P8
VDD_TEMPSENSOR_1P8
ADC1_IN0
ADC1_IN1
ADC1_IN2
ADC1_IN3
TEMPSENSOR_RESERVE
TEMPSENSOR_REXT
MCIMX7S3DVK08SA
Title *
Size: A2
Number:*
Revision:*
Date: 14/08/2019
Time: 02:28:18
Sheet * of *
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_CPU_INTERFACES_2.SchDoc
Altium

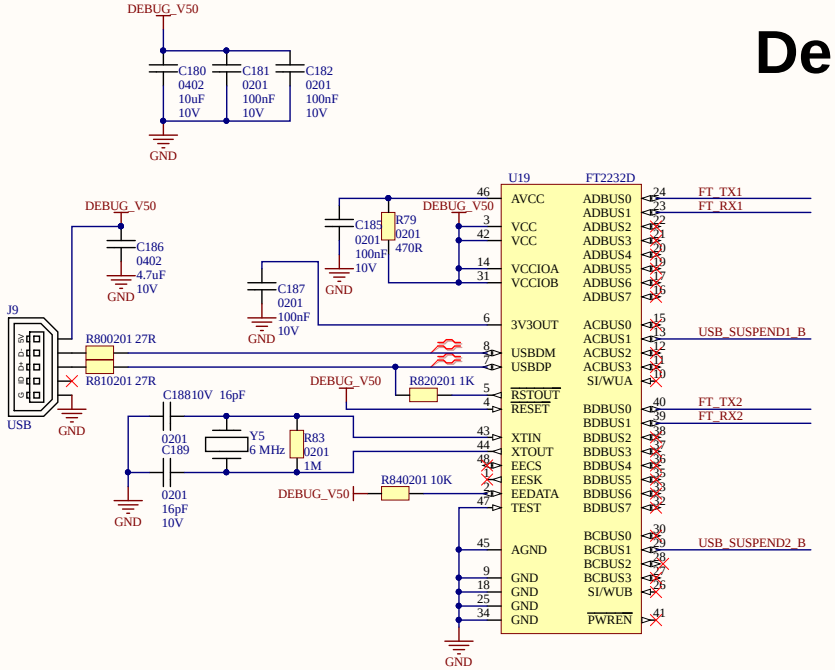
CPU_POWER

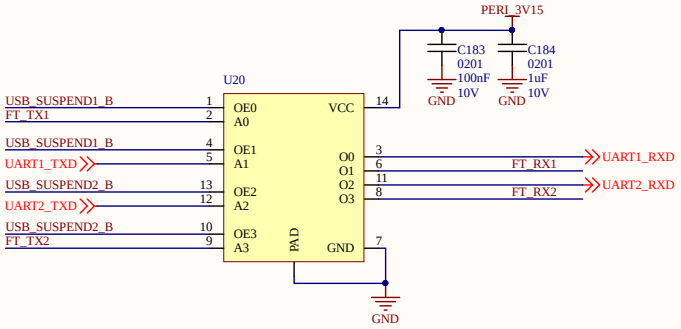




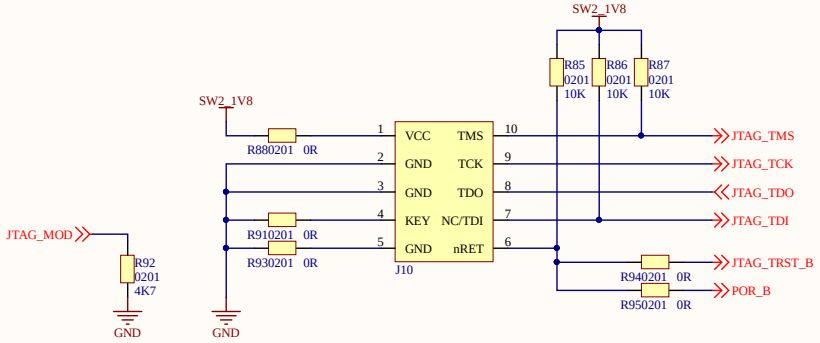
Title *			
Size: A2	Number:*	Revision:*	
Date: 14/08/2019	Time: 02:28:18	Sheet * of *	
File: C:\Users\aloisi\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_CPU_POWER.SchDoc			

Debug UART



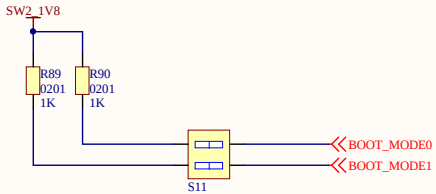


JTAG



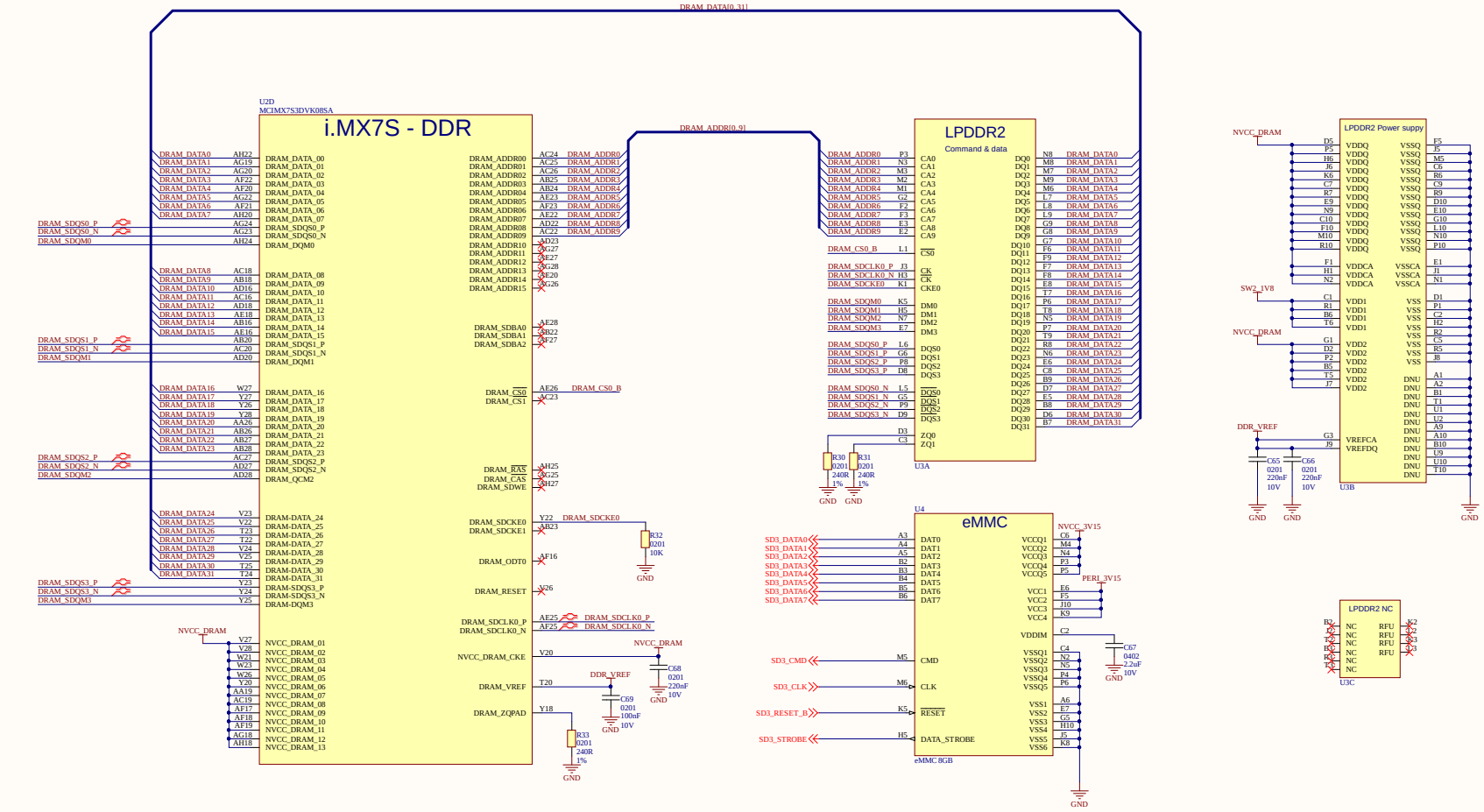
BOOT MODE

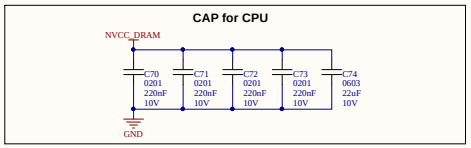
BOOT_MODE	1	0
FUSES	0	0
MANUFACTURE	0	1
INTERNAL BOOT	1	0
TEST MODE	1	1

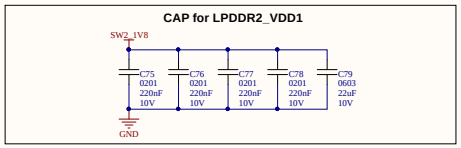


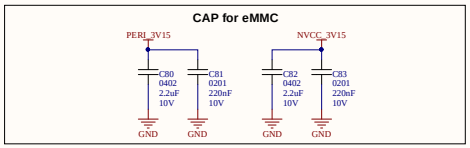
Title *			*
Size: A3	Number:*	Revision:*	*
Date: 14/08/2019	Time: 02:28:18	Sheet * of *	*
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_DEBUG.SchDoc			

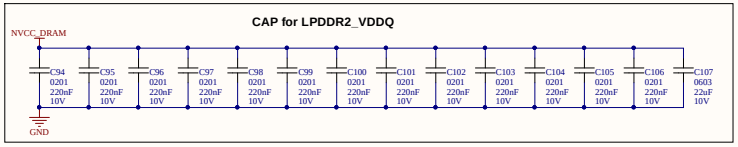
Altium

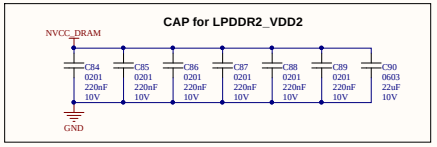


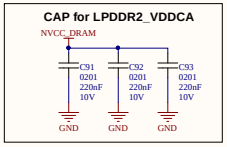






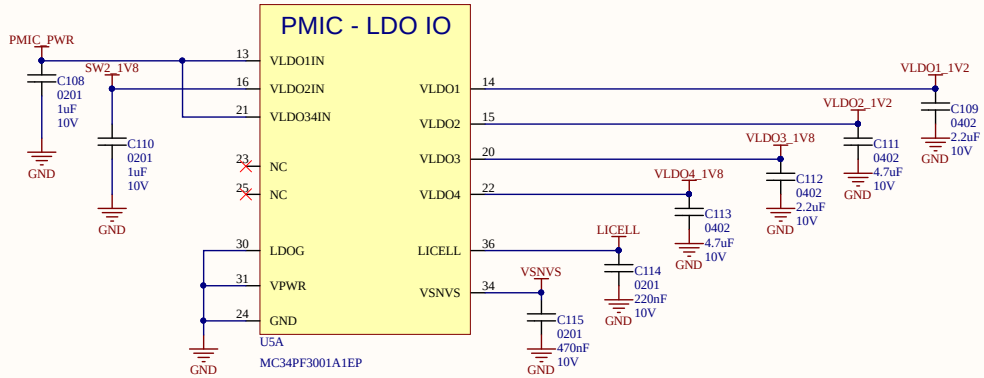


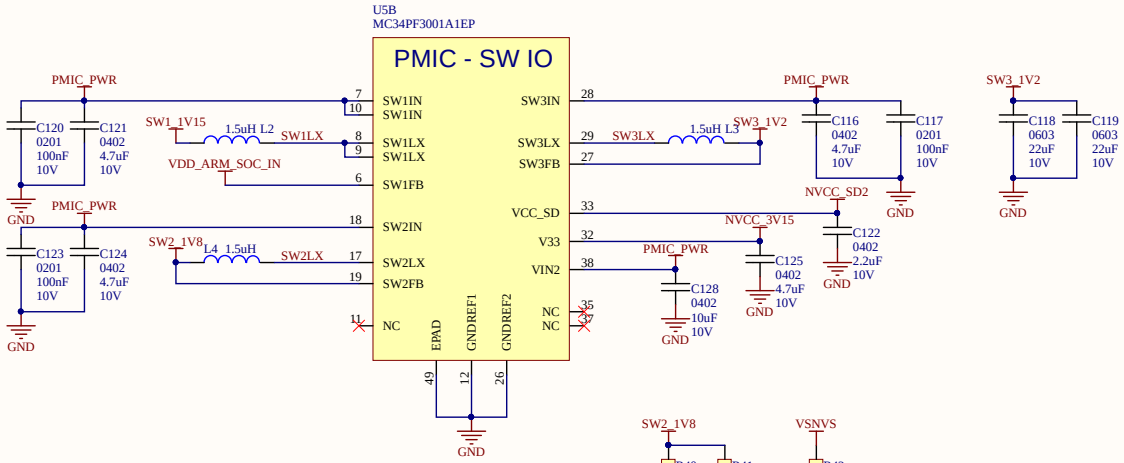


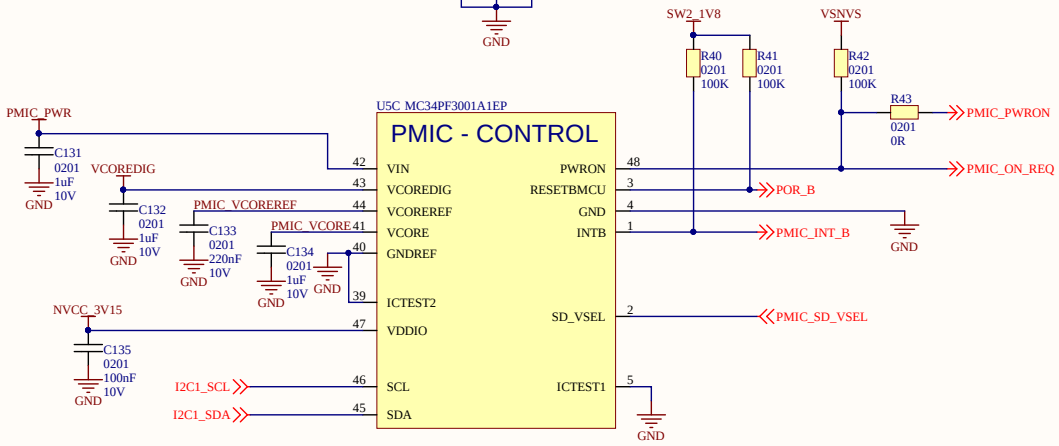


Title *			
Size: A2	Number:*	Revision:*	
Date: 14/08/2019	Time: 02:28:18	Sheet * of *	
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_MEMORY.SchDoc			

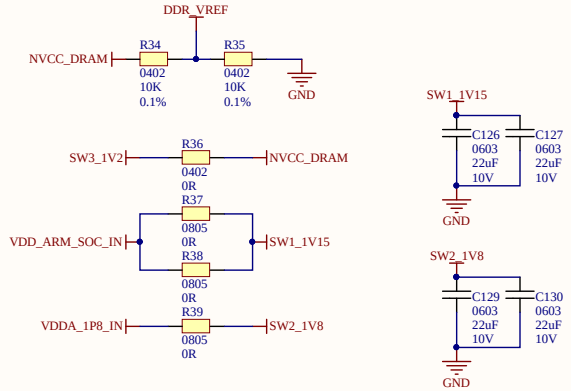
PMIC





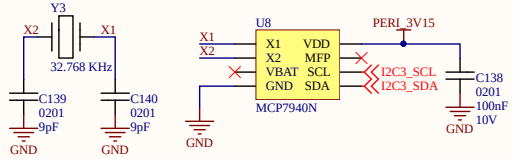


Note : I2C slaver address : 08h (exceptions to the standard are noted to be 7-bit only addressing)

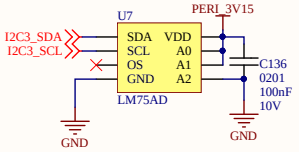


Title *			
Size: A3	Number:*	Revision:*	
Date: 14/08/2019	Time: 02:28:19	Sheet * of *	
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_PMIC.SchDoc			

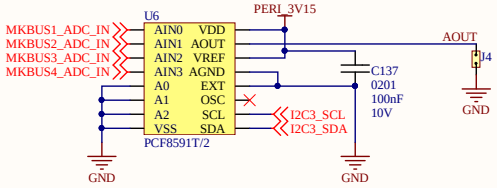
RTC



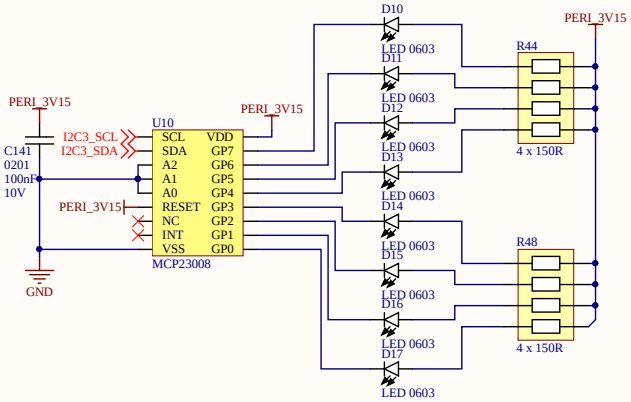
Temp. Sensor



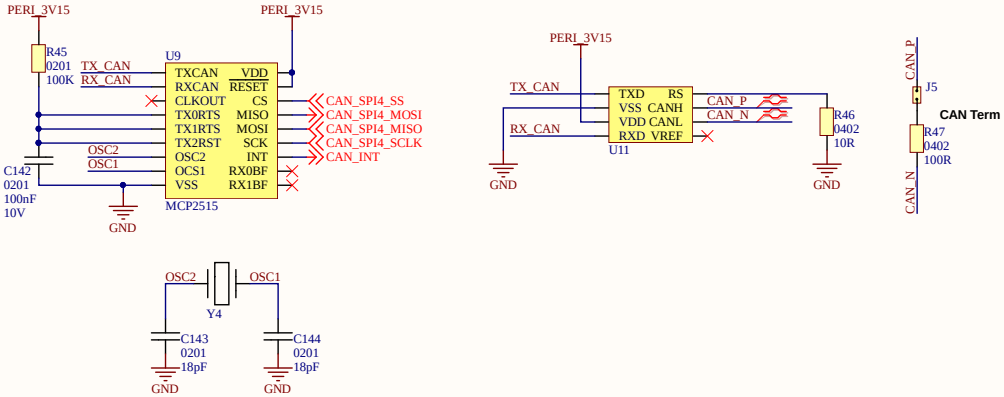
ADC/DAC



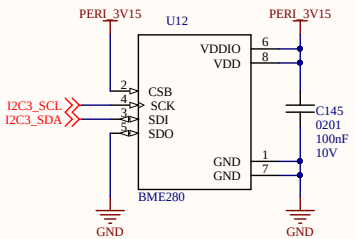
GPIO Expander



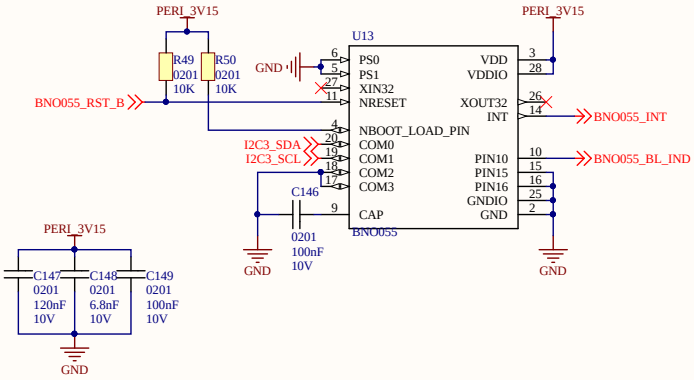
SPI / CAN



BME280

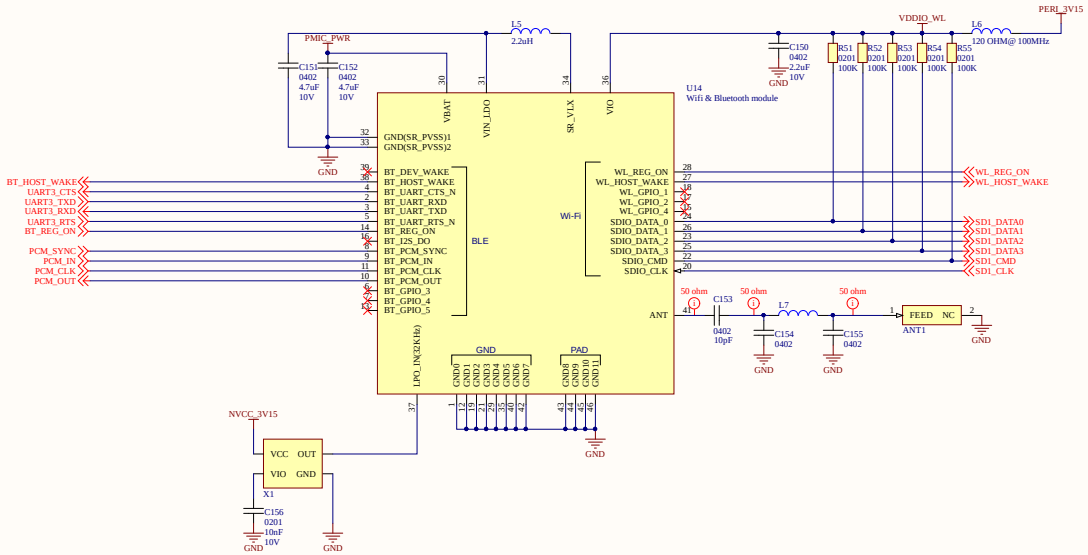


BNO055

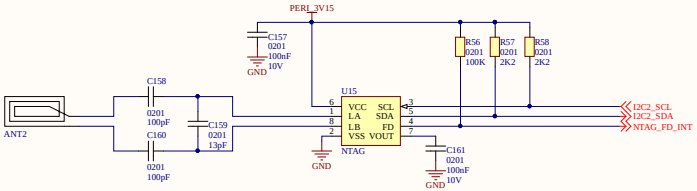


Title *			
Size: A3	Number:*	Revision:*	
Date: 14/08/2019	Time: 02:28:19	Sheet * of *	
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_SENSORS_&_EXTERNAL_INTERFACES.SchDoc			

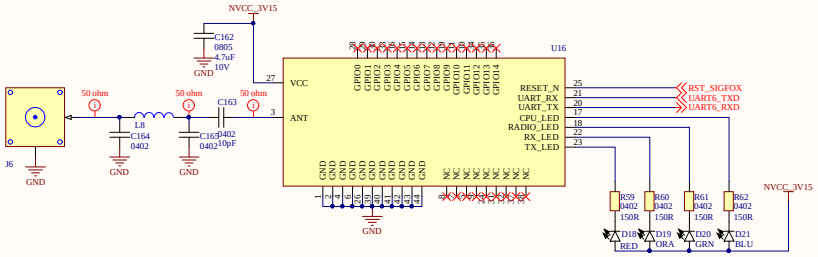
WiFi & Bluetooth



NTAG_I2C



SigFox



Title *			
Size: A2	Number:*	Revision:*	
Date: 14/08/2019	Time: 02:28:19	Sheet * of *	
File: C:\Users\alois\OneDrive\Documents\GitHub\IMX7S_Design\IMX7S_WIRELESS.SchDoc			

Altium