

Ajit Kumar Pradhan

B.Tech. | NIT Rourkela

Final Year, Electronics communication & Engg.
DOB: 21/09/2002
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Education

2021-2025
B.TECH., ECE
NIT Rourkela
CGPA : 8.68/10

MAY 2020
INTERMEDIATE
Boudh Panchayat Higher Secondary
School,Boudh
Percentage: 86%

MAY 2018
MATRICULATION
Kiakata High School,Kiakata
Percentage: 89%

Links

Linkdin:// Ajit kumar
Github:// Ajit Kumar

Skills

GENERAL PROGRAMMING
C programming
HARDWARE PROGRAMMING
Verilog hdl
SOFTWARES
Vscode,Xilling Vivado,keil uvision,proteus
LANGUAGES
English, Hindi, Odia

Relevant Courses

Analog Electronics
Digital Elctronics
Network Analysis
Computer Architecture and Organisation

Work Experience/Projects

2024 Research Intern

- Developed a 64-bit RISC-V single-cycle, non-pipelined processor in Verilog using AMD Xilinx Vivado, incorporating key components such as an Arithmetic Logic Unit (ALU), data memory, register file, and control unit for core functionality.
- Integrated and tested the processor design in Vivado, ensuring that it correctly executes RISC-V instructions while maintaining proper communication between all modules.

2024 Vending Machine

- Designed a Verilog-based vending machine system using Xilinx Vivado, targeting the Basys 3 FPGA board for real-time operation.
- Implemented functionality for user input, item selection, and change dispensing, ensuring smooth transaction processing on the hardware.

Achievements/Certifications

APR 2018 Scholarship

Awarded a state government scholarship for achieving the highest scores in Odia and Science across the entire district

Extra Curricular Activities

2022-NOW AASRA CLUB

CLUB

I volunteered at Aasra Club, teaching essential skills to underprivileged individuals. Contributed to community development, fostering personal growth through educational initiatives.

2022 National Service Scheme

NSS

Successfully managed and oversaw a Blood Donation camp within our college, ensuring efficient coordination and seamless execution. Actively participated in a campus clean-liness program.