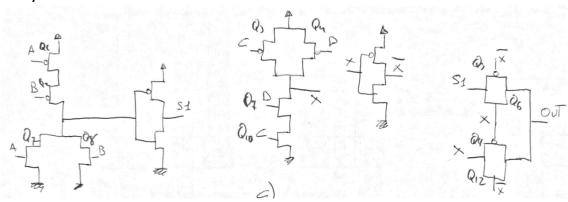
Problema 1

a)

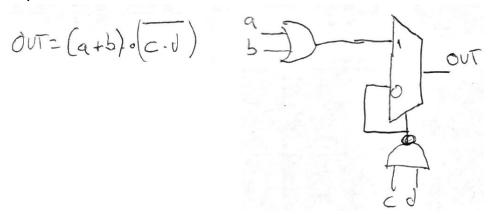


b)

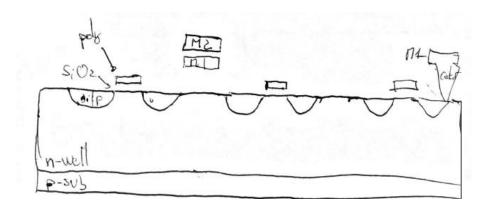
ABCD Q1 Q2 Q3 Q4 Q6 Q4 Q4 Q4 Q6 Q1 Q12

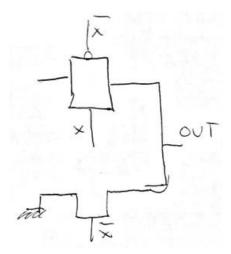
1011 Off On off On off on off on off off on on on on.

c)

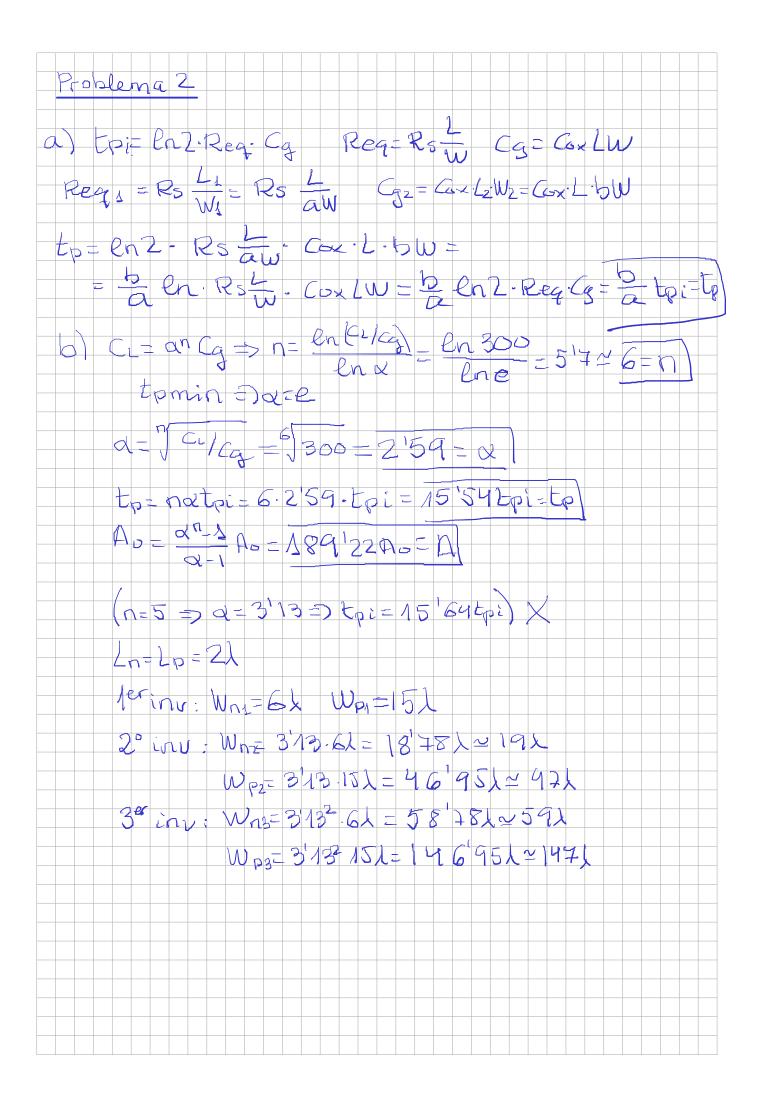


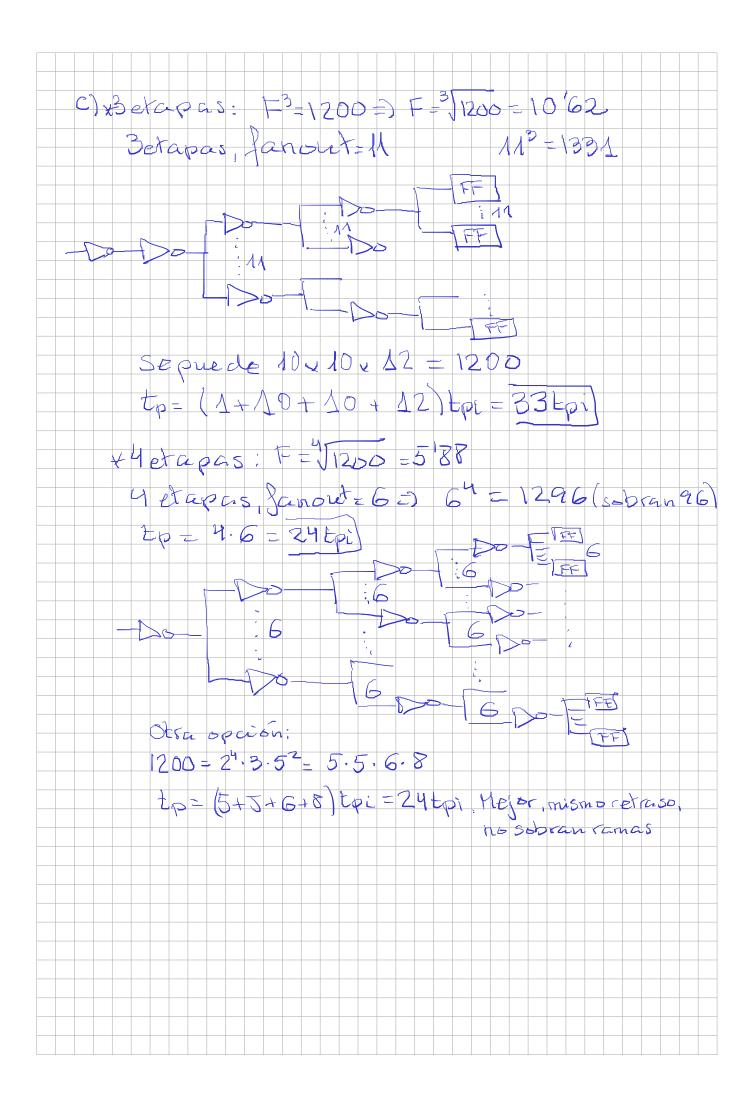
d)





Al tener una de las entradas del multiplexor conectada a un '0' lógico, se podría sustituir por un transistor de paso de tipo N que transmiten los ceros sin degradación.





Problema 3

$$\frac{1}{\sqrt{20}} = \frac{100 \mu A}{20 \mu A} = 5; \left(\frac{W}{L}\right)_{H2} = 5.10 = 50.$$

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$$\frac{1}{\sqrt{20}} = \frac{100 \mu A}{20 \mu A} = 5; \left(\frac{W}{L}\right)_{H2} = \frac{5.10}{20} = \frac{5}{20}$$

$$\frac{1}{\sqrt{20}} = \frac{100 \mu A}{20 \mu A} = \frac{$$

c) less dannel modulation = 1 db = 0 rout.

Pout =
$$g_{m3} ro_3 ro_2$$

 $g_{m3} = \sqrt{2 \cdot \mu_n G_x} \left(\frac{w}{c}\right)_3 I_{03} = \sqrt{2 \cdot 100 \cdot 20 \cdot 100} = 632 \mu^A/V$.
 $ro_3 = \frac{1}{\lambda I_{03}} = \frac{1}{0.6 \cdot 100 \cdot 10^6} = 16.67 \mu \Lambda$.
 $ro_2 = ro_3 = 16.67 \mu \Lambda$.
 $rout = roundard$

e)
$$|Av| = \frac{vout}{vin} = gm_3 \cdot rout$$

 $gm_3 = \sqrt{2\mu\rho G_x} \left(\frac{w}{c}\right) I_{D3} = \sqrt{2.50.20.100} = 447.21 \text{ MA/V}$
 $rout = ros |ros = \frac{16.64}{2} = 8.3kn$.
 $|Av| = 3.74V/V$.