| Serial # | VU7P | VU7P | VU7P x4 | VU7P x12 | VU7P x12 | KU15P | KU15P | KU15P x4 | KU15P x12 | KU15P x12 | Location |
|----------------|-----------|-----------|-------------|-------------|---------------|----------|----------|-------------|-------------|-------------|----------|
| | socketed | soldered | 25G FireFly | 14G FireFly | 14G FireFly | socketed | soldered | 25G FireFly | 14G FireFly | 14G FireFly | |
| | | | | (long) | (short) | | | _ | (long) | (short) | |
| 001 | | 1 | 8 | \ J, | , | | | | | , | PSB |
| 002 | | | | | | 1 | | 3 | 4 | | BU |
| 003 | 1 | | 4 | | 1 | 1 | | 3 | | 1 | PSB |
| 004 | 1 | | 2 | | 1 | 1 | | 2 | | 1 | PSB |
| 005 | | 1 | 10 | 2 | | | 1 | 3 | 4 | | BU |
| 006 | | | | | | | 1 | | | | W231 |
| 007 | | 1 | | | | | 1 | | | | W231 |
| 008 | | 1 | | | | | 1 | | | | W231 |
| 009 | | 1 | | | | | 1 | | | | W231 |
| 010 | | | | | | | 1 | | | | W231 |
| | | | | | | | | | | | |
| Installed | | | - | | | | | | | | |
| Totals | 2 | 5 | 24 | 2 | 2 | 3 | 6 | 11 | 8 | 2 | |
| | | | | | | | | | | | |
| | | | | | | | | | | | |
| | Used with | Used with | | | | | | | | | |
| Inventory | VU7P | KU15P | unused | total | | | | | | | |
| 25G x4 | | | | | | | | | | | |
| FireFly | 24 | 11 | 40 | 75 | | | | | | | |
| | | | | | | | | | | | |
| | | | | | | | | | | | |
| 14G x12 | | | 1 | | 3 pieces are | | | | | | |
| FireFly (long) | 2 | 8 | 5 | 15 | missing | | | | | | |
| | | | | | | | | | | | |
| | | | - | | | | | | | | |
| 14G x12 | | | 1 | | short will | | | | | | |
| FireFly | | | ! | | not work in | | | | | | |
| (short) | 2 | 2 | 3 | 7 | back row | | | | | | |
| , , | | | | | | | | | | | |
| VU7P | 7 | - | 1 | 8 | | | | | | | |
| | | | | | | | | | | | |
| | 1 | 9 | | 9 | 2 are on loan | | | | | | |

| being prepped for eventual use at TIF sent to BU, has empty VU7P socket checking VU7P socket for JTAG chain, KU15P fully checked by Ed mated to SM, fireflys have individual heatsinks sent to BU, KU15P and VU7P soldered, needs LGA80D heatsink ready for FPGA testing and final assembly | Notes |
|---|---|
| sent to BU, has empty VU7P socket checking VU7P socket for JTAG chain, KU15P fully checked by Ed mated to SM, fireflys have individual heatsinks sent to BU, KU15P and VU7P soldered, needs LGA80D heatsink ready for FPGA testing and final assembly | |
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| mated to SM, fireflys have individual heatsinks sent to BU, KU15P and VU7P soldered, needs LGA80D heatsink ready for FPGA testing and final assembly | sent to BU, has empty VU7P socket |
| sent to BU, KU15P and VU7P soldered, needs LGA80D heatsink ready for FPGA testing and final assembly | |
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