6089-103 Hardware Manual

# Introduction

## To Do

### PCBLayout

Label firefly sites   
  X4 XCVR   
  X12 XMIT   
  X12 RECV

### Schematic

update BOM   
  
order firefly, after determining length   
  
order missing parts   
  
label jumpers   
  
cleanup traces   
  equal length for 25G pairs   
  curves for 25G pairs   
  ugly routes   
  
check backdrill clearance   
  
check via tenting on top   
  covered in  FPGA   
  uncovered in QFN   
  
verify Xilinx design spreadsheets   
  
label test points for power supplies   
  near TM4C analog inputs   
  
check paste mask   
  
verify unused pins   
  
design covers   
  
order heatsinks for firefly   
  
create TM4C document for Peter

To get started right away, just tap any placeholder text (such as this) and start typing to replace it with your own.

Want to insert a picture from your files or add a shape, text box, or table? You got it! On the Insert tab of the ribbon, just tap the option you need.

Find even more easy-to-use tools on the Insert tab, such as to add a hyperlink or insert a comment.

# TM4C Controller

# I2C

# JTAG

# UARTs