

BUCK CONVERTER TRANSFER FUNCTION DERIVATION

AVERAGE MODEL & SMALL SIGNAL CIRCUITS

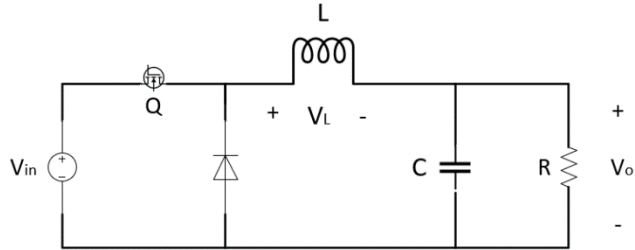
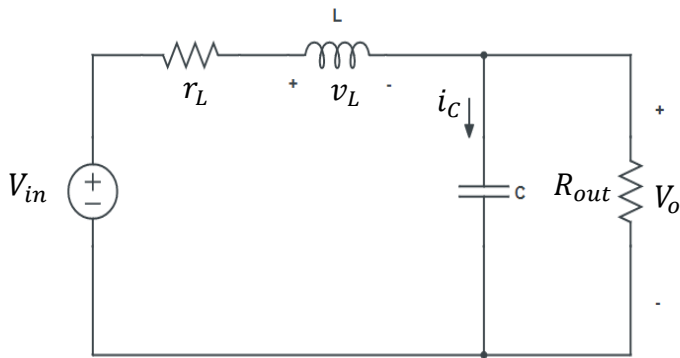
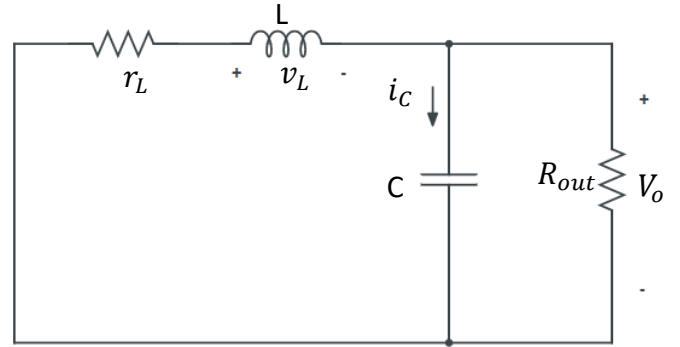


Figure 1. Buck Converter

Interval I: D



Interval II: 1 - D



1. Averaged, Small Signal Circuit Description

D Interval:

$$v_L(t) = V_{in} - i_L(t)r_L - V_o \quad \parallel \quad i_C(t) = i_L(t) - \frac{V_o}{R_{out}}$$

1 - D Interval:

$$v_L(t) = -i_L(t)r_L - V_o \quad \parallel \quad i_C(t) = i_L(t) - \frac{V_o}{R_{out}}$$

2. Capacitor Charge & Volt. Second Balance Equations

$$L \frac{di_L}{dt} = D(V_{in} - i_L r_L - V_o) + (1 - D)(-i_L r_L - V_o)$$

$$C \frac{dV_C}{dt} = D(i_L - \frac{V_o}{R_{out}}) + (1 - D)(i_L - \frac{V_o}{R_{out}})$$

3. Small Signal "hat" Terms ($x(t) = X + \hat{x}$)

Inductor:

$$L \frac{d(I_L + \hat{i}_L)}{dt} = \left((D + \hat{d})(V_{in} + \hat{v}_{in}) - (I_L + \hat{i}_L)r_L - (V_o + \hat{v}_o) \right) + \left((1 - (D + \hat{d}))(- (I_L + \hat{i}_L)r_L - (V_o + \hat{v}_o)) \right)$$

Single hat terms:

$$L \frac{d(\hat{i}_L)}{dt} = \hat{d}V_{in} - \hat{d}I_L r_L - \hat{d}V_o + D\hat{v}_{in} - D\hat{i}_L r_L - D\hat{v}_o - \hat{i}_L r_L - \hat{v}_o + \hat{d}I_L r_L + \hat{d}V_o + D\hat{i}_L r_L + D\hat{v}_o$$

$$L \frac{d(\hat{i}_L)}{dt} = \hat{d}V_{in} + D\hat{v}_{in} - \hat{i}_L r_L - \hat{v}_o$$

Averaged Small Signal

DC Terms:

$$L \frac{d(I_L)}{dt} = DV_{in} - DI_L r_L - DV_o - I_L r_L - V_o + DI_L r_L + DV_o$$

$$0 = DV_{in} - I_L r_L - V_o$$

Averaged steady-state DC eqn.

Capacitor:

$$C \frac{d(V_C + \hat{v}_C)}{dt} = (D + \hat{d}) \left((I_L + \hat{i}_L) - \frac{(V_o + \hat{v}_o)}{R_{out}} \right) + (1 - (D + \hat{d})) \left((I_L + \hat{i}_L) - \frac{(V_o + \hat{v}_o)}{R_{out}} \right)$$

Single hat terms:

$$C \frac{d(\hat{v}_C)}{dt} = \hat{d}I_L - \hat{d} \frac{V_o}{R_{out}} + D\hat{i}_L - D \frac{\hat{v}_o}{R_{out}} + \hat{i}_L - \frac{\hat{v}_o}{R_{out}} - \hat{d}I_L + \hat{d} \frac{V_o}{R_{out}} - D\hat{i}_L + D \frac{\hat{v}_o}{R_{out}}$$

$$C \frac{d(\hat{v}_C)}{dt} = \hat{i}_L - \frac{\hat{v}_o}{R_{out}}$$

Averaged Small Signal

DC Terms:

$$C \frac{d(V_C)}{dt} = DI_L - D \frac{V_o}{R_{out}} + I_L - \frac{V_o}{R_{out}} - DI_L + D \frac{V_o}{R_{out}}$$

$$C \frac{d(V_C)}{dt} = I_L - \frac{V_o}{R_{out}}$$

Averaged Steady-State DC eqn

Input Current:

$$i_{in}(t) = (D + \hat{d}) < i_L(t) >$$

$$I_{in} + \hat{i}_{in} = DI_L + \hat{d}I_L + D\hat{i}_L + \hat{d}\hat{i}_L$$

Very small compared to DC values, ignored.

$$\hat{i}_{in} = \hat{d}I_L + D\hat{i}_L$$

→ Averaged small signal

$$I_{in} = DI_L$$

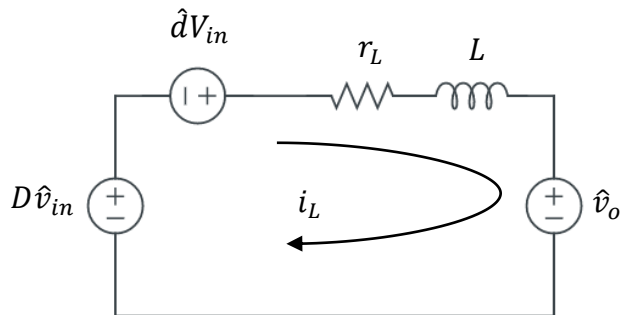
→ Steady-state DC signal

Small signal and steady-state DC signal analysis has been implemented. Now, they should be merged in one circuit to get the transfer function of the system.

Inductor:

Small Signal Eqn:

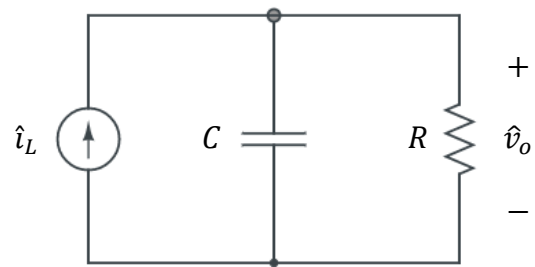
$$L \frac{d(\hat{i}_L)}{dt} = \hat{d}V_{in} + D\hat{v}_{in} - \hat{i}_L r_L - \hat{v}_o$$



Capacitor:

Small Signal Eqn:

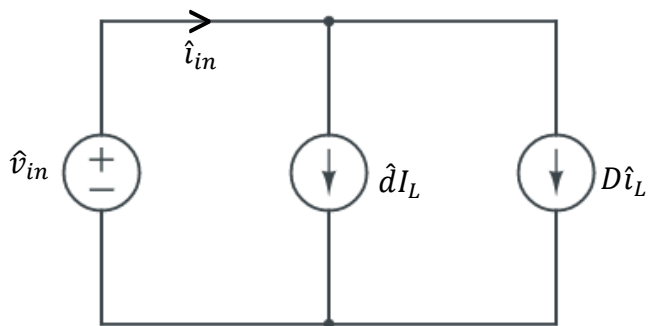
$$C \frac{d(\hat{v}_C)}{dt} = \hat{i}_L - \frac{\hat{v}_o}{R_{out}}$$

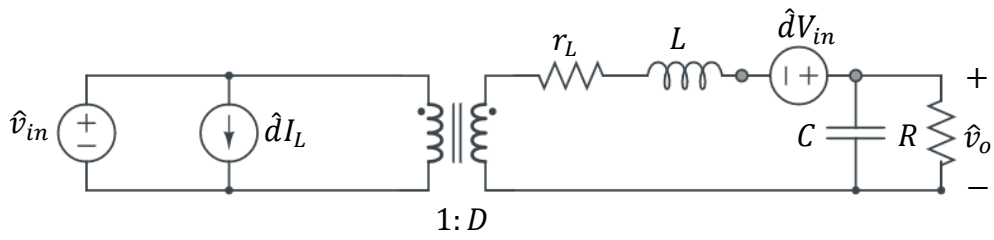


Input Current:

Small Signal Eqn:

$$\hat{i}_{in} = \hat{d}I_L + D\hat{i}_L$$





4. Control-to-Output Transfer Function

$$\hat{v}_o = V_{in} \hat{d} \times \frac{\left(\frac{1}{sC} // R_{out} \right)}{r_L + sL + \left(\frac{1}{sC} // R_{out} \right)}$$

$$\frac{1}{sC} // R_{out} = \frac{\frac{R_{out}}{sC}}{R_{out} + \frac{1}{sC}} = \frac{R_{out}}{1 + sCR_{out}}$$

$$G_p = \frac{\hat{v}_o}{\hat{d}} = V_{in} \frac{\frac{R_{out}}{1 + sCR_{out}}}{r_L + sL + \frac{R_{out}}{1 + sCR_{out}}} \rightarrow \frac{R_{out}}{r_L + sLCR_{out} + sL + s^2R_{out}LC + R_{out}} \times \frac{1}{\frac{R_{out}}{1}} \times \frac{1}{R_{out}}$$

$$\frac{\hat{v}_o}{\hat{d}} = V_{in} \frac{1}{s^2LC + s\left(\frac{L}{R_{out}} + r_L C\right) + 1 + \frac{r_L}{R_{out}}}$$

If there is no resistance of the inductor, use this formula:

$$\frac{\hat{v}_o}{\hat{d}} = V_{in} \frac{1}{s^2LC + s\frac{L}{R_{out}} + 1}$$

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