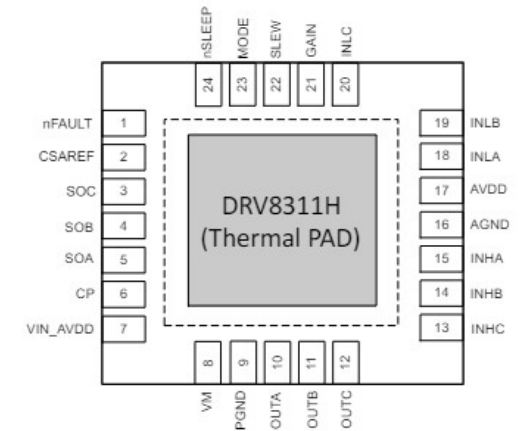


Table 8-5. Hardware Pins Decode

Configuration	GAIN	SLEW	MODE
Pin tied to AGND	0.25 V/A	35 V/us	6x PWM Mode and 9A OCP Level
Pin to 47 kΩ tied to AGND	0.5 V/A	75 V/us	6x PWM Mode and 5A OCP Level
Pin to Hi-Z	1 V/A	180 V/us	3x PWM Mode and 9A OCP Level
Pin tied to AVDD	2 V/A	230 V/us	3x PWM Mode and 5A OCP Level



Alper Basaran

Sheet: /Motor drive/
File: MotorDrive.kicad_sch

Title: Magnetic Encoder IC and Peripherals

Size: A4

Date:

Rev:

KiCad E.D.A. 8.0.1

Id: 2/3

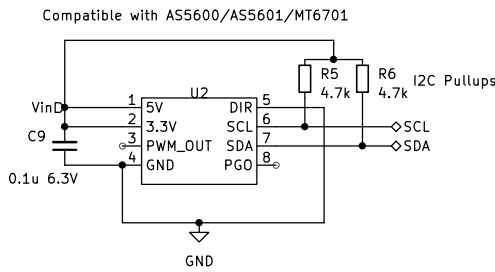


Figure 3:
SOIC-8 Pin-Out

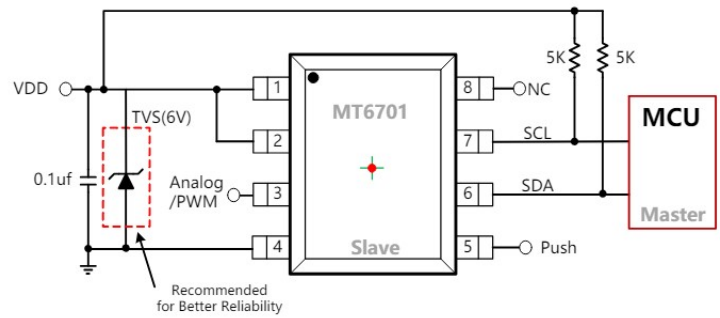
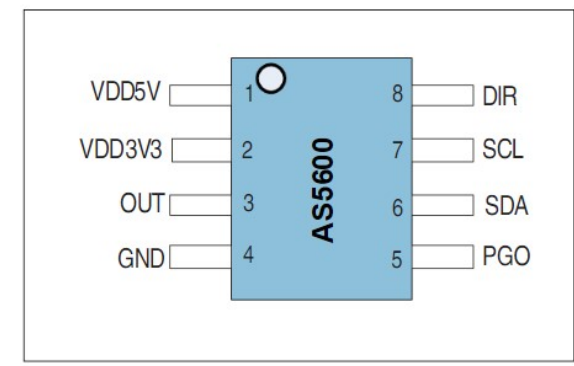


Figure 17: PC Reference Circuit of SOP-8 Package

