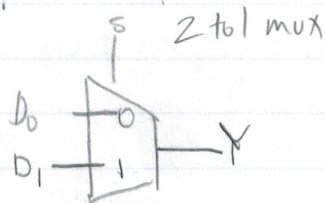


2 to 1, 4 to 1, and 8 to 1 MUX's  
LAB 1: #4

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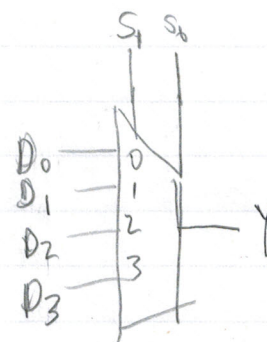


expanded 2 to 1 T.T.

S	D1	D0	Y
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

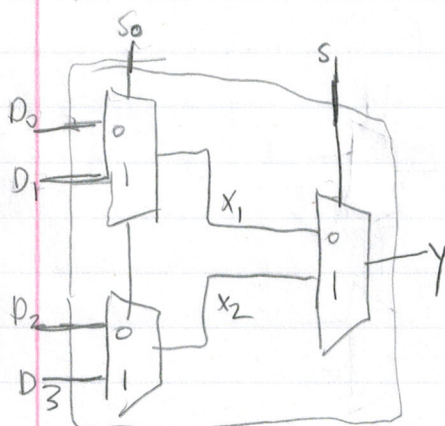
2 to 1 condensed T.T.

S	Y
0	D0
1	D1

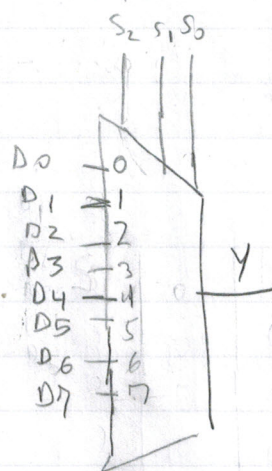


4 to 1 condensed T.T.

S <sub>1</sub>	S <sub>0</sub>	Y
00	D <sub>0</sub>	
01	D <sub>1</sub>	
10	D <sub>2</sub>	
11	D <sub>3</sub>	

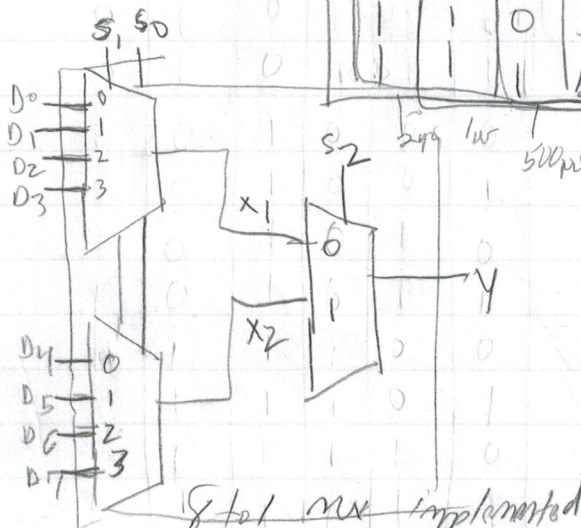


4 to 1 mux implemented  
w/ 2 to 1 mux



8 to 1 T.T.

S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>	Y
0	0	0	D <sub>0</sub>
0	0	1	D <sub>1</sub>
0	1	0	D <sub>2</sub>
0	1	1	D <sub>3</sub>
1	0	0	D <sub>4</sub>
1	0	1	D <sub>5</sub>
1	1	0	D <sub>6</sub>
1	1	1	D <sub>7</sub>



8 to 1 mux implemented w/  
4 to 1 & 2 to 1