

Input

```
graph TD; Input[Input] --> InceptionUnit1[Inception Unit]; InceptionUnit1 --> InceptionUnit2[Inception Unit]; InceptionUnit2 --> InceptionUnit3[Inception Unit]; InceptionUnit3 --> Output[Output];
```

The diagram illustrates a sequential neural network architecture. It begins with an 'Input' node, followed by three 'Inception Unit' blocks, and ends with an 'Output' node. The flow is indicated by downward-pointing arrows connecting each stage in sequence.

Inception Unit

Inception Unit

Inception Unit

Output