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MEK6800D2 MANUAL

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MEK 6800D2 Manual by Motorola 1976

by Motorola

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MCM6830 ROM with JBUG Monitor, MC6820, MC6850, Asynchronous Communications Interface Adapter (ACIA),

MC6871B Clock Generator

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MEK 6800D2 Manual

1976

Introduction

1-1 - General Description and Capability

This manual provides a general description and operating instructions for the Motorola MEK6800D2 Evaluation Kit II. The Kit, when assembled, is a fully functional microcomputer system based on the MC6800 Microprocessing Unit (MPU) and its family of associated memory and I/O devices. The family is described in the M6800 Microcomputer System Design Data book (included with the Kit) and in the M6800 Microprocessor Applications Manual. Detailed programming information is included in the M6800 Programming Reference Manual.

The MEK6800D2 is designed to provide a completely self-contained method for evaluating the characteristics of the M6800 family. The standard Kit includes the following devices:

Qty. Device

- MC6800 MPU
- MCM6830 ROM with JBUG Monitor (SCM44520P)
- MCM6810 RAM (128 x 8)
- MC6820 Peripheral Interface Adapter (PIA)
- MC6850 Asynchronous Communications Interface Adapter (ACIA)
- MC6871B Clock Generator

As assembled Kit is shown in Figure 1-1-1 (all components shown are included with the standard Kit.)

The Microcomputer Module printed circuit board is preengineered to accept the following additional components for expanding its capability:

Device

- 2 MCM6810 RAM (128 x 8)
- MCM68708 EPROM (Equivalent to 2708)
- MC8T97 Buffer
- 2 MC8T26 Bidirectional Buffer

The expansion capability provides for a variety of user operating modes.

The integral Keyboard/Display Module can be used in conjunction with the JBUG monitor program for entering and debugging user programs. Programs can also be loaded and dumped via the Audio Cassette Interface. The Keyboard, Display and Audio Cassette circuitry are on a separate printed circuit board so that the ACIA and a second PIA are available if the user has access to an RS-232 or TTY terminal. Wire-wrap space for up to twenty 16-pin DIP packages is available for user designed circuitry on the Microcomputer Module. A user generated terminal control program designed to interface with either the PIA or the ACIA can be entered via the integral keyboard. Alternatively, the Kit will accept (in place of JBUG) the Motorola MINIbug II monitor program. MINIbug II has monitor and diagnostic capabilities similar to JBUG but is intended for use with RS-232 and TTY type terminals. (See Appendix E of the Programming Reference Manual included in the Kit.)

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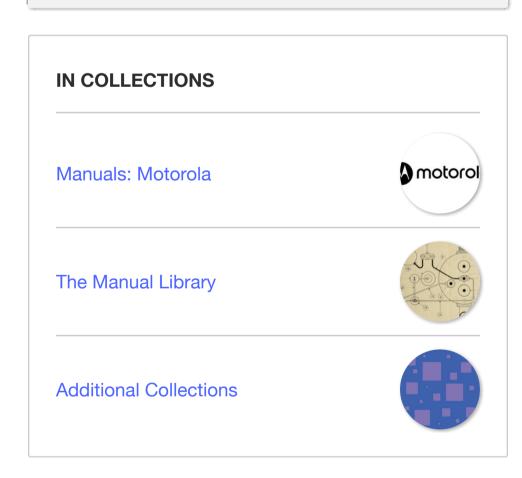
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