GigaDevice Semiconductor Inc.

GD32403Z-EVAL

User Guide V2.0



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1. Summary

GD32403Z-EVAL uses GD32F403ZET6 as the main controller. It uses Mini USB interface or DC-005 connector to supply 5V power. SWD, Reset, Boot, User button key, LED, CAN, I2C, I2S, USART, RTC, LCD, SPI, ADC, DAC, EXMC, CTC, SDIO, USBFS, GD-Link and Extension Pins are also included. For more details please refer to GD32403Z-EVAL-V1.1 schematic.

2. Function pin assignment

Table 2-1 Function pin assignment

Function	Pin	Description
	PF0	LED2
LED	PF1	LED3
RESET KEY USART0 USART1 ADC DAC	PF2	LED4
	PF3	LED5
RESET		K1-Reset
	PA0	K2-Wakeup
VEV	PC13	K3-Tamper
KEY	PF5	K4-User key1
	PF4	K5-User key2
LICADTO	PA9	USART0_TX
USARTU	PA10	USART0_RX
LICADTA	PA2	USART1_TX
USART1	PA3	USART1_RX
ADC	PC3	ADC012_IN13
D4.0	PA4	DAC_OUT0
DAC	PA3 USA PC3 ADC0 PA4 DA0 PA5 DA0 PB6 I20 PB7 I20	DAC_OUT1
100	PB6	I2C0_SCL
I2C	PF0 PF1 PF2 PF3 PA0 PA0 PC13 PF5 RA PF4 RS PA9 PA9 PA10 PA2 PA3 PC3 PA4 PA5 PA6 PA7 PB12 PB13 PB15 PC6	I2C0_SDA
	PF1 PF2 PF3 PF3 PA0 PC13 PC13 PF5 PF4 PA9 PA9 PA9 PA10 PA2 PA3 PC3 PA4 PA5 PB6 PB7 PA5 PA6 PA7 PE3 PA4 PA5 PA7 PB12 PB13 PB15 PC6	SPI0_SCK
CDI		SPI0_MISO
SPI	PA7	SPI0_MOSI
	PE3	SPI0_CS
	PA4	MSEL
	PA5	MCLK
	PA7	MDIN
128	PB12	I2S_WS
	PB13	I2S_CK
	PB15	I2S_DIN
	PC6	I2S_MCK
CAN	PD0	CAN0_RX



GD324032-EV		ODOZHOOZ EVILE
	PD1	CAN0_TX
	PD2	SDIO_CMD
	PC12	SDIO_CLK
0010	PC8	SDIO_DAT0
SDIO	PC9	SDIO_DAT1
	PC10	SDIO_DAT2
	PC11	SDIO_DAT3
	PD14	EXMC_D0
	PD15	EXMC_D1
	PD0	EXMC_D2
	PD1	EXMC_D3
	PE7	EXMC_D4
	PE8	EXMC_D5
	PE9	EXMC_D6
NAND Flash	PE10	EXMC_D7
	PD11	EXMC_A16
	PD12	EXMC_A17
	PD4	EXMC_NOE
	PD5	EXMC_NWE
	PD6	EXMC_NWAIT
	PG6	EXMC_INT
	PD7	EXMC_NCE1
	PD14	EXMC_D0
	PD15	EXMC_D1
	PD0	EXMC_D2
	PD1	EXMC_D3
	PE7	EXMC_D4
	PE8	EXMC_D5
	PE9	EXMC_D6
	PE10	EXMC_D7
	PE11	EXMC_D8
	PE12	EXMC_D9
LCD	PE13	EXMC_D9
	PE13	EXMC_D10
	PE15	EXMC_D12
	PD8	EXMC_D13
	PD9	EXMC_D14
	PD10	EXMC_D15
	PE2	EXMC_A23
	PD4	EXMC_NOE
	PD5	EXMC_NWE
	PG9	EXMC_NE1
USBFS	PA9	USB_VBUS



PA11	USB_DM
PA12	USB_DP
PA10	USB_ID

3. Getting started

The EVAL board uses Mini USB connecter or DC-005 connector to get power DC +5V, which is the hardware system normal work voltage. A J-Link tool or GD-Link on board is necessary in order to download and debug programs. Select the correct boot mode and then power on, the LED1 will turn on, which indicates that the power supply is OK.

There are Keil version and IAR version of all projects. Keil version of the projects are created based on Keil MDK-ARM 4.74 uVision4. IAR version of the projects are created based on IAR Embedded Workbench for ARM 7.40.2. During use, the following points should be noted:

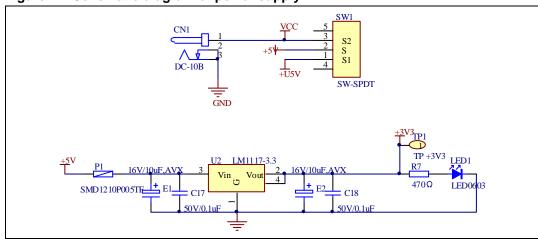
- 1. If you use Keil uVision4 to open the project, install the GD32F4xx_Addon.2.0.0.exe to load the associated files.
- 2. If you use Keil uVision5 to open the project, there are two ways to solve the "Device Missing (s)" problem. One is to install GigaDevice.GD32F4xx_DFP.2.0.0.pack. In Project menu, select the Manage sub menu, click on the "Version Migrate 5 Format..." menu, the Keil uVision4 project will be converted to Keil uVision5 project. Then add "C:\Keil_v5\ARM\Pack\ARM\CMSIS\4.2.0\CMSIS\Include" to C/C++ in Option for Target. The other is to install Addon directly. Select the installation directory of Keil uVision5 software, such as C:\Keil_v5, in Destination Folder of Folder Selection. Select the device in Device of Option for corresponding Target "C:\Keil v5\ARM\Pack\ARM\CMSIS\4.2.0\CMSIS\Include" to C/C++ in Option for Target. 3. If you use IAR to open the project, install IAR GD32F4xx Addon.2.0.0.exe to load the associated files.



4. Hardware layout overview

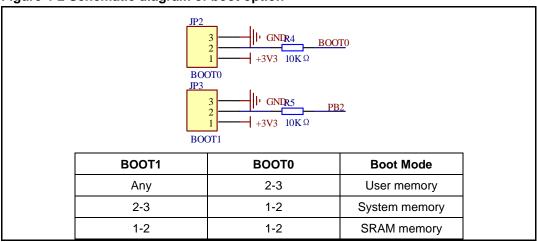
4.1. Power supply

Figure 4-1 Schematic diagram of power supply



4.2. Boot option

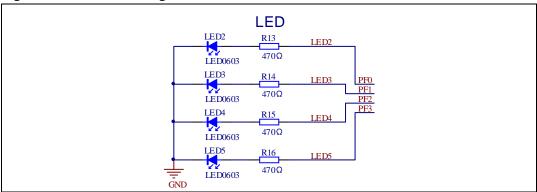
Figure 4-2 Schematic diagram of boot option





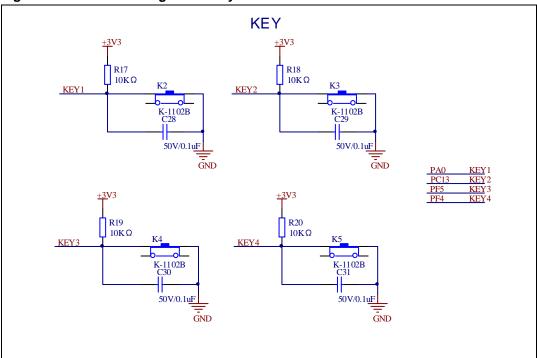
4.3. LED

Figure 4-3 Schematic diagram of LED function



4.4. **KEY**

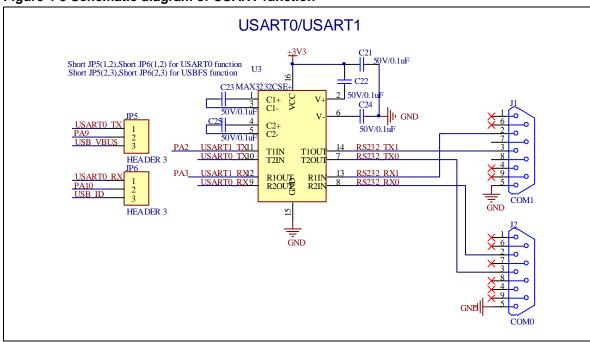
Figure 4-4 Schematic diagram of Key function





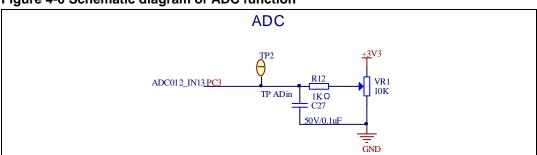
4.5. **USART**

Figure 4-5 Schematic diagram of USART function



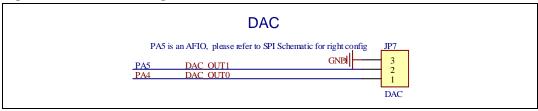
4.6. ADC

Figure 4-6 Schematic diagram of ADC function



4.7. DAC

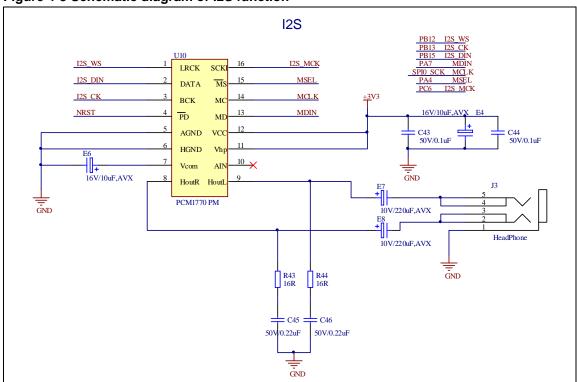
Figure 4-7 Schematic diagram of DAC function





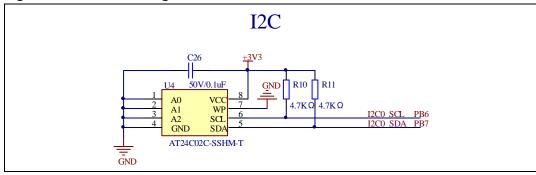
4.8. I2S

Figure 4-8 Schematic diagram of I2S function



4.9. I2C

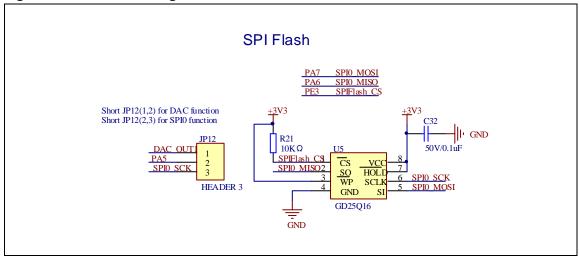
Figure 4-9 Schematic diagram of I2C function





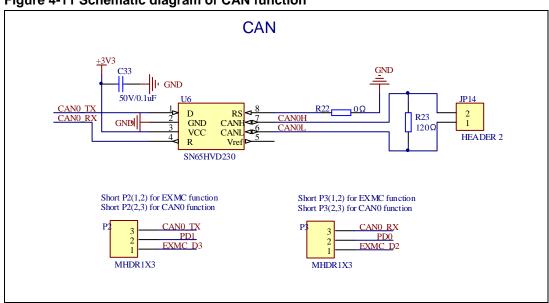
4.10. SPI

Figure 4-10 Schematic diagram of SPI function



4.11. CAN

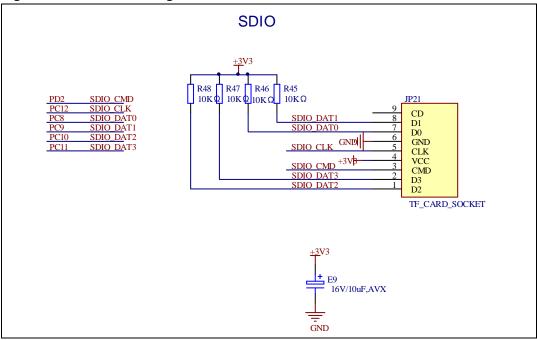
Figure 4-11 Schematic diagram of CAN function





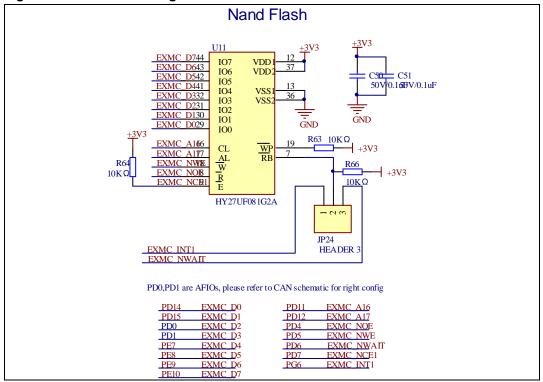
4.12. SDIO

Figure 4-12 Schematic diagram of SDIO function



4.13. NAND

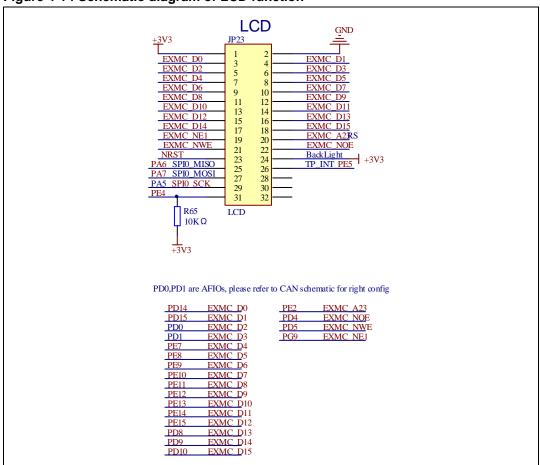
Figure 4-13 Schematic diagram of NAND Flash function





4.14. LCD

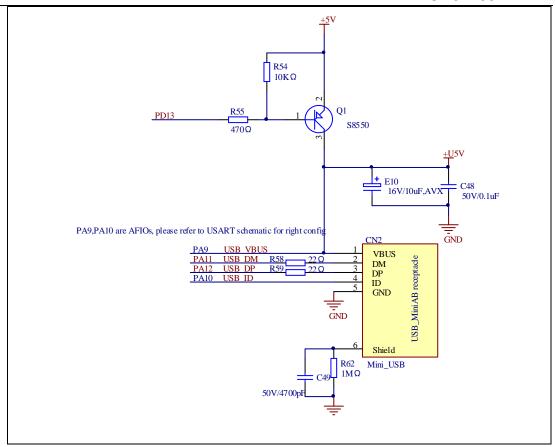
Figure 4-14 Schematic diagram of LCD function



4.15. **USBFS**

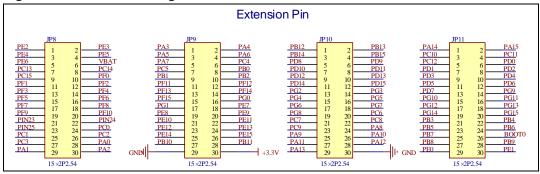
Figure 4-15 Schematic diagram of USBFS function





4.16. Extension

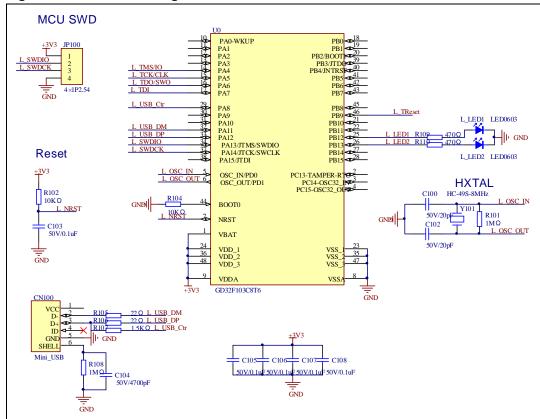
Figure 4-16 Schematic diagram of Extension Pin





4.17. **GD-Link**

Figure 4-17 Schematic diagram of GD-Link





5. Routine use guide

5.1. **GPIO_Running_LED**

5.1.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED
- Learn to use SysTick to generate 1ms delay

GD32403Z-EVAL-V1.1 board has four LEDs. The LED2, LED3, LED4 and LED5 are controlled by GPIO. This demo will show how to light the LEDs.

5.1.2. DEMO running result

Download the program <01_GPIO_Running_LED> to the EVAL board, LED2, LED3, LED4 and LED5 will turn on in sequence with interval of 1000ms, and repeat the process.

5.2. **GPIO_Key_Polling_mode**

5.2.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED and the key
- Learn to use SysTick to generate 1ms delay

GD32403Z-EVAL-V1.1 board has five keys and four LEDs. The five keys are Reset key, Tamper key, Wakeup key, User1 key and User2 key. The LED2, LED3, LED4 and LED5 are controlled by GPIO.

This demo will show how to use the Tamper key to control the LED2. When press down the Tamper Key, it will check the input value of the IO port. If the value is 0 and will wait for 100ms. Check the input value of the IO port again. If the value still is 0, it indicates that the button is pressed successfully and toggle LED2.

5.2.2. **DEMO** running result

Download the program <02_GPIO_Key_Polling_mode> to the EVAL board, press down the Tamper Key, LED2 will be turned on. Press down the Tamper Key again, LED2 will be turned off.



5.3. EXTI_Key_Interrupt_mode

5.3.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED and the KEY
- Learn to use EXTI to generate external interrupt

GD32403Z-EVAL-V1.1 board has five keys and four LEDs. The five keys are Reset key, Tamper key, Wakeup key, User1 key and User2 key. The LED2, LED3, LED4 and LED5 are controlled by GPIO.

This demo will show how to use the EXTI interrupt line to control the LED2. When press down the Tamper Key, it will produce an interrupt. In the interrupt service function, the demo will toggle LED2.

5.3.2. DEMO running result

Download the program <03_EXTI_Key_Interrupt_mode> to the EVAL board. Press down the Tamper Key, LED2 will be turned on. Press down the Tamper Key again, LED2 will be turned off.

5.4. USART Printf

5.4.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED
- Learn to retarget the C library printf function to the USART

5.4.2. **DEMO** running result

Download the program < 04_USART_Printf > to the EVAL board, jump the JP5 and JP6 to USART with the jumper cap and connect serial cable to COM0. This implementation outputs "USART printf example: please press the Tamper key" on the HyperTerminal using COM0. Press the Tamper key, the LED3 will be turned on and serial port will output "USART printf example".

The output information via the serial port is as following.

USART printf example: please press the Tamper key

USART printf example



5.5. USART_Echo_Interrupt_mode

5.5.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use the USART transmit and receive interrupts to communicate with the serial terminal tool

5.5.2. DEMO running result

Download the program < 05_USART_Echo_Interrupt_mode > to the EVAL board, jump the JP5 and JP6 to USART with the jumper cap and connect serial cable to COM0. Firstly, all the LEDs are turned on and off for test. Then, the COM0 sends the tx_buffer array (from 0x00 to 0xFF) to the serial terminal tool supporting hex format communication and waits for receiving data of BUFFER_SIZE bytes from the serial terminal. The data MCU have received is stored in the rx_buffer array. After that, compare tx_buffer with rx_buffer. If tx_buffer is same with rx_buffer, LED2, LED3, LED4, LED5 flash by turns. Otherwise, LED2, LED3, LED4, LED5 toggle together.

The output information via the serial port is as following.

```
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 17
                           25 26 27
                                   28
                                       29 2A 2B 2C
                                                   2D 2E 2F
            30
               ЗD
                 3E 3F
                       40
                           41 42 43
                                   44 45
                                          46 47 48 49 4A
                                                         4B
                                                            4C 4D
        57 58
              59
                 5A 5B 5C
                          5D 5E 5F 60 61 62 63 64 65 66 67 68 69 6A 6B 6C 6D
70 71 72 73 74 75 76 77 78 79 7A 7B 7C 7D 7E 7F 8O 81 82 83 84 85 86 87 88 89 8A 8B
  8D 8E 8F 90 91 92 93 94 95 96 97
                                   98
                                       99 9A 9B
                                               9C 9D 9E 9F AO A1
A8 A9 AA AB AC AD AE AF BO B1 B2 B3 B4 B5 B6 B7 B8 B9 BA BB BC BD BE BF CO C1 C2 C3
C4 C5 C6 C7 C8 C9 CA CB CC CD CE CF DO D1 D2 D3 D4 D5 D6 D7 D8 D9 DA DB DC DD DE DF
EO E1 E2 E3 E4 E5 E6 E7 E8 E9 EA EB EC ED EE EF FO F1 F2 F3 F4 F5 F6 F7 F8 F9 FA FB
FC FD FE FF
```

5.6. USART DMA

5.6.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

Learn to use the USART transmit and receive data using DMA

5.6.2. DEMO running result

Download the program < 06_USART_DMA > to the EVAL board, jump the JP5 and JP6 to USART with the jumper cap and connect serial cable to COM0. Firstly, all the LEDs are turned on and off for test. Then, the COM0 sends the tx_buffer array (from 0x00 to 0xFF) to the serial terminal tool supporting hex format communication and waits for receiving data of same bytes as tx_buffer from the serial terminal. The data MCU have received is stored in the rx_buffer array. After that, compare tx_buffer with rx_buffer. If tx_buffer is same with rx_buffer, LED2, LED3, LED4, LED5 flash by turns. Otherwise, LED2, LED3, LED4, LED5 toggle together.



00 01 02 03 04 05 06 07 08 09 0A OB OC OD OE OF 10 11 12 13 14 15 16 17 18 19 1A 1B 1C 1D 1E 1F 2O 21 22 23 24 25 26 27 28 29 2A 2B 2C 2D 2E 2F 30 31 3E 3F 40 41 42 43 44 45 46 47 48 49 4A 4B 54 55 56 57 58 59 5A 5B 5C 5D 5E 5F 60 61 62 63 64 65 66 67 68 69 6A 6B 6C 6D 6E 6F 70 71 72 73 74 75 76 77 78 79 7A 7B 7C 7D 7E 7F 80 81 82 83 84 85 86 87 88 89 8A 8B 8D 8E 8F 90 91 92 93 94 95 96 97 98 99 9A 9B 9C 9D 9E 9F AO A1 A9 AA AB AC AD AE AF B0 B1 B2 B3 B4 B5 B6 B7 B8 B9 BA BB BC BD BE BF C0 C1 C2 C3 C4 C5 C6 C7 C8 C9 CA CB CC CD CE CF DO D1 D2 D3 D4 D5 D6 D7 D8 D9 DA DB DC DD DE DF EO E1 E2 E3 E4 E5 E6 E7 E8 E9 EA EB EC ED EE EF FO F1 F2 F3 F4 F5 F6 F7 F8 F9 FA FB FC FD FE FF

5.7. ADC_Temperature_Vrefint

5.7.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the ADC to convert analog signal to digital data
- Learn to get the value of inner channel 16(temperature sensor channel) and channel 17 (VREFINT channel)

5.7.2. DEMO running result

Jump the JP5 and JP6 to USART with the jumper cap, and then download the program <07_ADC_Temperature_Vrefint> to the board. Connect serial cable to COM0, open the HyperTerminal.

When the program is running, HyperTerminal display the value of temperature and internal voltage reference (VREFINT).

Notice: because there is an offset, when inner temperature sensor is used to detect accurate temperature, an external temperature sensor part should be used to calibrate the offset error.

the temperature data is 31 degrees Celsius the reference voltage data is 1,205V

the temperature data is 31 degrees Celsius the reference voltage data is 1.206V

the temperature data is 31 degrees Celsius the reference voltage data is 1,206V

the temperature data is 31 degrees Celsius the reference voltage data is 1,206V

the temperature data is 31 degrees Celsius the reference voltage data is 1.205V

the temperature data is 31 degrees Celsius the reference voltage data is 1.205V



5.8. ADC0_ADC1_Follow_up_mode

5.8.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the ADC to convert analog signal to digital data
- Learn to use ADC0 and ADC1 follow-up mode

5.8.2. DEMO running result

Jump the JP5 and JP6 to USART with the jumper cap, and then download the program <08_ADC0_ADC1_Follow_up_mode> to the board. Connect serial cable to COM0, open the HyperTerminal. PC3 and PC5 pin voltage access by external voltage.

TIMER0_CH0 is the trigger source of ADC0 and ADC1. When the rising edge of TIMER0_CH0 coming, ADC0 starts immediately and ADC1 starts after a delay of several ADC clock cycles. The values of ADC0 and ADC1 are transmitted to array adc_value[0] and adc_value[1] by DMA.

When the first rising edge of TIMER0_CH0 coming, the value of the ADC0 conversion of PC3 pin is stored into the low half word of adc_value[0], and after a delay of several ADC clock cycles the value of the ADC1 conversion of PC5 pin is stored into the high half word of adc_value[0]. When the second rising edge of TIMER0_CH0 coming, the value of the ADC0 conversion of PC5 pin is stored into the low half word of adc_value[1], and after a delay of several ADC clock cycles the value of the ADC1 conversion of PC3 pin is stored into the high half word of adc_value[1].

When the program is running, HyperTerminal display the regular value of ADC0 and ADC1 by adc_value[0] and adc_value[1].

```
the data adc_value[0] is 000009B2 the data adc_value[1] is 09C00000

the data adc_value[0] is 000009B8 the data adc_value[1] is 09BD0000

the data adc_value[0] is 000009B8 the data adc_value[1] is 09BD0000

the data adc_value[0] is 000009B3 the data adc_value[1] is 09BF0000

the data adc_value[1] is 09BF0000

the data adc_value[1] is 09BF0000

the data adc_value[1] is 09BF0000
```



5.9. ADC0_ADC1_Regular_Parallel_mode

5.9.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the ADC to convert analog signal to digital data
- Learn to use ADC0 and ADC1 regular parallel mode

5.9.2. DEMO running result

Jump the JP5 and JP6 to USART with the jumper cap, and then download the program <09_ADC0_ADC1_Regular_Parallel_mode> to the board. Connect serial cable to COM0, open the HyperTerminal. PC3 and PC5 pin connect to external voltage input.

TIMER0_CH0 is the trigger source of ADC0 and ADC1. When the rising edge of TIMER0_CH0 coming, ADC0 and ADC1 convert the regular channel group parallelly. The values of ADC0 and ADC1 are transmitted to array adc_value[0] and adc_value[1] by DMA.

When the first rising edge of TIMER0_CH0 coming, the value of the ADC0 conversion of PC3 pin is stored into the low half word of adc_value[0], the value of the ADC1 conversion of PC5 pin is stored into the high half word of adc_value[0]. When the second rising edge of TIMER0_CH0 coming, the value of the ADC0 conversion of PC5 pin is stored into the low half word of adc_value[1], the value of the ADC1 conversion of PC3 pin is stored into the high half word of adc_value[1].

When the program is running, HyperTerminal displays the regular value of ADC0 and ADC1 stored in adc_value[0] and adc_value[1].

```
the data adc_value[0] is 000009BB the data adc_value[1] is 09BB0000
the data adc_value[0] is 000009BB the data adc_value[1] is 09BB0000
the data adc_value[0] is 000009B9 the data adc_value[1] is 09BB0000
the data adc_value[0] is 000009BB the data adc_value[1] is 09BA0000
the data adc_value[1] is 09BA0000
the data adc_value[1] is 000009BA the data adc_value[1] is 09BA0000
```

5.10. DAC_Output_Voltage_Value

5.10.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

Learn to use DAC to output voltage on DAC0 output



5.10.2. DEMO running result

Download the program <10_DAC_Output_Voltage_Value> to the EVAL board and run, all the LEDs will turn on and turn off for test. The digital value is 0x7FF0, its converted analog voltage should be 1.65V (VREF/2), using the voltmeter to measure PA4 or DAC_OUT0 on JP7, its value is 1.65V. And the signal can be observed through the oscilloscope.

5.11. I2C EEPROM

5.11.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the master transmitting mode of I2C module
- Learn to use the master receiving mode of I2C module
- Learn to read and write the EEPROM with I2C interface

5.11.2. **DEMO** running result

Jump the JP5 and JP6 to USART with the jumper cap, and download the program <11_I2C_EEPROM> to the EVAL board and run. Connect serial cable to COM0, and open the HyperTerminal to show the print message.

Firstly, the data of 256 bytes will be written to the EEPROM from the address 0x00 and printed by the serial port. Then, reading the EEPROM from address 0x00 for 256 bytes and the result will be printed. Finally, compare the data that were written to the EEPROM and the data that were read from the EEPROM. If they are the same, the serial port will output "I2C-AT24C02 test passed!" and the four LEDs lights flashing, otherwise the serial port will output "Err: data read and write aren't matching." and all the four LEDs light. The output information via the serial port is as following.



```
I2C-24CO2 configured....
The I2CO is hardware interface
The speed is 400000
AT24CO2 writing...
0x00 0x01 0x02
                   0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F
0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F
0x2D 0x2E 0x2F
      0x31 0x32 0x33 0x34
                                              0x37
0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47 0x48 0x49 0x4A 0x4B 0x4C
                                                                                       Ox4D Ox4E Ox4F
0x50 0x51 0x52 0x53 0x54 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C
0x60 0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68 0x69 0x6A 0x6B 0x6C
                                 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C
                                                                                       0x6D
                                                                                              0x6E
                                                                                                     0x6F
                                 0x75 0x76 0x77 0x78 0x79 0x7A 0x7B 0x7C
0x85 0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C
0x70 0x71 0x72 0x73 0x74
                                                                                       0x7D 0x7E
                                                                                                     0x7F
      0x81 0x82
                   0x83 0x84 0x85
                                                                                       0x8D
                                                                                              0x8E
                                                                                                     0x8F
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97 0x98 0x99 0x9A 0x9B 0x9C
0xAO 0xA1 0xA2 0xA3 0xA4 0xA5 0xA6 0xA7 0xA8 0xA9 0xAA 0xAB 0xAC
                                                                                       Ox9D Ox9E Ox9E
                                                                                       OxAD OxAE
                                                                                                    0xAF
0xB0 0xB1 0xB2 0xB3 0xB4 0xB5 0xB6 0xB7 0xB8 0xB9 0xBA 0xBB 0xC0 0xC1 0xC2 0xC3 0xC4 0xC5 0xC6 0xC7 0xC8 0xC9 0xCA 0xCB 0xCC
                                                                                       OxBD OxBE
                                                                                                    \Omega_{\mathbf{Y}}\mathbf{BF}
OxDO OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7 OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDE
OxFO OxF1 OxF2 OxF3 OxF4 OxF5 OxF6 OxF7 OxF8 OxF9 OxFA OxFB OxFC OxFD OxFE OxFE
AT24CO2 reading...
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C
0x10 0x11 0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C
0x20 0x21 0x22 0x23 0x24 0x25 0x26 0x27 0x28 0x29 0x2A 0x2B 0x2C
                                                                                       Ox2D Ox2E Ox2E
0x30 0x31 0x32 0x33 0x34 0x35 0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C
                                                                                       Ox3D Ox3E Ox3F
0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47 0x48 0x49 0x4A 0x4B 0x4C
                                                                                       0x4D 0x4E 0x4F
0x50 0x51 0x52 0x53 0x54 0x55 0x56 0x57 0x58 0x59 0x5A 0x5B 0x5C
                                                                                       Ov5D Ov5E Ov5E
      0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68
                                                            0x69 0x6A 0x6B
0x70 0x71 0x72 0x73 0x74 0x75 0x76 0x77 0x78 0x79 0x7A 0x7B 0x7C
0x80 0x81 0x82 0x83 0x84 0x85 0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C
                                                                                       0x7D 0x7E 0x7E
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97 0x98 0x99 0x9A 0x9B 0x9C 0x9D 0x9E 0x9E
0xA0 0xA1 0xA2 0xA3 0xA4 0xA5 0xA6 0xA7 0xA8 0xA9 0xAA 0xAB 0xAC
0xB0 0xB1 0xB2 0xB3 0xB4 0xB5 0xB6 0xB7 0xB8 0xB9 0xBA 0xBB 0xBC
                                                                                       OxBD OxBE OxBE
0xC0 0xC1 0xC2 0xC3 0xC4 0xC5 0xC6 0xC7 0xC8 0xC9 0xCA 0xCB 0xCC 0xD0 0xD1 0xD2 0xD3 0xD4 0xD5 0xD6 0xD7 0xD8 0xD9 0xDA 0xDB 0xDC
                                                                                      OxCD OxCE OxCF
OxDD OxDE OxDF
OXEO OXE1 OXE2 OXE3 OXE4 OXE5 OXE6 OXE7 OXE8 OXE9 OXEA OXEB OXEC OXED OXEE OXEF
OxFO OxF1 OxF2 OxF3 OxF4 OxF5 OxF6 OxF7 OxF8 OxF9 OxFA OxFB OxFC OxFD OxFE OxFF
```

5.12. SPI_SPI_Flash

5.12.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use the master mode of SPI unit to read and write NOR Flash with the SPI interface

5.12.2. DEMO running result

The computer serial port line connected to the COM0 port of development board, set the baud rate of HyperTerminal software to 115200, 8 bits data bit, 1 bit stop bit. At the same time you should jump the JP5 and JP6 to USART, and jump the JP12 to SPI.

Download the program <12_SPI_SPI_Flash> to the EVAL board, the HyperTerminal software can observe the operation condition and will display the ID of the flash, 256 bytes data which are written to and read from flash. Compare the data that were written to the flash and the data that were read from the flash. If they are the same, the serial port will output "SPI-GD25Q40 Test Passed!", otherwise, the serial port will output "Err: Data Read and Write aren't Matching.". At last, turn on and off the leds one by one. The following is the experimental results.



```
GD32403Z-EVAL-V1.1 System is Starting up...
GD32403Z-EVAL-V1.1 Flash:512K
GD32403Z-EVAL-V1.1 The CPV Unique Device ID:[37373431-7363533-600200]
GD32403Z-EVAL-V1.1 SPI Flash:GD25Q16 configured...
The Flash_ID:0xC84015
Write to tx_buffer:
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F 0x10 0x11
0x12 0x13 0x14 0x15 0x16 0x17
                            0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F
                                                                   0x20 0x21 0x22 0x23
0x24 0x25 0x26 0x27 0x28 0x29 0x2A 0x2B 0x2C 0x2D 0x2E 0x2F 0x3O 0x31 0x32 0x33 0x34 0x35
                                 0x3D 0x3E 0x3F 0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47
0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C
0x50 0x51 0x52 0x53 0x54 0x55
                                                                   0x56 0x57 0x58
                                                                                  0x59
0x5A 0x5B 0x5C 0x5D 0x5E 0x5F 0x6O 0x61 0x62 0x63 0x64 0x65 0x66 0x67
                                                                   0x68 0x69 0x6A
0x6C 0x6D 0x6E 0x6F 0x70
                        0x71
                            0x72
                                 0x73
                                      0x74 0x75 0x76
                                                     0x77
                                                          0x78 0x79
                                                                   0x7A 0x7B 0x7C
0x7E 0x7F 0x80 0x81 0x82 0x83 0x84 0x85
                                      0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C 0x8D 0x8E 0x8F
0x90 0x91 0x92 0x93 0x94 0x95
                            0x96 \ 0x97
                                      0x98 0x99 0x9A 0x9B 0x9C
                                                               Ox9D Ox9E Ox9F OxAO OxA1
lOxA2 OxA3 OxA4 OxA5 OxA6 OxA7
                            OxA8 OxA9
                                      Oxaa Oxab Oxac
                                                     OxAD OxAE OxAF
                                                                   0xB0 0xB1 0xB2
                                                                                  0xB3
OxB4 OxB5 OxB6 OxB7 OxB8 OxB9 OxBA OxBB OxBC OxBD OxBE OxBF OxCO OxC1 OxC2 OxC3 OxC4 OxC5
0xC6 0xC7
         OxC8 OxC9 OxCA OxCB OxCC OxCD OxCE OxCF OxD0 OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7
OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDF OxEO OxE1 OxE2 OxE3 OxE4 OxE5 OxE6 OxE7 OxE8 OxE9
OXEA OXEB OXEC OXED OXEE OXEF OXFO OXF1 OXF2 OXF3 OXF4 OXF5 OXF6 OXF7 OXF8 OXF9 OXFA OXFB
OxFC OxFD OxFE OxFF
Read from rx_buffer:
0x00 0x01 0x02 0x03 0x04 0x05 0x06 0x07 0x08 0x09 0x0A 0x0B 0x0C 0x0D 0x0E 0x0F 0x10 0x11
0x12 0x13 0x14 0x15 0x16 0x17 0x18 0x19 0x1A 0x1B 0x1C 0x1D 0x1E 0x1F 0x20 0x21 0x22 0x23
0x24 0x25 0x26 0x27 0x28 0x29
                            Ox2A Ox2B Ox2C Ox2D Ox2E
                                                     0x2F
                                                         0x30 0x31 0x32 0x33 0x34
0x36 0x37 0x38 0x39 0x3A 0x3B 0x3C 0x3D 0x3E 0x3F 0x40 0x41 0x42 0x43 0x44 0x45 0x46 0x47
Ox48 Ox49 Ox4A Ox4B Ox4C Ox4D Ox4E Ox4F
                                      0x50 0x51 0x52
                                                     0x53 0x54 0x55
                                                                   0x56 0x57 0x58
                                                                                  0x59
0x5A 0x5B 0x5C 0x5D 0x5E 0x5F 0x60 0x61 0x62 0x63 0x64 0x65 0x66 0x67 0x68 0x69 0x6A 0x6B
0x6C 0x6D 0x6E 0x6F 0x70 0x71 0x72 0x73 0x74 0x75 0x76 0x77
                                                         0x78 0x79 0x7A 0x7B 0x7C
                                                                                  0x7D
Ox7E Ox7F
         0x80 0x81 0x82 0x83
                            0x84 0x85
                                      0x86 0x87 0x88 0x89 0x8A 0x8B 0x8C 0x8D 0x8E
0x90 0x91 0x92 0x93 0x94 0x95 0x96 0x97
                                      Ox98 Ox99 Ox9A Ox9B Ox9C Ox9D Ox9E Ox9F OxAO
                                                                                  0xA1
OxA2 OxA3 OxA4 OxA5 OxA6 OxA7 OxA8 OxA9 OxAA OxAB OxAC
                                                     OxAD OxAE OxAF
                                                                   OxBO OxB1 OxB2
                                                                                  0xB3
OxB4 OxB5 OxB6 OxB7 OxB8 OxB9 OxBA OxBB OxBC OxBD OxBE OxBF OxC0 OxC1 OxC2 OxC3 OxC4 OxC5
OxC6 OxC7 OxC8 OxC9 OxCA OxCB OxCC OxCD OxCE OxCF OxD0 OxD1 OxD2 OxD3 OxD4 OxD5 OxD6 OxD7
OxD8 OxD9 OxDA OxDB OxDC OxDD OxDE OxDF OxEO OxE1 OxE2 OxE3 OxE4 OxE5 OxE6 OxE7 OxE8 OxE9
OXEA OXEB OXEC OXED OXEE OXEF OXFO OXF1 OXF2 OXF3 OXF4 OXF5 OXF6 OXF7 OXF8 OXF9 OXFA OXFB
OxFC OxFD OxFE OxFF
SPI-GD25Q16 Test Passed!
```

5.13. I2S Audio Player

5.13.1. DEMO purpose

This Demo includes the following functions of GD32 MCU:

- Learn to use I2S module to output audio file
- Parsing audio files of wav format

GD32403Z-EVAL-V1.1 board integrates the I2S (Inter-IC Sound) module, and the module can communicate with external devices using the I2S audio protocol. This Demo shows how to use the I2S interface of the board for audio output.

5.13.2. DEMO running result

Download the program<13_I2S_Audio_Player>to the EVAL board, insert the headphone into the audio port, and then listen to the audio file.



5.14. EXMC_NandFlash

5.14.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use EXMC control the NAND flash

5.14.2. **DEMO** running result

GD32403Z-EVAL-V1.1 board has EXMC module to control NAND flash. Before running the demo, JP6 must be fitted to USART, P2 and P3 must be fitted to the EXMC port, JP24 must be fitted to the Nwait port. Download the program <14_EXMC_NandFlash> to the EVAL board. This demo shows the write and read operation process of NAND flash memory by EXMC module. If the test pass, LED2 will be turned on. Otherwise, turn on the LED4. Information via a HyperTerminal output as following:

```
NAND flash initialized!
Read NAND ID!
Nand flash ID:0xC8 0xF1 0x80 0x1D
Write data successfully!
Read data successfully!
Check the data!
Access NAND flash successfully!
The data to be read:
                                                               8
   10
           11
                  12
                          13
                                 14
                                        15
                                                16
                                                       17
                                                               18
                                                                      19
                                                                             1a
                                                                                     1Ъ
                                                                                            1 c
                                                                                                   1 d
                                                                                                                  1f
   20
           21
                  22
                          23
                                 24
                                        25
                                                26
                                                       27
                                                               28
                                                                      29
                                                                                            2c
                                                                                                   2d
                                                                                                                  2f
                                                                             2а
                                                                                    2Ъ
           31
                  32
                          33
                                        35
                                                       37
                                                                                            Зс
                                                                                                                  3f
   30
                                 34
                                                36
                                                               38
                                                                      39
                                                                             Зa
                                                                                    ЗЪ
                                                                                                   3d
                                                                                                           3e
                                        45
55
   40
                  42
                                                46
                                                       47
57
           41
                          43
                                                               48
                                                                      49
                                                                             4a
                                                                                            4с
                                                                                                                  4£
                                 44
                                                                                    4Ъ
                                                                                                   4d
                                                                                                           4e
          51
   50
                  52
                                                                                                                  5f
                         53
                                                56
                                 54
                                                              58
                                                                      59
                                                                             5a
                                                                                    5Ъ
                                                                                            5c
                                                                                                   5d
                                                                                                           5e
   60
           61
                  62
                         63
                                 64
                                        65
                                                66
                                                       67
                                                               68
                                                                      69
                                                                             ба
                                                                                    6Ъ
                                                                                            6c
                                                                                                   64
                                                                                                           бе
                                                                                                                  6f
                  72
   70
           71
                         73
                                 74
                                        75
                                                76
                                                       77
                                                               78
                                                                      79
                                                                             7a
                                                                                            7с
                                                                                                   7d
                                                                                                           7e
                                                                                                                  7£
                                                                                    7Ъ
   80
          81
                  82
                         83
                                 84
                                        85
                                                86
                                                       87
                                                              88
                                                                      89
                                                                                    8Ъ
                                                                                            8c
                                                                                                   84
                                                                                                                  8f
                                                                             8a
                                                                                                           8e
   90
                  92
                                        95
                         93
                                                96
                                                       97
                                                               98
          91
                                 94
                                                                      99
                                                                             9a
                                                                                    9Ъ
                                                                                            9c
                                                                                                   94
                                                                                                           9e
                                                                                                                  9£
                  а2
                                        a5
   aO
           a1
                          ങ
                                 а4
                                                аб
                                                       a7
                                                               а8
                                                                      a9
                                                                             aa
                                                                                     ab
                                                                                            ac
                                                                                                   ad
                                                                                                                  af
   ЪО
           Ъ1
                  Ъ2
                         ЪЗ
                                 Ъ4
                                        Ъ5
                                                Ъ6
                                                       Ъ7
                                                               Ъ8
                                                                      Ъ9
                                                                                                                  Ъf
                                                                             ba
                                                                                    ЪЪ
                                                                                            Ъс
                                                                                                   Ъd
                                                                                                           Ъe
                  c2
                                        c5
                                                с6
   c0
           c1
                          с3
                                 c4
                                                       c7
                                                               с8
                                                                      с9
                                                                                     cb
                                                                                                   cd
                                                                                                                  cf
                                                                             ca
                                                                                            cc
                                                                                                           ce
                  ď2
   ďΟ
           d1
                          dЗ
                                        d5
                                                d6
                                                       d7
                                                               d8
                                                                      d9
                                                                                                                  df
                                 d4
                                                                             da
                                                                                     dЪ
                                                                                            dc
                                                                                                   dd
                                                                                                           de
   e0
                                        e5
f5
                  e2
                                                e6
           e1
                          eЗ
                                 e4
                                                       e7
                                                               е8
                                                                      е9
                                                                             ea
                                                                                     eb
                                                                                            ec
                                                                                                   ed
                                                                                                                  ef
           f1
                  f2
                                                       £7
                                                                                                                  ff
   f0
                          £3
                                 £4
                                                f6
                                                               £8
                                                                      f9
                                                                             fa
                                                                                     fЪ
                                                                                            fc
                                                                                                   fd
                                                                                                           fе
```

5.15. EXMC_TouchScreen

5.15.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

Learn to use EXMC control LCD

5.15.2. **DEMO** running result

GD32403Z-EVAL-V1.1 board has EXMC module to control LCD. Before running the demo, JP12 must be fitted to the SPI port, P2 and P3 must be fitted to the EXMC port. Download the program <15_EXMC_TouchScreen> to the EVAL board. This demo displays GigaDevice logo and four green buttons on the LCD screen by EXMC module.



Users can touch the green button to turn on the corresponding LED on board, and then the color of button you had touched will change to red.



5.16. SDIO_SDCardTest

5.16.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use SDIO to single block or multiple block write and read
- Learn to use SDIO to erase, lock and unlock a SD card

GD32403Z-EVAL board has a secure digital input/output interface (SDIO) which defines the SD/SD I/O /MMC CE-ATA card host interface. This demo will show how to use SDIO to operate on SD card.

5.16.2. **DEMO** running result

Jump the JP5 to USART to show the print message through HyperTerminal, and download the program <16_SDIO_SDCardTest> to the EVAL board and run. Connect serial cable to EVAL_COM0, open the HyperTerminal. Firstly, all the LEDs flash once for test. Then initialize the card and print out the information of the card. After that, test the function of single block operation, lock and unlock operation, erase operation and multiple blocks operation. If any error occurs, print the error message and turn on LED2, LED4 and turn off LED3 and LED5. Otherwise, turn on all the LEDs.



Uncomment the macro DATA_PRINT to print out the data and display them through HyperTerminal. Set bus mode(1-bit or 4-bit) and data transfer mode(polling mode or DMA mode) by comment and uncomment the related statements.

Information via a serial port output as following.

```
Card init success!
Card information:
## Card version 3.0x ##
## SDHC card ##
## Device size is 7782400KB ##
## Block size is 512B ##
## Block count is 15564800 ##
## CardCommandClasses is: 5b5 ##
## Block operation supported ##
## Erase supported ##
## Lock unlock supported ##
## Application specific supported ##
## Switch function supported ##
 Card test:
 Block write success!
 Block read success!
 The card is locked!
 Erase failed!
 The card is unlocked!
 Erase success!
 Block read success!
 Multiple block write success!
 Multiple block read success!
```

5.17. CAN_Network

5.17.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use the CAN0 communication between two boards

GD32403Z-EVAL development board integrates the CAN(Controller Area Network) bus controller, which is a common industrial control bus. CAN bus controller follows the CAN bus protocol of 2.0 A and 2.0 B. This demo mainly shows how to communicate two EVAL boards through CAN0.

5.17.2. DEMO running result

This example is tested with two GD32F403Z-EVAL boards. Jump the JP5 and JP6 to USART and P2, P3 to CAN with the jumper cap. Connect L pin to L pin and H pin to H pin of JP14 on the boards for sending and receiving frames. Download the program <17_CAN_Network> to the two EVAL boards, and connect serial cable to EVAL_COM0. Firstly, the EVAL_COM0 sends "please press the Tamper key to transmit data!" to the HyperTerminal. The frames are sent and the transmit data are printed by pressing Tamper Key push button. When the frames are received, the receive data will be printed and the LED2 will toggle one time.

The output information via the serial port is as following.



please press the Tamper key to transmit data! CANO transmit data: ab,cd CANO recive data: ab,cd

5.18. RCU Clock Out

5.18.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use GPIO control the LED
- Learn to use the clock output function of RCU
- Learn to communicate with PC by USART

5.18.2. DEMO running result

Jump the JP5 and JP6 to USART with the jumper cap, and download the program <18_RCU_Clock_Out> to the EVAL board and run. Connect serial cable to COM0, open the HyperTerminal. When the program is running, HyperTerminal will display the initial information. Then user can choose the type of the output clock by pressing the TAMPER button. After pressing, the corresponding LED will be turned on and HyperTerminal will display which mode be selected. The frequency of the output clock can be observed through the oscilloscope by PA8 pin.

Information via a serial port output as following:

/======= Gigadevice Clock output Demo ========/
press tamper key to select clock output source
CK_OUTO: system clock
CK_OUTO: IRC8M
CK_OUTO: HXTAL
CK_OUTO: system clock

5.19. CTC Calibration

5.19.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use external low speed crystal oscillator (LXTAL) to implement the CTC calibration function
- Learn to use clock trim controller (CTC) to trim internal 48MHz RC oscillator (IRC48M) clock

The CTC unit trim the frequency of the IRC48M based on an external accurate reference signal source. It can automatically adjust the trim value to provide a precise IRC48M



clock.

5.19.2. **DEMO** running result

Download the program <19_CTC_Calibration> to the EVAL board and run. Firstly, all the LEDs flash once for test. Then if the clock trim is OK, LED2 will be on. Otherwise, all the LEDs are turned off.

5.20. PMU_sleep_wakeup

5.20.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

■ Learn to use the USART receive interrupt to wake up the PMU from sleep mode

5.20.2. **DEMO** running result

Download the program < 20_PMU_sleep_wakeup > to the EVAL board, jump the JP5 and JP6 to USART with the jumper cap and connect serial cable to EVAL_COM0. After power-on, all the LEDs are off. The mcu will enter sleep mode and the software stop running. When the USART0 receives a byte of data from the HyperTerminal, the mcu will wake up from a receive interrupt. And all the LEDs will flash together.

5.21. RTC Calendar

5.21.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use RTC module to implement calendar function
- Learn to use USART module to implement time display

5.21.2. DEMO running result

Jump the JP5 and JP6 to USART with the jumper cap, and download the program <21_RTC_Calendar> to the EVAL board and run. Connect serial cable to EVAL_COM0, open the HyperTerminal. After start-up, the program will ask to set the time on the HyperTerminal. The calendar will be displayed on the HyperTerminal.



```
This is a RTC demo.....
This is a RTC demo!
 RTC not yet configured....
 RTC configured...
            ==Time Settings=====
 Please Set Hours: 0
 Please Set Minutes: 0
 Please Set Seconds: O Time: 00:00:00
 Time: 00:00:00
 Time: 00:00:01
 Time: 00:00:02
Time: 00:00:03
 Time: 00:00:04
      00:00:05
 Time:
Time: 00:00:06
 Time: 00:00:07
 Time: 00:00:08
 Time: 00:00:09
 Time: 00:00:10
```

5.22. TIMER Breath LED

5.22.1. DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use Timer output PWM wave
- Learn to update channel value

5.22.2. DEMO running result

Use the DuPont line to connect the TIMER0_CH0 (PA8) and LED2 (PF0), and then download the program <22_TIMER_Breath_LED> to the GD32403Z-EVAL-V1.1 board and run. PA8 should not be reused by other peripherals.

When the program is running, you can see LED2 lighting from dark to bright gradually and then gradually darken, ad infinitum, just like breathing as rhythm.

5.23. USB_Device

5.23.1. HID_Keyboard

DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn how to use the USBFS peripheral mode
- Learn how to implement USB HID(human interface) device

GD32403Z-EVAL-V1.1 board has five keys and one USB_FS interface. The five keys are Reset key, Wakeup key, Tamper key, User key1 and User key2. In this demo, the GD32403Z-EVAL-V1.1 board is enumerated as an USB Keyboard, which uses the native PC Host HID driver, as shown below. The USB Keyboard uses three keys(wakeup key,



tamper key and user key1) to output three characters ('b', 'a' and 'c'). In addition, the demo also supports remote wakeup which is the ability of a USB device to bring a suspended bus back to the active condition, and the wakeup key is used as the remote wakeup source.



DEMO running result

Before running the demo, please ensure that jumper JP5, JP6 jump to OTG. After doing this, download the program <23_USBFS\USB_Device\HID_Keyboard> to the EVAL board and run. If you press the Wakeup key, will output 'b'. If you press the User key, will output 'c'. If you press the Tamper key, will output 'a'.

If you want to test USB remote wakeup function, you can do as follows:

- Manually switch PC to standby mode
- Wait for PC to fully enter the standby mode
- Push the Wakeup key
- If PC is ON, remote wakeup is OK, else failed.

5.23.2. MSC_Udisk

DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn how to use the USB_FS peripheral mode
- Learn how to implement USB MSC(mass storage) device

This demo mainly implements a U disk. U disk is currently very widely used removable MSC devices. MSC, the Mass Storage device Class, is a transport protocol between a computer and mobile devices, which allow a universal serial bus (USB) equipment to access a host computing device, file transfer between them, mainly including mobile hard disk, mobile U disk drive, etc. The MSC device must have a storage medium, and this Demo uses the MCU's internal flash as the storage medium. For more details of the MSC protocol please refer to the MSC protocol standard.

MSC device will use a variety of transport protocols and command formats for communication, so it need to choose the appropriate protocol and command format in the realization of the application. This Demo selects the BOT (bulk only transport)

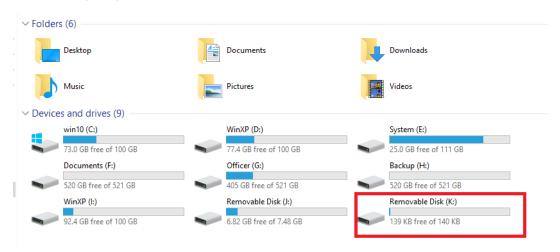


protocol and the required SCSI (small computer interface) command, and is compatible with a wide variety of Window operating systems. Specific BOT protocol and SCSI command specification please refer to the standard of their agreement.

DEMO running result

Before running the demo, please ensure that jumper JP5, JP6 jump to OTG. After doing this, download the program <23_USBFS\USB_Device\MSC_Udisk> to the EVAL board and run. When the EV-board connect to the PC, you will find a USB large capacity storage device is in the universal serial bus controller, and there is 1 more disk drives in the equipment manager of PC.

Then, after opening the resource manager, you will see more of the 1 disk, as shown in the following diagram:



At this point, the write/read/formatting operation can be performed as the other mobile devices.

5.24. USB_Host

5.24.1. **HID_Host**

DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the USBFS as a HID host
- Learn the operation between the HID host and the mouse device
- Learn the operation between the HID host and the keyboard device

GD32403Z-EVAL-V1.1 board integrates the USBFS module, and the module can be used as a USB device, a USB host or an OTG device. This demo mainly shows how to use the USBFS as a USB HID host to communicate with external USB HID device.

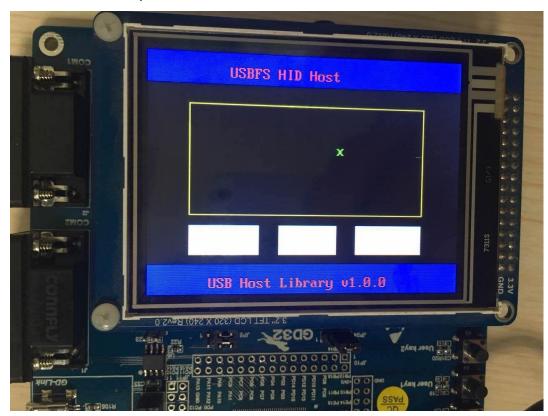
DEMO running result

Jump the JP5 and the JP6 to OTG. Then download the program



<23_USBFS\USB_Host\HID_Host> to the EVAL board and run.

If a mouse has been attached, the user will see the information of mouse enumeration. First pressing the user key1 will see the inserted device is mouse, and then moving the mouse will show the position of mouse and the state of button in the screen.



If a keyboard has been attached, the user will see the information of keyboard enumeration. First pressing the user key1 will see the inserted device is keyboard, and then pressing the keyboard will show the state of the button in the screen.





5.24.2. MSC_Host

DEMO purpose

This demo includes the following functions of GD32 MCU:

- Learn to use the USBFS as a MSC host
- Learn the operation between the MSC host and the Udisk

GD32403Z-EVAL-V1.1 board integrates the USBFS module, and the module can be used as a USB device, a USB host or an OTG device. This demo mainly shows how to use the USBFS as a USB MSC host to communicate with external Udisk.

DEMO running result

Jump the JP5 and the JP6 to OTG. Then insert the OTG cable to the USB port, download the program <23_USBFS\USB_Host\MSC_Host > to the EVAL board and run.

If an Udisk has been attached, the user will see the information of Udisk enumeration. First pressing the user key1 will see the Udisk information, next pressing the tamper key will see the root content of the Udisk, then press the wakeup key will write file to the Udisk, finally the user will see information that the msc host demo is end.



6. Revision history

Table 6-1 Revision history

Revision No.	Description	Date
1.0	Initial Release	May.19, 2017
2.0	Update EVAL board	Jan.07,2019



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