



# Master's Thesis

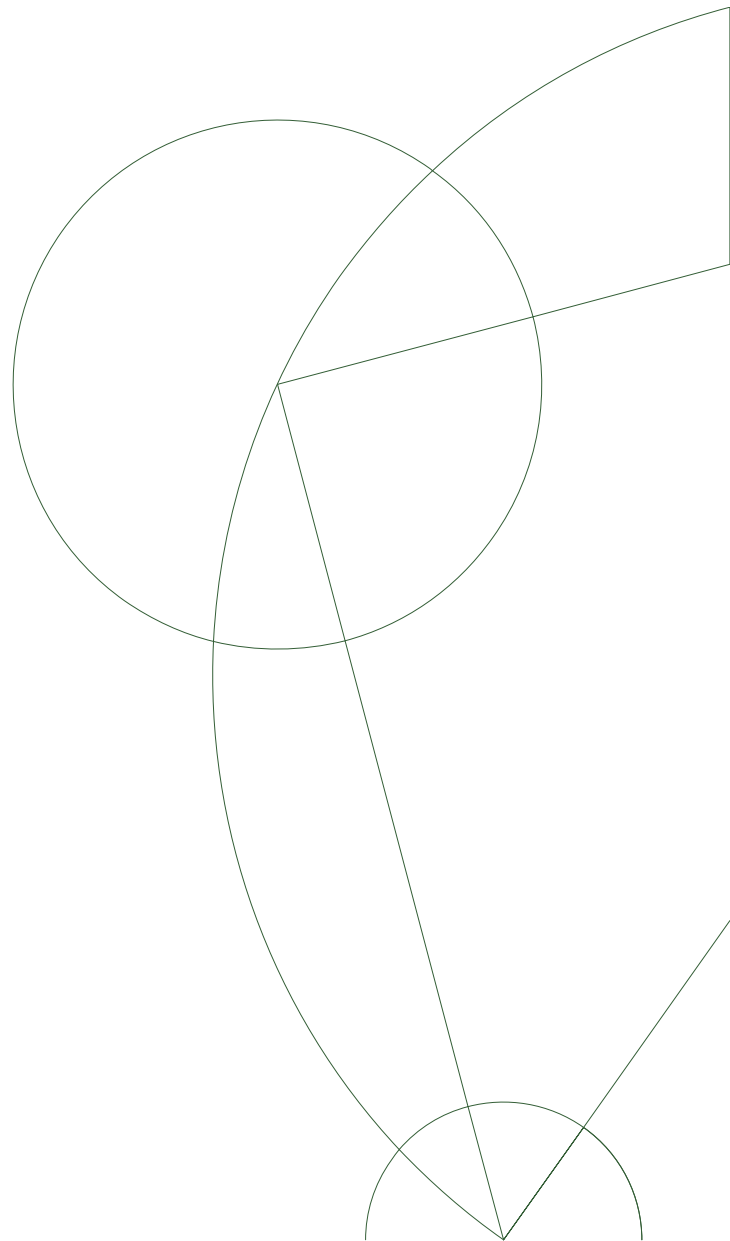
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## Towards formal verification of FDR4

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## **Abstract**

Bla bla bla bla

# Contents

<b>1</b>	<b>Introduction</b>	<b>1</b>
1.1	Learning goals . . . . .	1
<b>2</b>	<b>Related work</b>	<b>2</b>
<b>3</b>	<b>Theory</b>	<b>4</b>
3.1	Hoare's logic . . . . .	4
<b>4</b>	<b>Method</b>	<b>5</b>
<b>5</b>	<b>Results and tests (Experiment?)</b>	<b>6</b>
<b>6</b>	<b>Discussion</b>	<b>7</b>
<b>7</b>	<b>Conclusion</b>	<b>8</b>
7.1	Future work . . . . .	8
7.2	Appendix . . . . .	10

# Todo list

Figure out if Hoare used this information/update in his work with CSP. I am not sure if CSP work on Hoare logic? . . . . .	2
download file and make citation - I have not been able to find a free version of this paper . . . . .	2
Citation to Communicating Sequential Processes: The first 25 years . . .	2
Figure out the precise difference between refinement and model checking .	3
Look at the site on ProB I found - under Thesis bookmarks . . . . .	3

# Chapter 1

## Introduction

When we create programs, we wish to verify that it is also correct. There are several ways to do this, one commonly used is **testing** which require that the programmer creates several different scenarios and its expected output, or that the programmer programs a test-generator to create the scenarios and expected output. This, however, is not adequate for (word for important systems). Therefore it is of high interest to create a verification of the system or program.

Talk about how verification was first created and how it became to be used for concurrent systems. Then write about how it works and then write about the different systems and formal languages that is used for it.

In this thesis we look at model checking, that is, verifying that a specific property will always hold for a piece of code.

Formal verification is the process of checking whether a program satisfies specific properties. Different methods have evolved, all having different advantages and disadvantages. FDR is sometimes referred to as a model checker however is it actually a refinement checker.

Matematicians tend to reject proofs by exhaustive checking of all cases as being less satisfying than deductive proofs, and with good reason. First, they are not applicable for proving theorems about integers and real numbers, which are infinite domains so that the number of interpretations is infinite and they cannot be exhaustively checked. Second, they offer no insight into why a theorem is true. But computer scientists have more practical concerns. If they can check all computations of a program and show that they all satisfy a correctness property, we will be willing to forego elegance and be more than satisfied that our program has been proven correct. (from "A primer on model checking af Ben-Ari [1]

### 1.1 Learning goals

This is where the learning goals go.

## Chapter 2

# Related work

In this chapter we will discuss previous work that has lead formal verification to what it is today. We will also discuss different tools and languages that are used today and the differences between them.

In 1967 Robert W. Floyd was published with his paper *Assigning meaning to programs*[4]. In his paper he provide a basis for formal definitions of the meaning of programs which can be used for proving correctness, equivalence and termination. He uses flowcharts to argue that when a command is reached all previous commands will have been true as well.

C.A.R Hoare was inspired by Floyd and in 1969 his paper *An axiomatic basis for computer programming*[5] was published. With his *Hoare's logic*, he builds on Floyd's ideas and proposed that program could be viewed as a partial correctness relation between a precondition and a postcondition predicate. This means that if the state the program starts in satisfies the precondition and it terminates, then the final state satisfies the postcondition. L. Lamport expended Hoare's logic to concurrent programs in his paper *The 'Hoare logic' of concurrent programs*[8]. He argues why Hoare's logic as proposed by C.A.R Hoare does not work for concurrent programs and proposes a "generalized Hoare's logic" that takes concurrency into account.

In 1972 his paper *Towards a Theory of Parallel Programming* was published and in 1978 his paper *Communicating Sequential Processes* was published. CSP was born and have been widely used and have also been expanded since Hoare initially described it in 1978. The first version of CSP was mostly a concurrent programming language and later, Brookes, Hoare and Roscoe[2] continued the work on CSP and created the modern process algebra it is today, only a few minor changes have been made to CSP since then, and they are described in Roscoe's *The Theory and Practice of Concurrency*[12]

A number of tools have been created to analyse, verify and understand CSP written systems. In order to use these tools along with CSP, different types of machine-readable CSP syntaxes have been created over the years, but most of today's CSP tools use a version of machine-readable CSP called CSP<sub>M</sub> which was created by Scattergood[13]. Since CSP was mostly a blackboard language,

Figure out if Hoare used this information/update in his work with CSP. I am not sure if CSP work on Hoare logic?

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Citation to Communicating

Scattergood created a combination of the standard CSP and a functional programming language which created a better baseline for tools to work with CSP. One of the most known CSP tool is the Failure-Divergence Refinement (FDR), build by Formal Systems (Europe) Ltd., which is currently at version 4.2.3[14]. FDR is a refinement checker which differs from a lot of other CSP tools that are merely model checkers. .

The Adelaide Refinement Checker (ARC)[9] is a automatic verification tool for untimed CSP. It represents the internal representation by using Ordered Binary Decision Diagrams (OBDDs). This lessen the state explosion problem that other model checkers have with LTS representations.

The ProB project

The programming language Occam, which was first released in 1983, is a concurrent programming language that builds on the Communicating Sequential Processes process algebra. Occam developed over the years and the Kent Retargetable occam Compiler (KRoC) team at Kent University created the Occam- $\pi$  variant of the occam programming language. It is a version that extends the idas of CSP in the original occam language but adding mobility features from pi-calculus. On the KRoC webpage they describe the reason to include functionality from pi-calculus; *"Specifically, we want to allow networks of processes to evolve, to change their topologies, to cope with growth and decay without losing semantic or structural integrity. We want to address the mobility of processes, channels and data and understand the relationships between these ideas. We want to retain the ability to reason about such systems, preserving the concept of refinement."*<sup>1</sup>

Figure out the precise difference between refinement and model checking

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SPIN is a verification system that uses process interactions to prove correctness for a system. The system is described in the formal language PROMELA (PROcess MEta LAnguage)[6] and the correctness properties are spcified in Linear Temporal Logic (LTL)[10]. In the paper *Reasoning About Infinite Computations*[16], Vardi and Wolper showed that all LTL formulas can be translated into a Büchi automata which SPIN makes use of and thus converting the given LTL into a Büchi automaton.

Spin was developed at Bell Labs, starting in 1980. Since 1991 it has been freely available and today it is used by thousands of people worldwide.

Gerard J. Holzmann gives an introduction to the theoretical foundations, the design and structure and examples of applications in the paper *The model checker SPIN*[7]. SPIN was build on the pioneering work on logic model checking by Clarke and Emerson[3], as well as Sifakis and Queille[11]. Vardi and Wolper extended their work with an automata-theoretic approach to automatically verify programs[15].

<sup>1</sup><https://www.cs.kent.ac.uk/projects/ofa/kroc/> access date: 3/4/18

## Chapter 3

# Theory

**This is where the theory go. fx. SME and the correlation between that and CSP.** CSPm was devised by Bryan Scattergood as a machine-readable dialect of CSP - se the paper *The Semantics and Implementation of Machine-Readable CSP*

” FDR2 is often described as a model checker, but is technically a refinement checker, in that it converts two CSP process expressions into Labelled Transition Systems (LTSs), and then determines whether one of the processes is a refinement of the other within some specified semantic model (traces, failures, or failures/divergence)” (from Wikipedia - se paper *Model-checking CSP - af Roscoe*)

### 3.1 Hoare’s logic



## Chapter 4

# Method

This is the method section that describes what I did, how and why.

## Chapter 5

# Results and tests (Experiment?)

Does it work? why, why not

## Chapter 6

# Discussion

## Chapter 7

# Conclusion

### 7.1 Future work

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## **7.2 Appendix**