



Faculty of Science



# Towards Automatic Program Specification Using SME Models

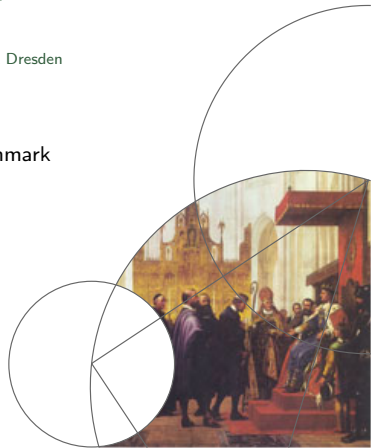
Communicating Process Architectures 2018 – Technische Universität Dresden

Alberte Thegler

Niels Bohr Institute, University of Copenhagen, Denmark

21 August 2018

Slide 1/3



# Table of Contents

- 1 Why should we verify hardware?
- 2 What can SME do?
- 3 Simple example
- 4 CSPm process structure
- 5 Monitor process
- 6 Example continued
- 7 Results - time to verify in FDR4?
- 8 Conclusion
- 9 Future work



# Why should we verify hardware?

Somebody else has already written a faster method than you could ever do.



# Why should we verify hardware?

Somebody else has already written a faster method than you could ever do.

An example of such a method is a fast way of multiplying two matrices that comes with the ...



# Why should we verify hardware?

Somebody else has already written a faster method than you could ever do.

An example of such a method is a fast way of multiplying two matrices that comes with the ...

If possible, we always want to use this faster method.

