

		OPCODE	Reg2_Select	PCSrc	MemWrite	MemAddressSrc	MemDataSrc	IForwardable	IN1_Select	Select[1..0]	RegWrite	MemRegister1Access	Register2Access
NOP	0	00000*	0		0						0	0	0
ADD	1	00001*	0		0				0	0	1	0	0
SUB	2	00010*	0		0				0	0	1	0	0
AND	3	00011*	0		0				0	0	1	0	0
OR	4	00100*	0		0				0	0	1	0	0
SLL	5	00101*			0				0		1	0	0
SRL	6	00110*			0				0		1	0	0
MAX	7	00111*	0		0				0	0	1	0	0
SLT	8	01000*	0		0				0	0	1	0	0
MUL	9	01001*			0						0	0	0
MOV	10	01010*	0		0				0		1	0	0
ADDNDR	11	01011*			0						0	0	0
LUI	12	01100*			0					1	1	0	0
ADDI	13	01101*			0				0	1	1	0	0
ORI	14	01110*			0				0	1	1	0	0
SLTI	15	01111*			0				0	1	1	0	0
BEQ	16	10000*	1	0	0						0	0	0
BNEQ	17	10001*	1	0	0						0	0	0
JR	18	10010*	1	1	0						0	0	0
J	19	10011*			0						0	0	0
LW	20	10100*			0	0		1	1		1	0	0
ADDM	21	10101*			0	0			0	10	1	0	0
SW	22	10110*			1	0	0		0		0	1	0
MULH	23	10111*	0		0				0	0	1	0	0
MULL	24	11000*	0		0				0	0	1	0	0
SADDNDR	25	11001*	0		0	1			1	0	1	1	0
LWNDR	26	11010*			0	1		1	1		1	1	0
SWNDR	27	11011*	0		1	1	1			0	0	1	1
	28	11100*			0							0	0
	29	11101*			0							0	0
	30	11110*			0							0	0
	31	11111*			0							0	0