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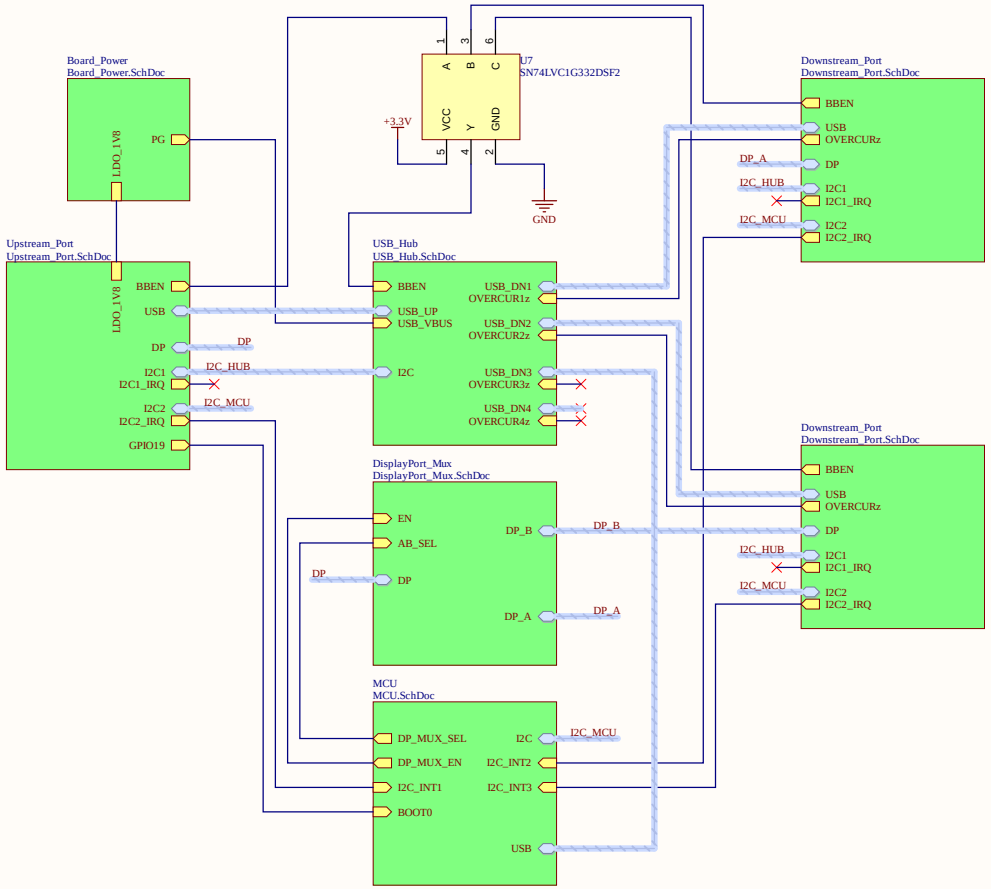
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

| | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT

Altium

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: f2a941d9 | DESIGN ITEM:

TITLE

# USBC Hub

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME Top.SchDoc | SHEET 1 OF 9

<SCH DWG NO>

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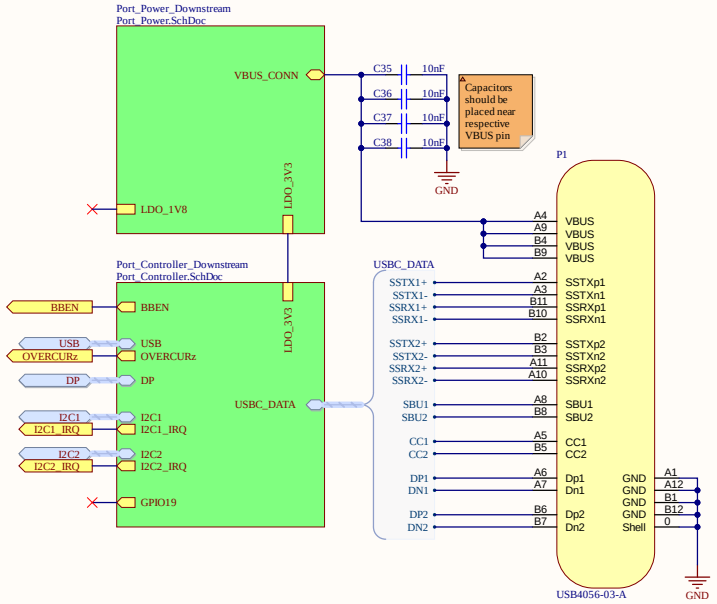
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

| | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: | DESIGN ITEM: 8610c55b

TITLE

# Downstream Port

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME Downstream_Port.SchDoc | SHEET 2 OF 9

<SCH DWG NO> REV 2

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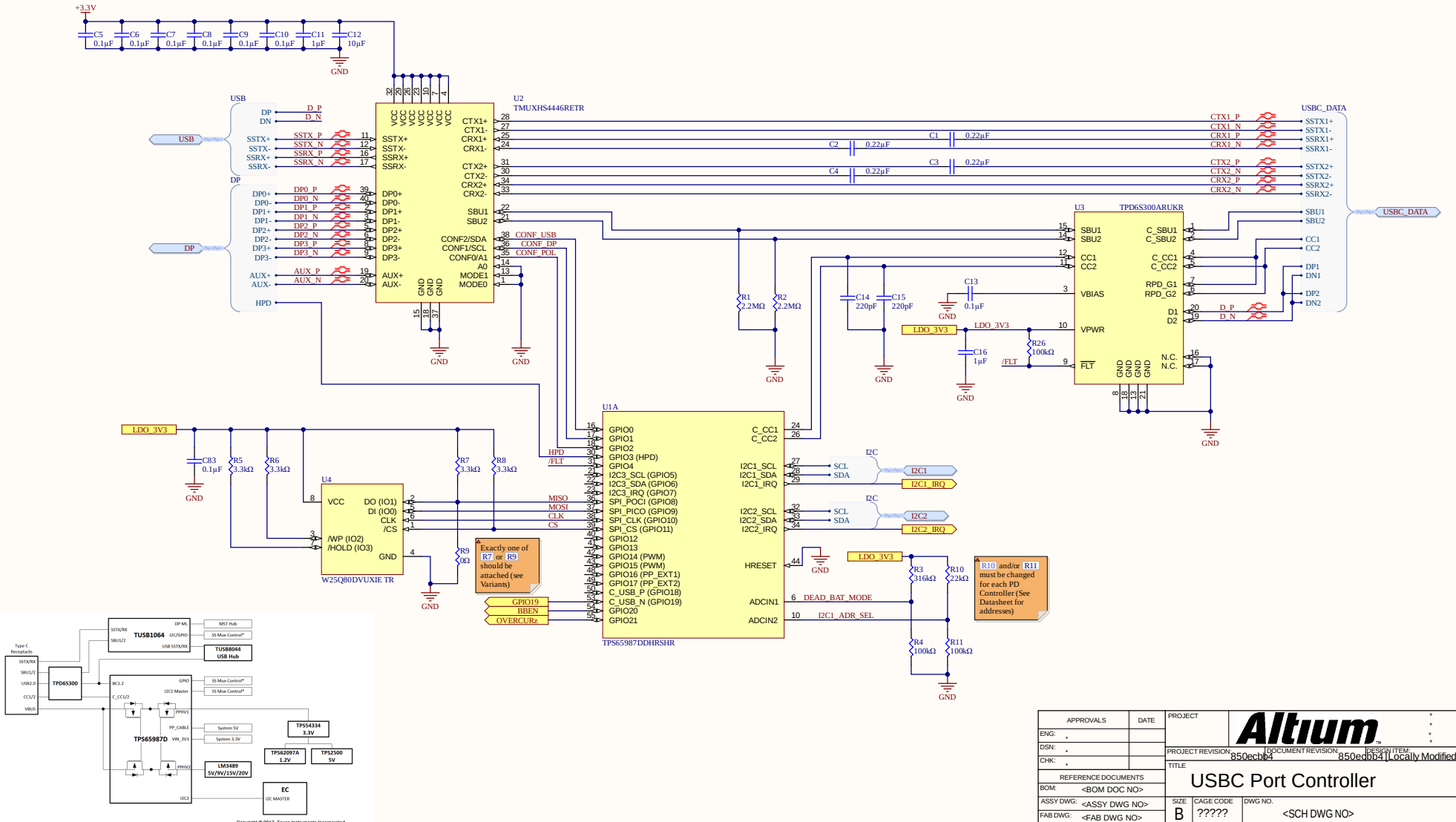
|---|---|---|---|
| | | | |







| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

| | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT: Altium

PROJECT REVISION: 850ecdb4 | DOCUMENT REVISION: | DESIGN ITEM: 850ecdb4 [Locally Modified]

TITLE: USBC Port Controller

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME: Port_Controller.SchDoc | SHEET 3 OF 9

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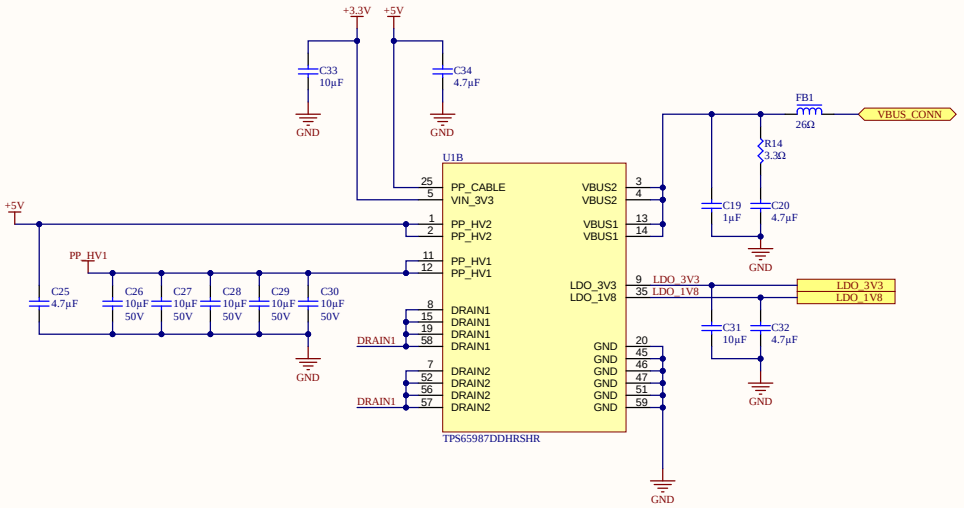
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

| REFERENCE DOCUMENTS | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: f2a941d9 | DESIGN ITEM:

TITLE

# Port Power

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME Port_Power.SchDoc | SHEET 4 OF 9

<SCH DWG NO> DWG. NO. | 4 REV | SHT.

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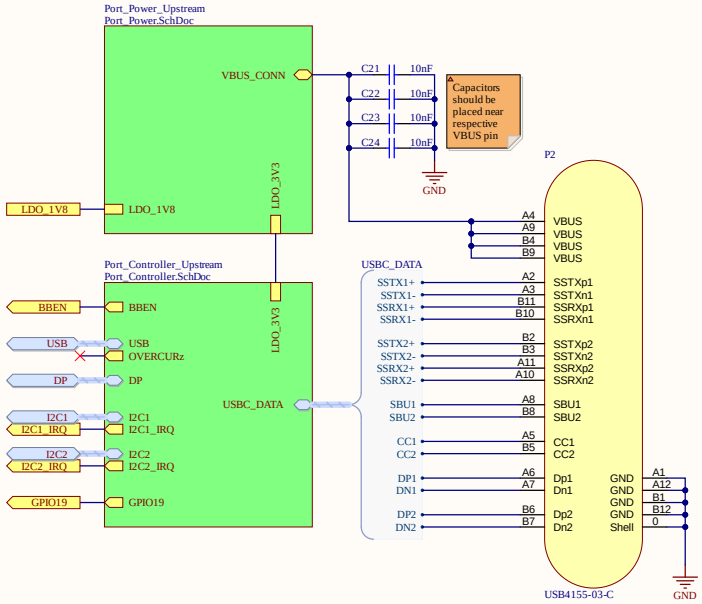
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

| | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: | DESIGN ITEM: 8610c55b

TITLE

# Upstream Port

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME Upstream_Port.SchDoc | SHEET 5 OF 9

<SCH DWG NO> 5

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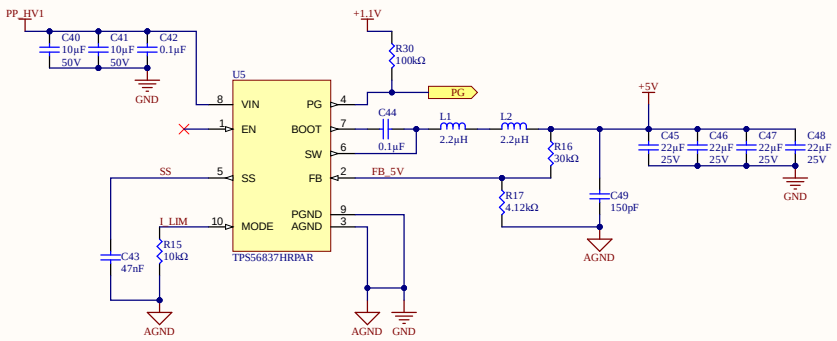
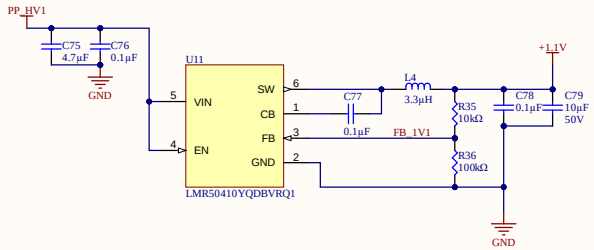
| | | | |

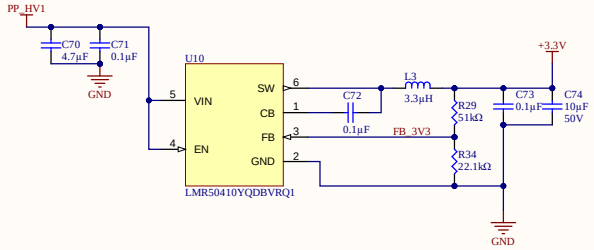
## 5V Buck




## 1.1V Buck




## 3.3V Buck




## 1.8V LDO

1.8V is supplied via integrated LDO in upstream PD Controller, LDO_1V8_3

| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

BOM: <BOM DOC NO>
ASSY DWG: <ASSY DWG NO>
FAB DWG: <FAB DWG NO>
PCB DWG: <PCB DWG NO>

PROJECT

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: f2a94109 | DESIGN ITEM:

TITLE: Board Power

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME: Board_Power.SchDoc | SHEET 6 OF 9

<SCH DWG NO> 6

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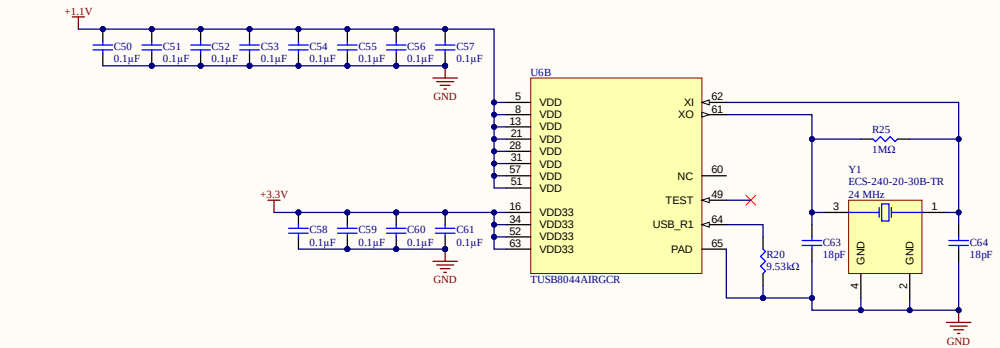
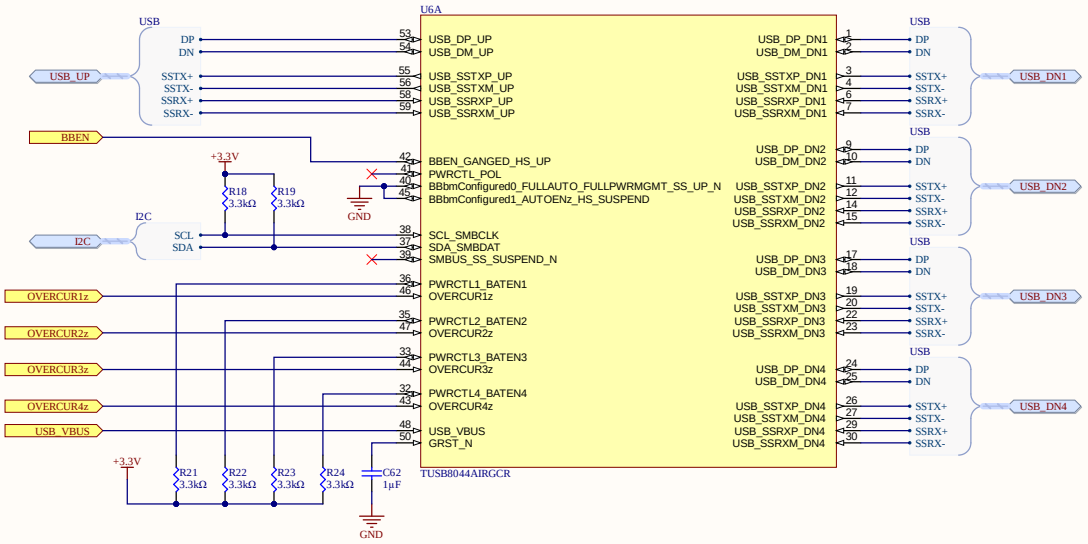
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

BOM: <BOM DOC NO>
ASSY DWG: <ASSY DWG NO>
FAB DWG: <FAB DWG NO>
PCB DWG: <PCB DWG NO>

PROJECT

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: | DESIGN ITEM: 3905d703

TITLE

# USB Hub

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME USB_Hub.SchDoc | SHEET 7 OF 9

<SCH DWG NO> 7

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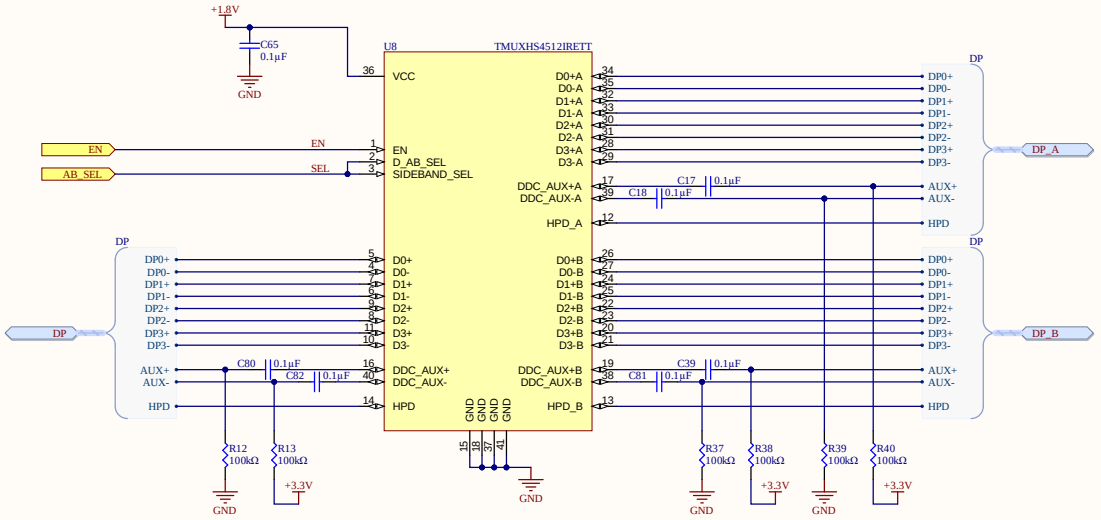
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

| | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT

Altium

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: | DESIGN ITEM: f2a94109

TITLE

# DisplayPort Mux

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME DisplayPort Mux.SchDoc | SHEET 8 OF 9

<SCH DWG NO> 8

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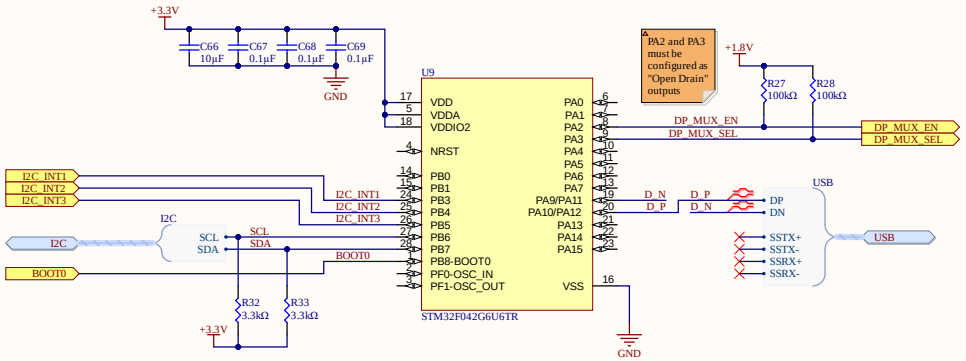
|---|---|---|---|
| | | | |




| APPROVALS | DATE |
|---|---|
| ENG: * | |
| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

| | |
|---|---|
| BOM: | <BOM DOC NO> |
| ASSY DWG: | <ASSY DWG NO> |
| FAB DWG: | <FAB DWG NO> |
| PCB DWG: | <PCB DWG NO> |

PROJECT

PROJECT REVISION: 850ecbb4 | DOCUMENT REVISION: f2a941d9 | DESIGN ITEM:

TITLE: MCU

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME: MCU.SchDoc | SHEET 9 OF 9

<SCH DWG NO> 9