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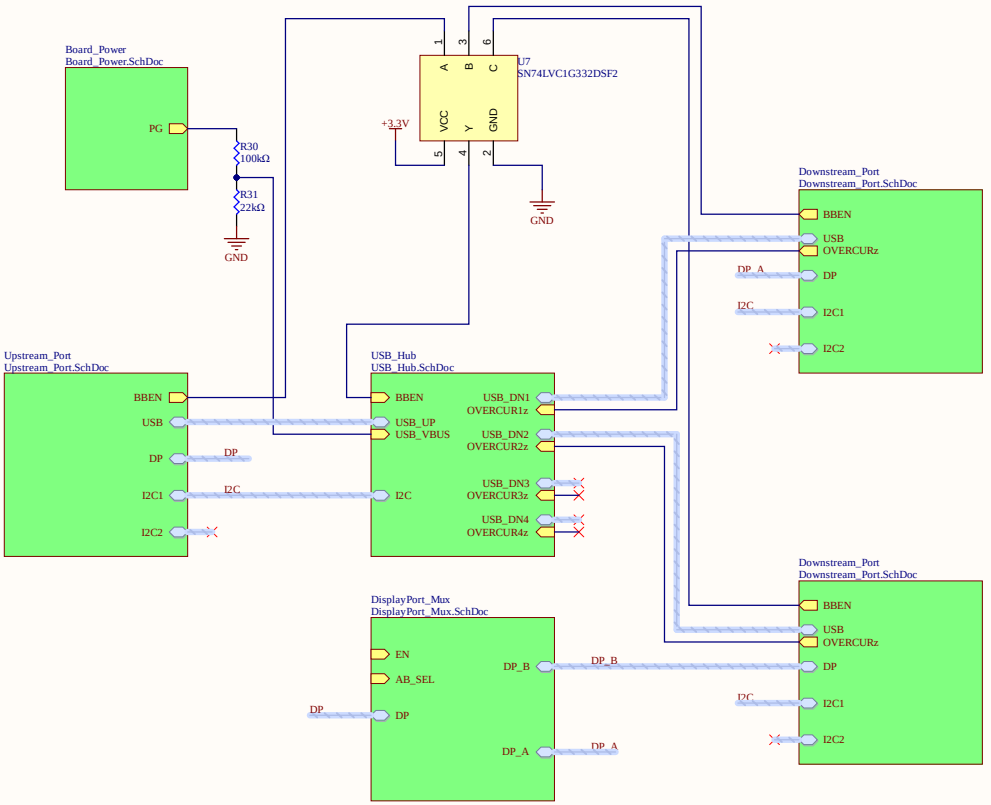

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PROJECT

Altium

PROJECT REVISION: 5ed51104 DOCUMENT REVISION: 5ed51104 DESIGN ITEM:

TITLE

USBC Hub

SIZE	CAGE CODE	DWG NO.	REV
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SCALE: FILE NAME Top.SchDoc SHEET 1 OF 8

<SCH DWG NO> DWG. NO. | REV | 1 SHT.

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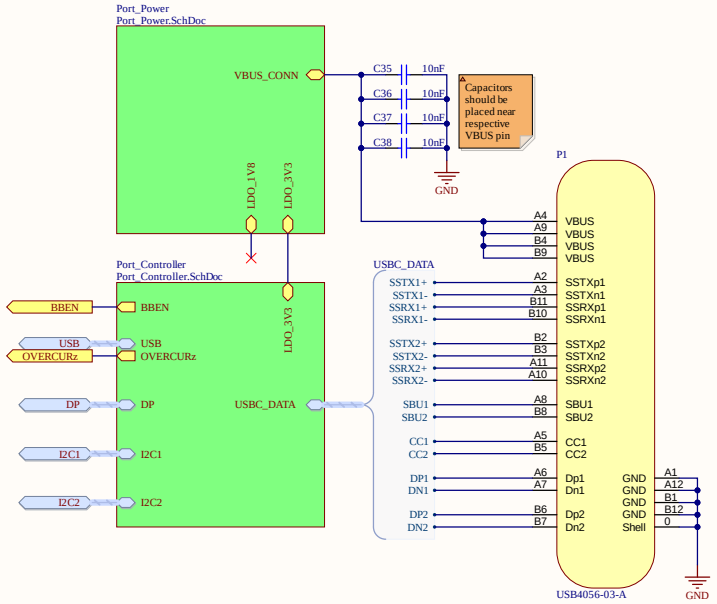

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PROJECT

PROJECT REVISION: 5ed51104 DOCUMENT REVISION: 830a91a8 DESIGN ITEM:

TITLE

Downstream Port

SIZE	CAGE CODE	DWG NO.	REV
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SCALE: FILE NAME Downstream_Port.SchDoc SHEET 2 OF 8

<SCH DWG NO> DWG. NO. REV 2 SHT.

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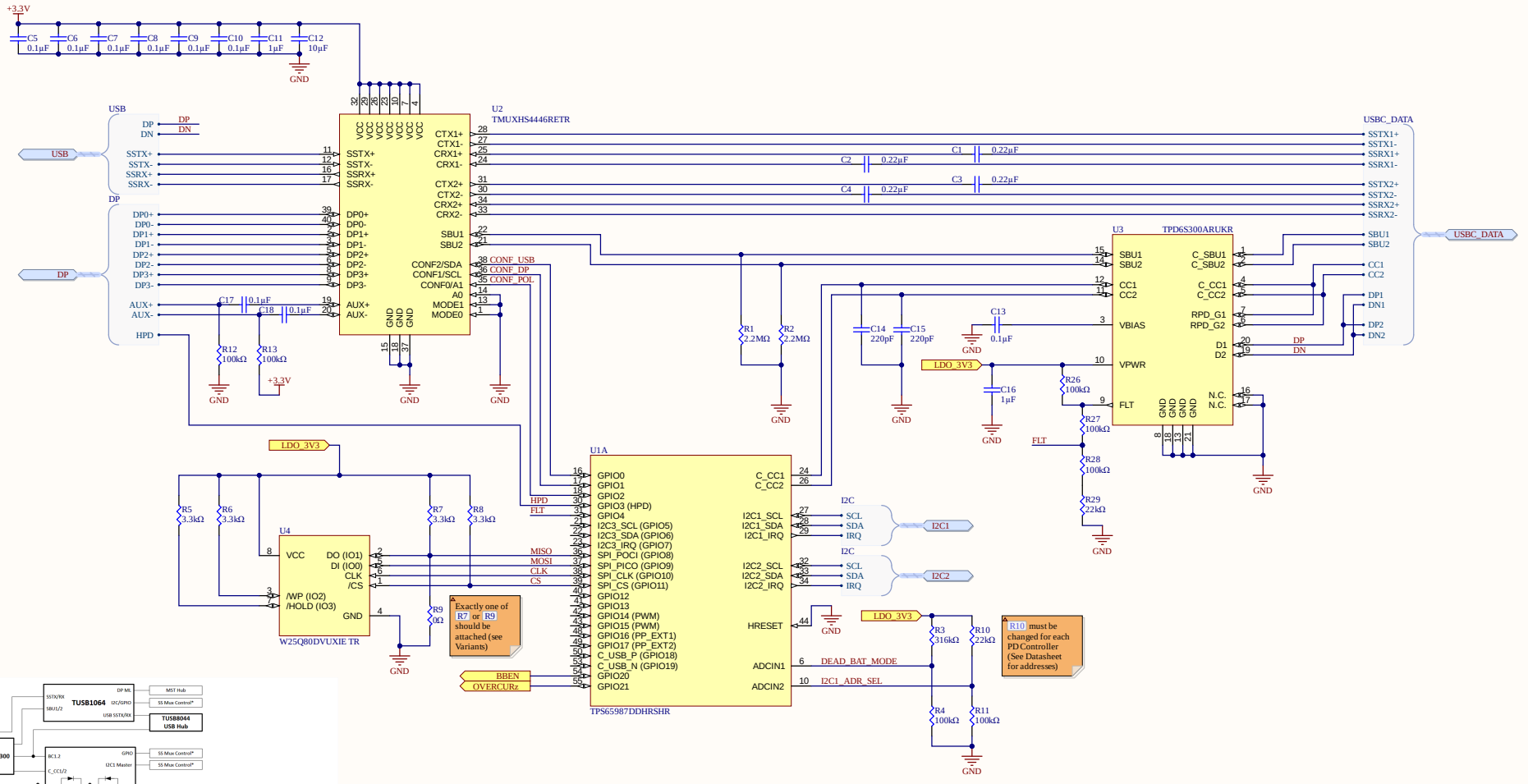

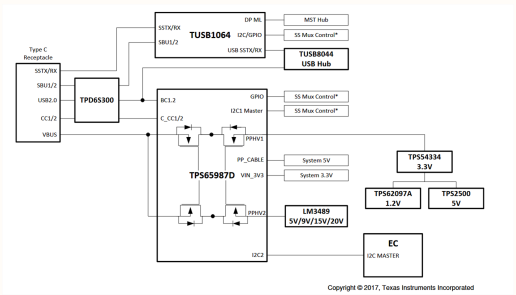

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PROJECT: Altium

PROJECT REVISION: 5ed51104 | DOCUMENT REVISION: adcba55b | DESIGN ITEM:

TITLE: USBC Port Controller

SIZE	CAGE CODE	DWG NO.	REV
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SCALE: | FILE NAME: Port_Controller.SchDoc | SHEET 3 OF 8

<SCH DWG NO> | REV 3

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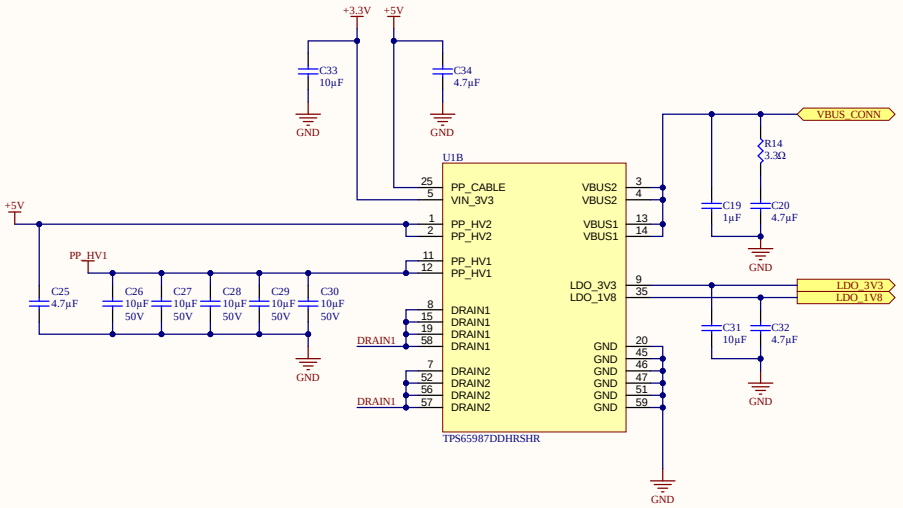

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PROJECT

Altium

PROJECT REVISION: 5ed5110d | DOCUMENT REVISION: a2d2c34a | DESIGN ITEM:

TITLE

Port Power

SIZE	CAGE CODE	DWG NO.	REV
B	?????	<SCH DWG NO>	

SCALE: | FILE NAME Port_Power.SchDoc | SHEET 4 OF 8

<SCH DWG NO> DWG. NO. | 4 REV | SHT.

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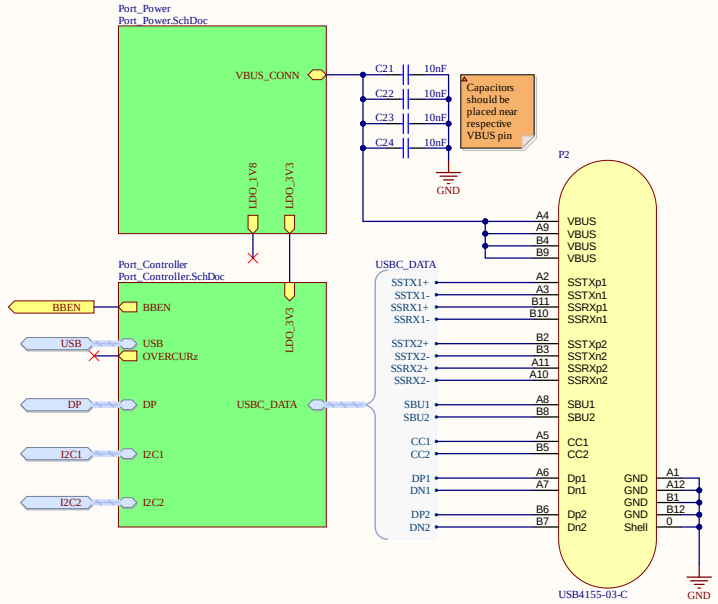

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PROJECT

PROJECT REVISION: 5ed5110d DOCUMENT REVISION: 830a91a8 DESIGN ITEM:

TITLE

Upstream Port

SIZE	CAGE CODE	DWG NO.	REV
B	?????	<SCH DWG NO>	

SCALE: FILE NAME Upstream_Port.SchDoc SHEET 5 OF 8

<SCH DWG NO> 5

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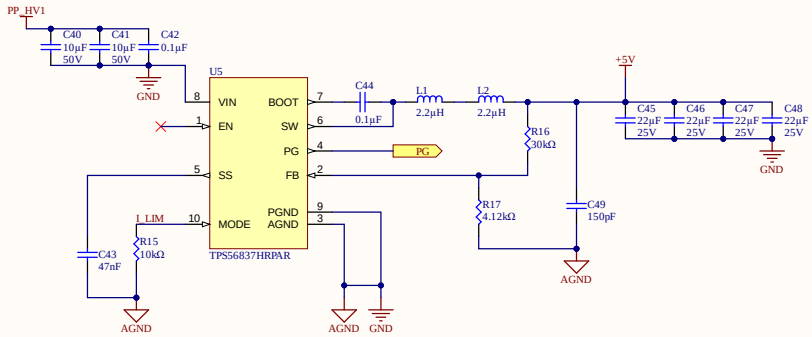

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PROJECT

PROJECT REVISION: 5ed5110d4 DOCUMENT REVISION: adcba55b DESIGN ITEM:

TITLE

Board Power

SIZE	CAGE CODE	DWG NO.	REV
B	?????	<SCH DWG NO>	

SCALE: FILE NAME Board_Power.SchDoc SHEET 6 OF 8

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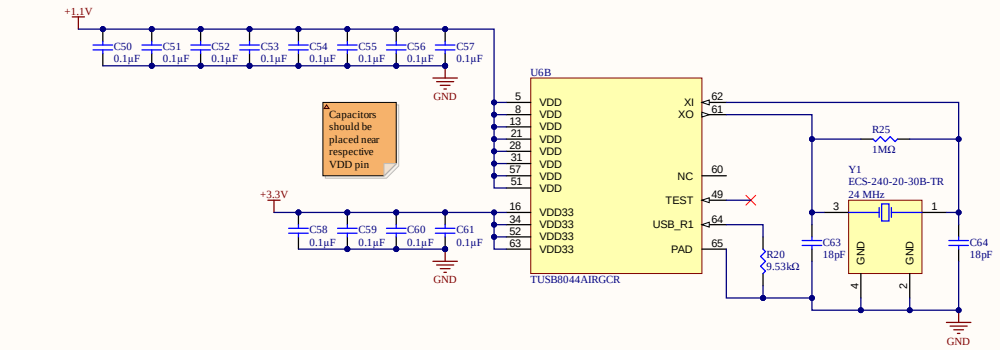

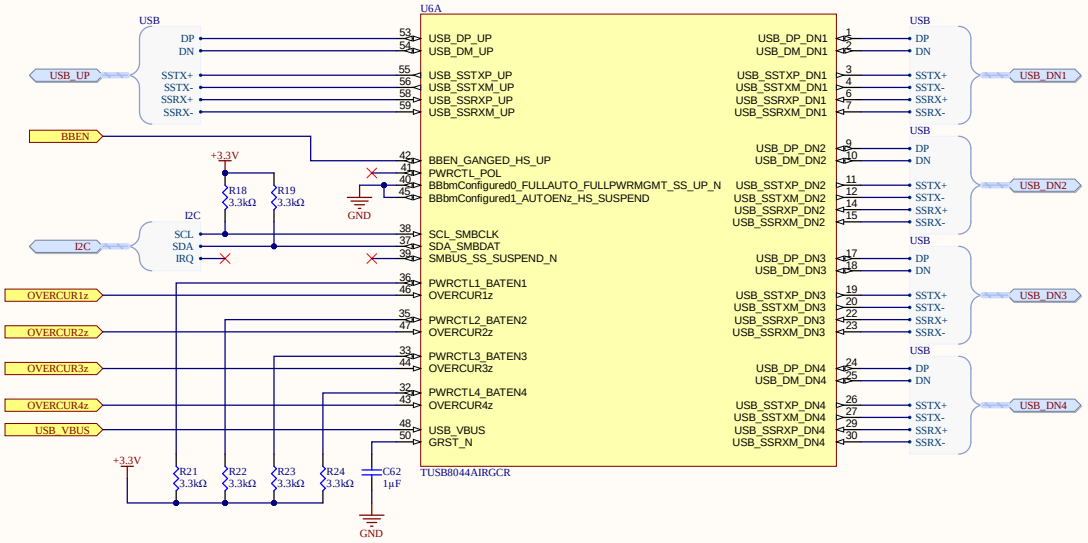

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PROJECT

Altium

PROJECT REVISION: 5ed51104 DOCUMENT REVISION: 0583d3c DESIGN ITEM:

TITLE

USB Hub

SIZE	CAGE CODE	DWG NO.	REV
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SCALE: FILE NAME USB_Hub.SchDoc SHEET 7 OF 8

<SCH DWG NO> 7

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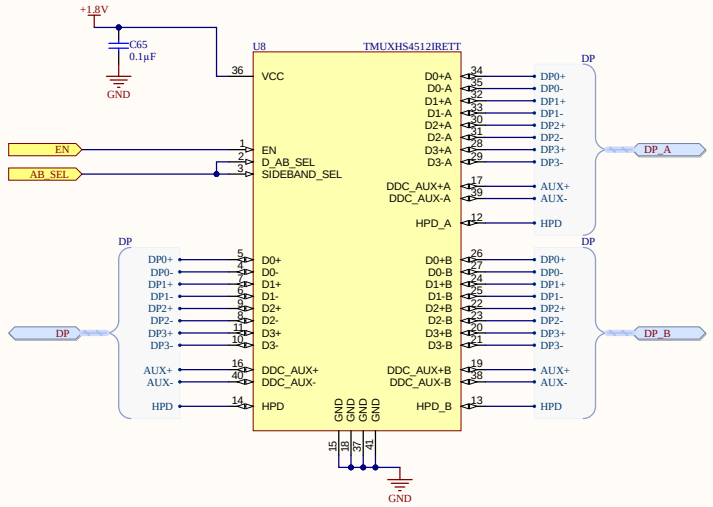

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PROJECT: Altium™

PROJECT REVISION: 5ed51104 | DOCUMENT REVISION: adcba85D [Locally Modified] | DESIGN ITEM:

TITLE: DisplayPort Mux

SIZE	CAGE CODE	DWG NO.	REV
B	?????	<SCH DWG NO>	

SCALE: | FILE NAME: DisplayPort Mux.SchDoc | SHEET 8 OF 8

<SCH DWG NO> DWG. NO. | REV | 8 SHT.