

8 7 6 5 4 3 2 1

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

| <SCH DWG NO> | | REV | 1 |
|--------------|-------------|------|----------|
| REVISION | DESCRIPTION | DATE | APPROVED |

F

E

D

C

B

A

F

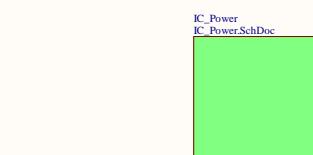
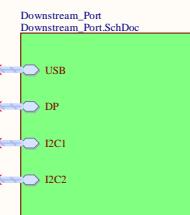
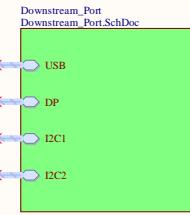
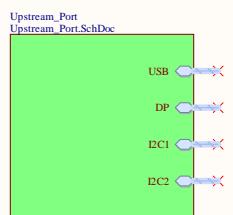
E

D

C

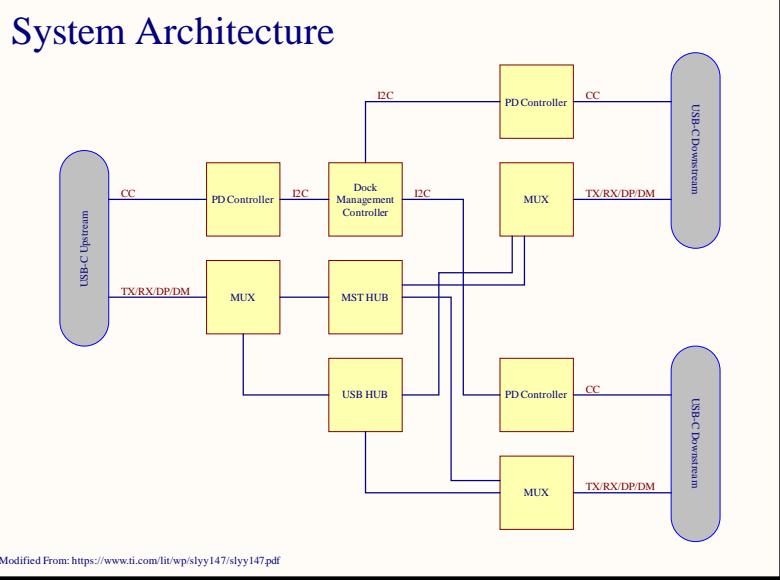
B

A



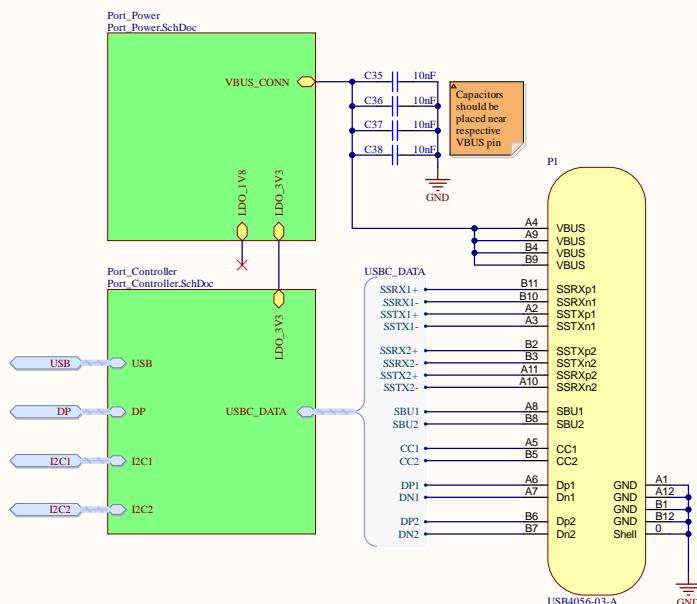
System Architecture

Modified From: <https://www.ti.com/lit/wp/slyy147/slyy147.pdf>



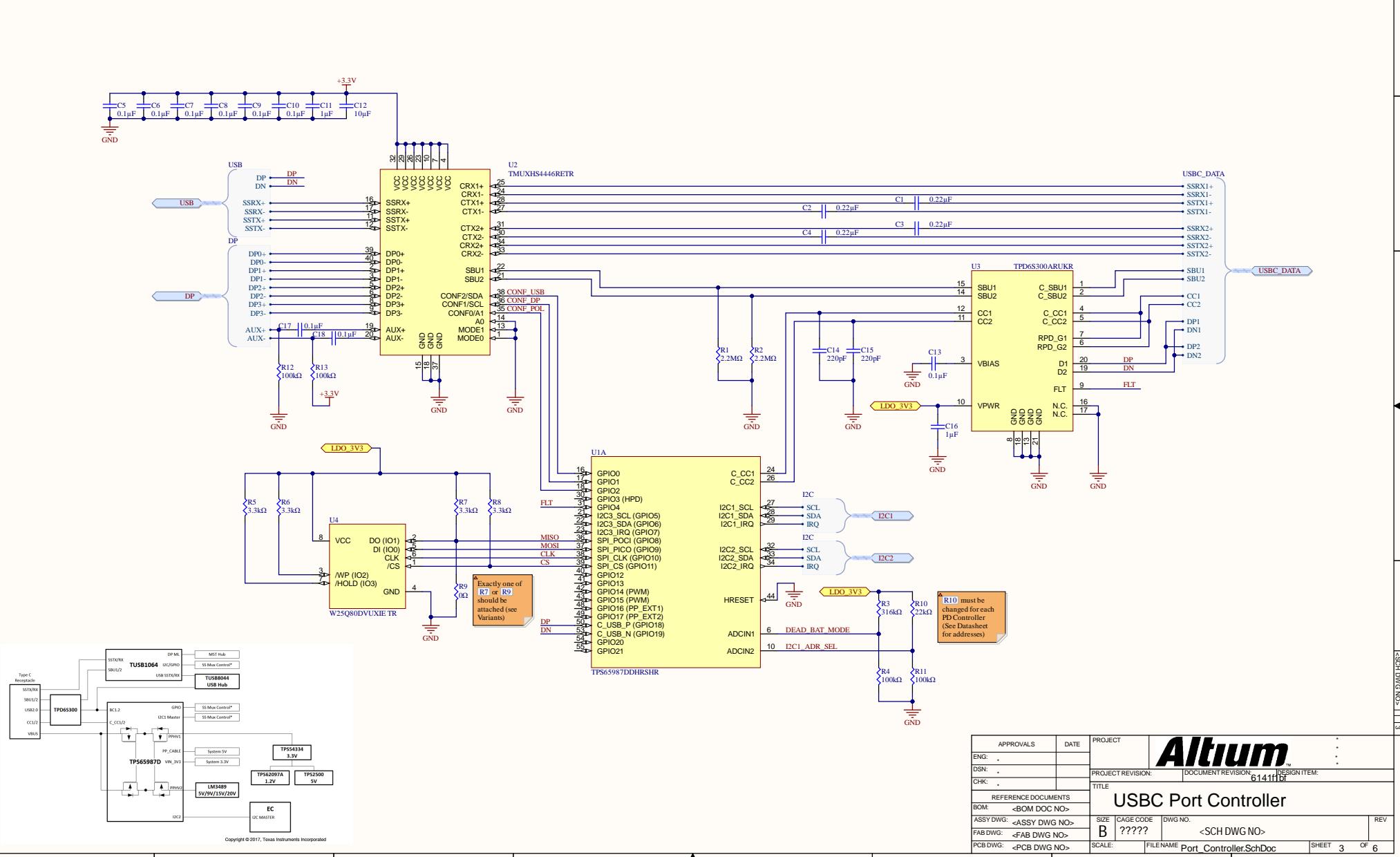
| | | | |
|-----------------------------|----------------------------|---------------|-----------------------------|
| APPROVALS | DATE | PROJECT | Altium |
| ENG: . | | | . |
| DSN: . | | | . |
| CHK: . | | | . |
| REFERENCE DOCUMENTS | | | |
| BOM: <BOM DOC NO> | PROJECT REVISION: 61411f0r | | |
| ASSY DWG: <ASSY DWG NO> | CAGE CODE: B | DWG NO: ????? | DOCUMENT REVISION: 61411f0r |
| FAB DWG: <FAB DWG NO> | DESIGN ITEM: USBC Hub | | |
| PCB DWG: <PCB DWG NO> | SIZE: REV: | <SCH DWG NO> | |
| SCALE: FILENAME: Top.SchDoc | SHEET: 1 OF 6 | | |

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARRANTY GIVEN.

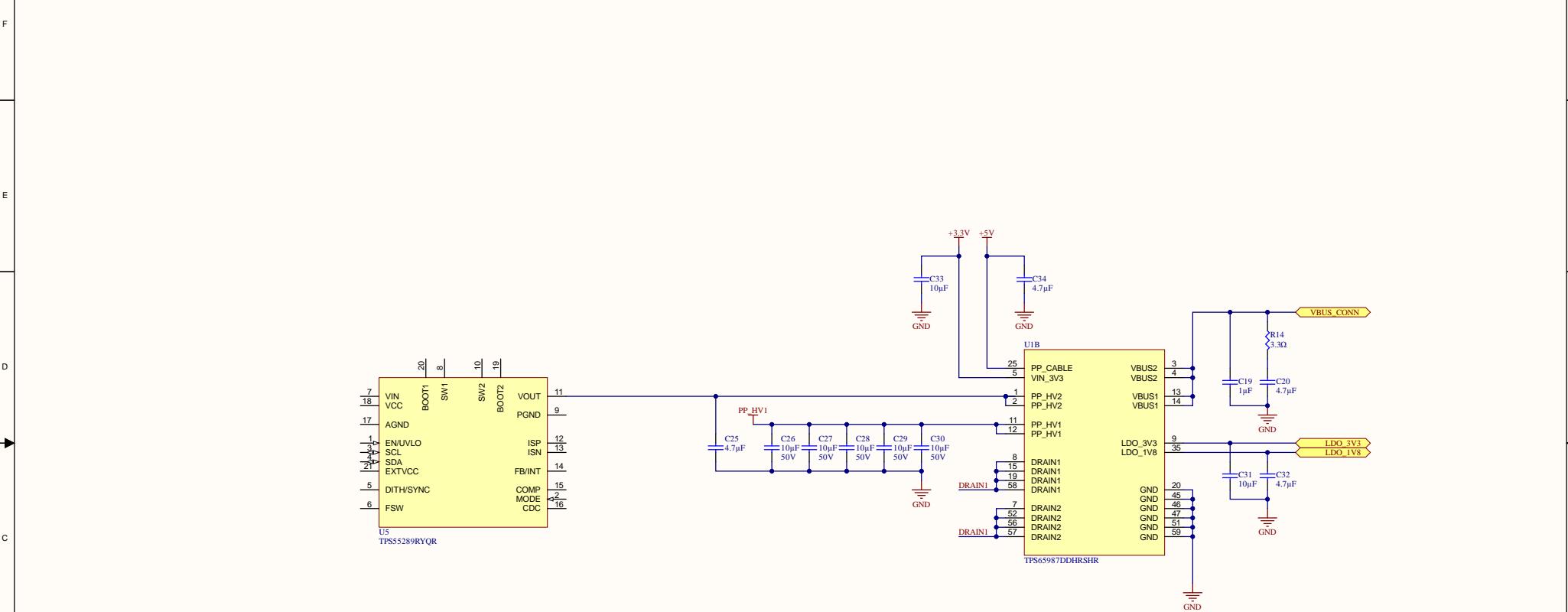


| | | | | | |
|-------------------------|----------|------------------------|--------------------|----------------------------------|--------------|
| APPROVALS | DATE | PROJECT | Altium™ | | |
| ENG: . | | PROJECT REVISION: | DOCUMENT REVISION: | ITEM 6141H Df [Locally Modified] | |
| DSN: . | | | | | |
| CHK: . | | | | | |
| REFERENCE DOCUMENTS | | | | | |
| <BOM DOC NO> | | | | | |
| ASSY DWG: <ASSY DWG NO> | | | | | |
| FAB DWG: <FAB DWG NO> | | | | | |
| PCB DWG: <PCB DWG NO> | | | | | |
| TITLE | | Downstream Port | | | |
| SIZE | | CAGE CODE | DWG NO | REV | |
| B | | ????? | <SCH DWG NO> | | |
| SCALE: | FILENAME | Downstream_Port.SchDoc | | | SHEET 2 OF 6 |

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.



| | | | | | | | | |
|---|---|---|---|---|---|-----------------------|---|---|
| 8 | 7 | 6 | 5 | 4 | 3 | <SCH DWG NO> REV B | 2 | 1 |
| THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN. | | | | | | | | |



| | | | | | |
|-------------------------|------|---------|---|-------------------|--------------|
| APPROVALS | DATE | PROJECT | Altium | | |
| ENG: . | | | PROJECT REVISION: DOCUMENT REVISION: 61411fbf | | |
| DSN: . | | | DESIGN ITEM: | | |
| CHK: . | | | TITLE: Port Power | | |
| REFERENCE DOCUMENTS | | | | | |
| BOM: <BOM DOC NO> | | | SIZE | CAGE CODE | DWG NO. |
| ASSY DWG: <ASSY DWG NO> | | | B | ????? | <SCH DWG NO> |
| FAB DWG: <FAB DWG NO> | | | REV | | |
| PCB DWG: <PCB DWG NO> | | | SCALE: | FILENAME | SHEET 4 OF 6 |
| | | | | Port_Power.SchDoc | |

8
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

7

6

5

4

3

<SCHEMATIC NO>

REV

5

SHEET

2

1

| REVISION | DESCRIPTION | DATE | APPROVED |
|----------|-------------|------|----------|
| | | | |

F

F

E

E

D

D

C

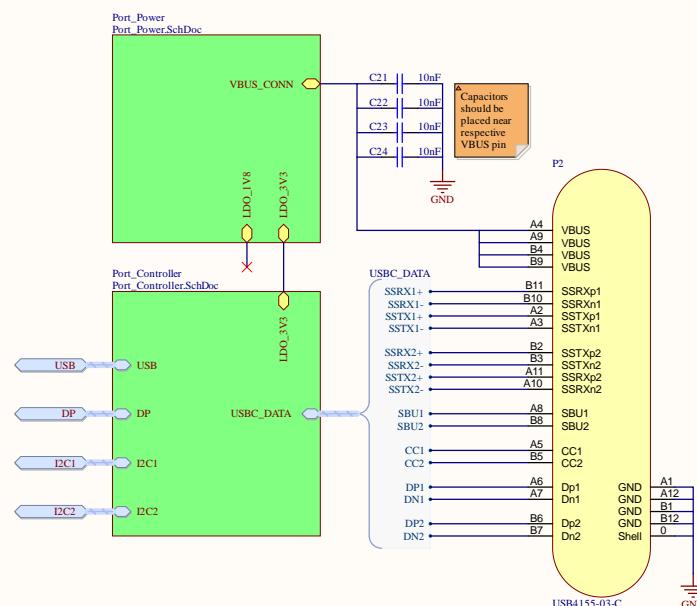
C

B

B

A

A



| | | | | | |
|---------------------|------|---------|---|---------------------------------------|--------------------------|
| APPROVALS | DATE | PROJECT | Altium | | |
| ENG: . | | | PROJECT REVISION: DOCUMENT REVISION: DESIGN ITEM: 6141ffbf [Locally Modified] | | |
| DSN: . | | | TITLE: Upstream Port | | |
| CHK: . | | | BOM: <BOM DOC NO> | SIZE: CAGE CODE: DWG NO: | REV: B ???? <SCH DWG NO> |
| REFERENCE DOCUMENTS | | | ASSY DWG: <ASSY DWG NO> | | |
| | | | FAB DWG: <FAB DWG NO> | | |
| | | | PCB DWG: <PCB DWG NO> | SCALE: FILENAME: Upstream_Port.SchDoc | SHEET 5 OF 6 |

8

7

6

5

4

3

2

1

| | | | | | | | | | | | |
|---|---|---|---|---|---|---|-----------------------------|-------------|------|------|----------|
| 8 | 7 | 6 | 5 | ↓ | 4 | 3 | <SCH DWG NO> DWG NO. REV | 6 | SHFT | 2 | 1 |
| THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN. | | | | | | | REVISION | DESCRIPTION | | DATE | APPROVED |

F

F

E

E

D

D

C

C

B

B

A

A

| | | | | |
|-------------------------|------|-------------------|--------------------|-----------------|
| APPROVALS | DATE | PROJECT | Altium | |
| ENG: . | | | : | |
| DSN: . | | | : | |
| CHK: . | | | : | |
| REFERENCE DOCUMENTS | | PROJECT REVISION: | DOCUMENT REVISION: | DESIGN ITEM: |
| BOM: <BOM DOC NO> | | B | ????? | 61411f0r |
| ASSY DWG: <ASSY DWG NO> | | SIZE | CAGE CODE | DWG NO. |
| FAB DWG: <FAB DWG NO> | | | | REV |
| PCB DWG: <PCB DWG NO> | | SCALE: | FILENAME | IC_Power.SchDoc |
| | | | SHEET | 6 OF 6 |

| | | | | | | | | |
|---|---|---|---|---|---|---|---|---|
| 8 | 7 | 6 | 5 | ↑ | 4 | 3 | 2 | 1 |
|---|---|---|---|---|---|---|---|---|

