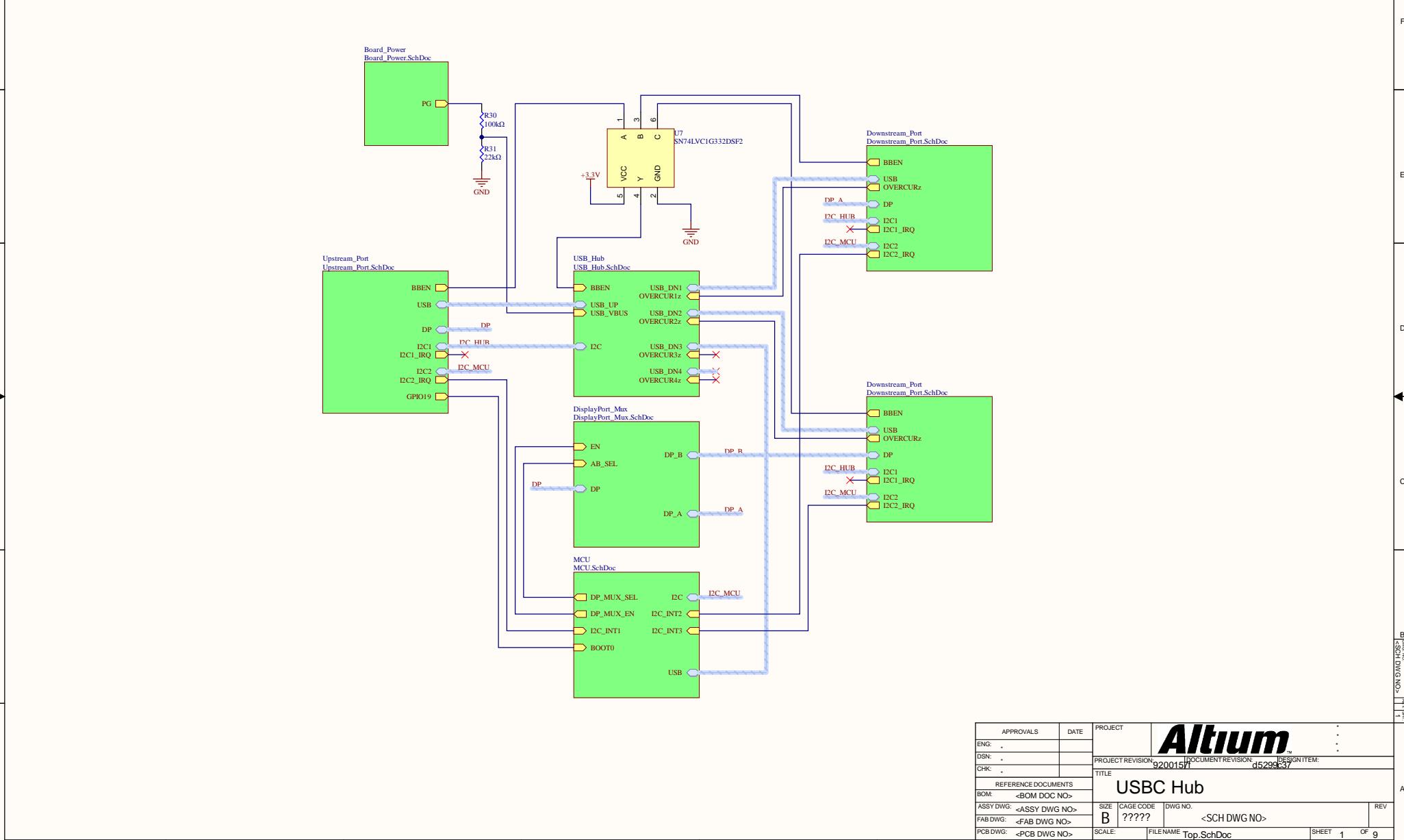


8	7	6	5	4	3	<SCH DWG NO> REV NO	1	2	1		
				↓		1	SHFT	REVISION	DESCRIPTION	DATE	APPROVED
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.											



APPROVALS	DATE	PROJECT	Altium		
ENG: .		PROJECT REVISION:	920015V1		
DSN: .		DOCUMENT REVISION:	d529437		
CHK: .		TITLE:	USBC Hub		
REFERENCE DOCUMENTS					
BOM:	<BOM DOC NO>		SIZE	CAGE CODE	DWG NO
ASSY DWG:	<ASSY DWG NO>		B	?????	<SCH DWG NO>
FAB DWG:	<FAB DWG NO>		REV		
PCB DWG:	<PCB DWG NO>		SCALE:	FILENAME	SHEET 1 OF 9
	Top.SchDoc				

8
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

7

6

5

4

3

2

1

<SCH DWG NO>		REV	2	1
REVISION	DESCRIPTION	DATE	APPROVED	

F

E

D

C

B

A

F

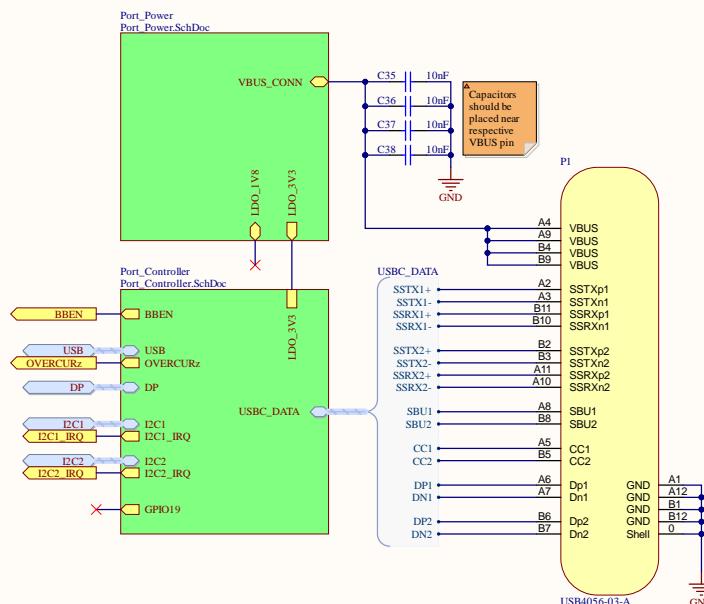
E

D

C

B

A



APPROVALS	DATE	PROJECT	Altium		
ENG:			920015V1		
DSN:			C35edb01		
CHK:			DOCUMENT REVISION:		
REFERENCE DOCUMENTS					
BOM: <BOM DOC NO>					
ASSY DWG: <ASSY DWG NO>					
FAB DWG: <FAB DWG NO>					
PCB DWG: <PCB DWG NO>					
SIZE	CAGE CODE	DWG NO	REV		
B	?????	<SCH DWG NO>			
SCALE:	FILENAME	Downstream_Port.SchDoc	SHEET	2	OF 9

8

7

6

5

4

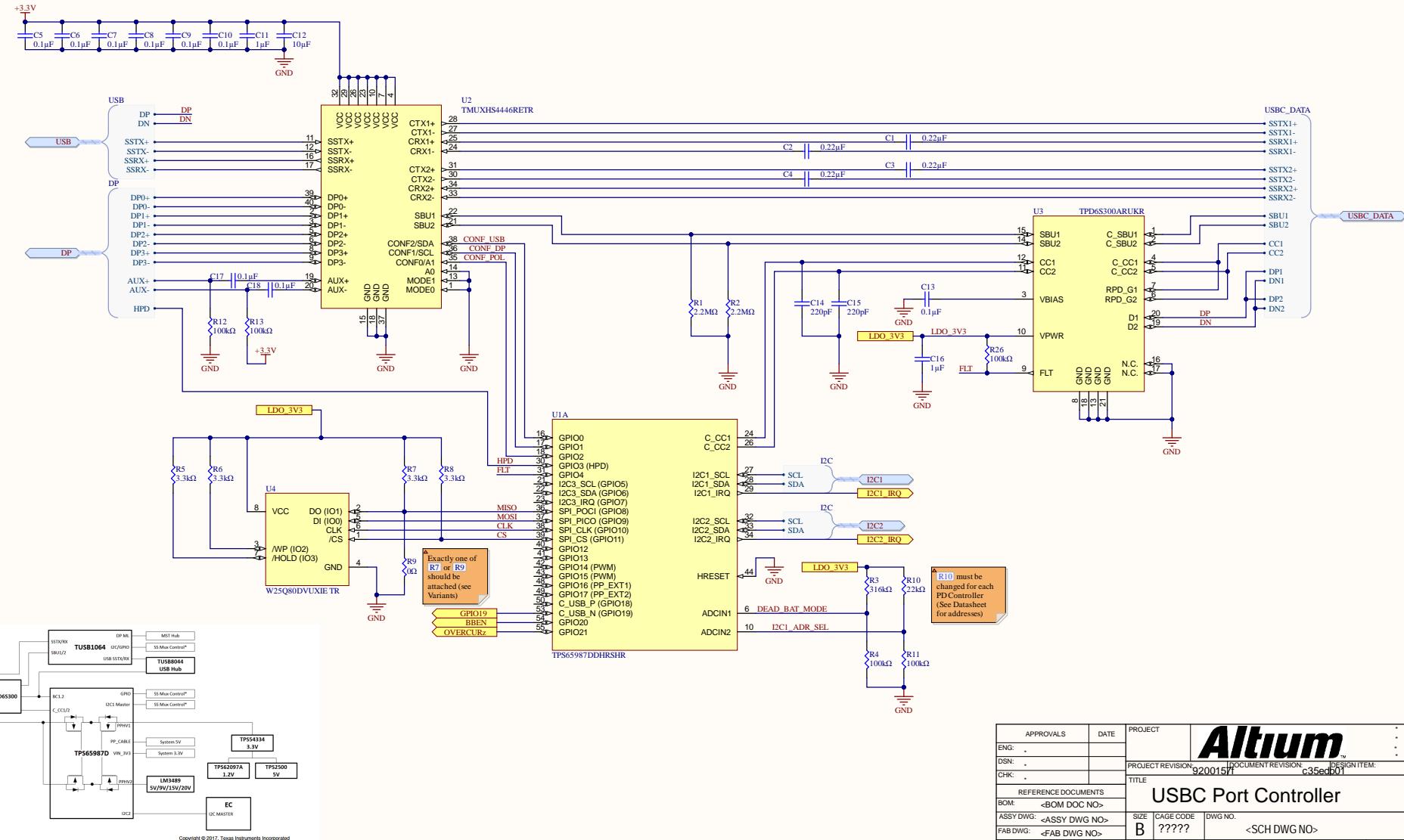
3

2

1

2

10



8 7 6 5 **4** 3 2 1

8	7	6	5	4	3	<SCH DWG NO> DWG NO. REV	4 SHEET	2	1
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.						REVISION	DESCRIPTION	DATE	APPROVED

F

F

E

E

D

D

C

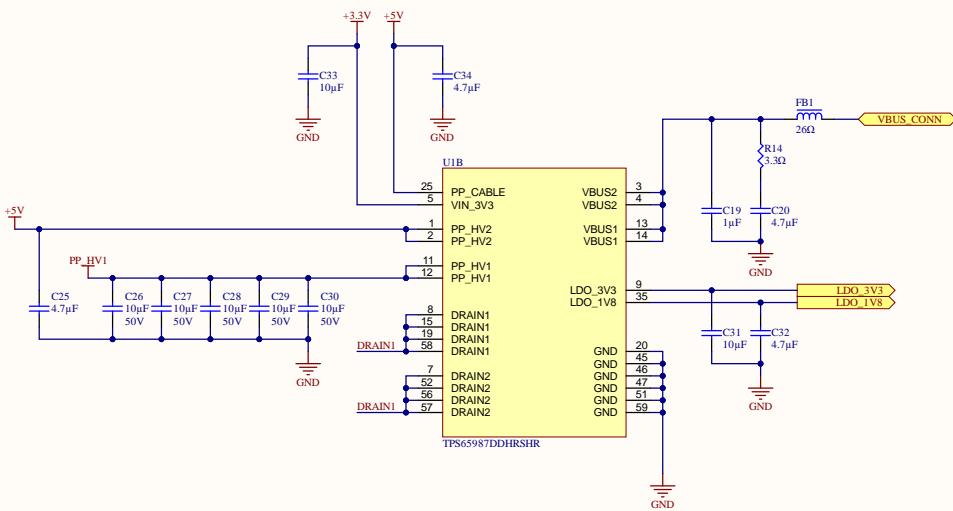
C

B

B

A

A



APPROVALS	DATE	PROJECT	Altium	.
ENG: .				
DSN: .				
CHK: .				
REFERENCE DOCUMENTS		PROJECT REVISION: 920015V1	DOCUMENT REVISION: 89fbabae	DESIGN ITEM:
BOM: <BOM DOC NO>		TITLE: Port Power		
ASSY DWG: <ASSY DWG NO>	SIZE	CAGE CODE	DWG NO.	REV
FAB DWG: <FAB DWG NO>	B	?????	<SCH DWG NO>	
PCB DWG: <PCB DWG NO>	SCALE:	FILENAME	Port_Power.SchDoc	SHEET 4 OF 9

8
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

7

6

5

4

3

<SCH DWG NO>

REV

5

SHEET

2

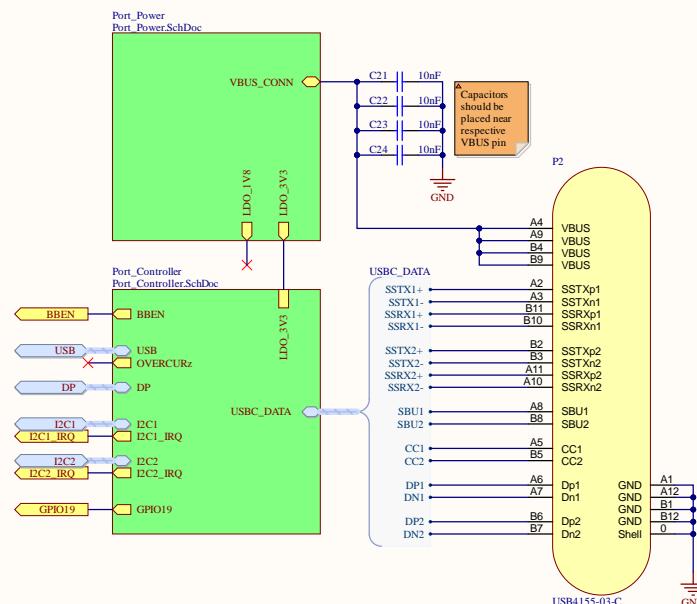
1

REVISION

DESCRIPTION

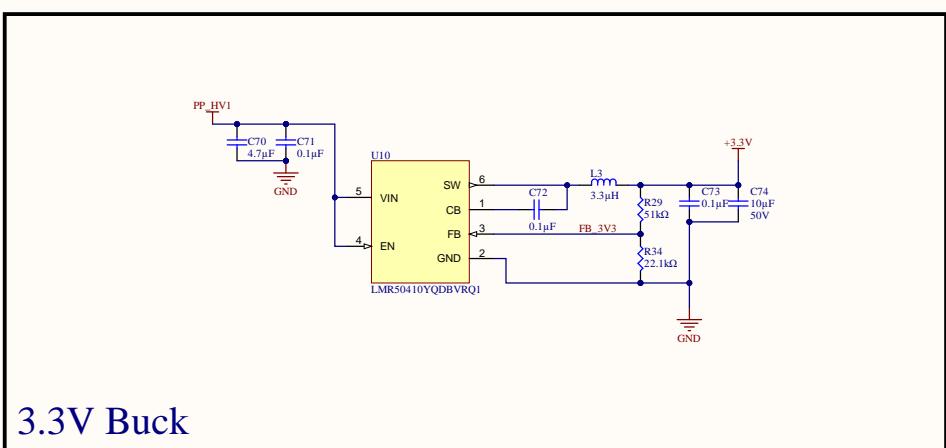
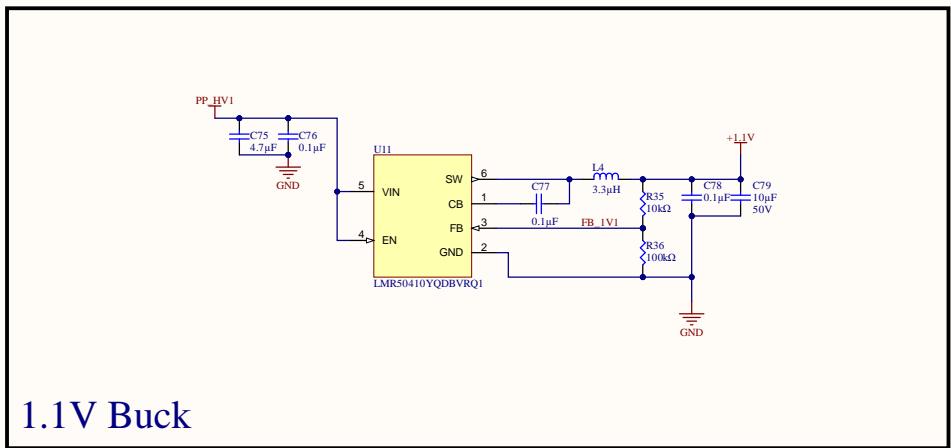
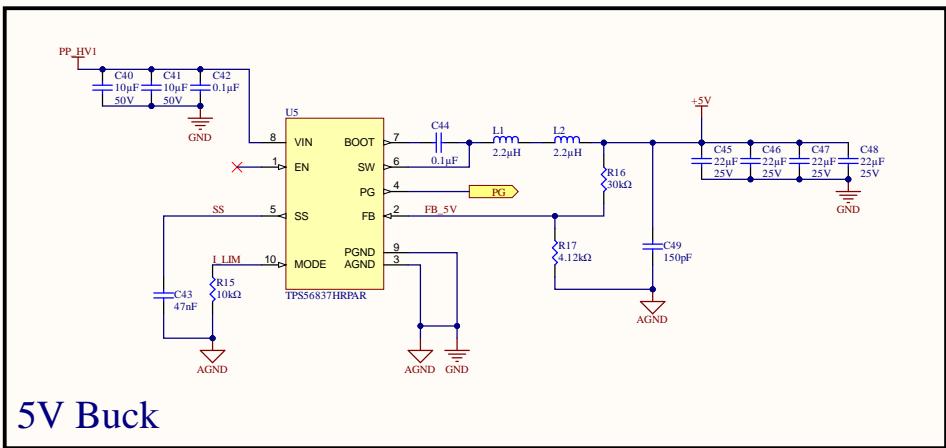
DATE

APPROVED



APPROVALS	DATE	PROJECT	Altium		
ENG: .			PROJECT REVISION: 920015V1		
DSN: .			DOCUMENT REVISION: C35edb01		
CHK: .			DESIGN ITEM:		
REFERENCE DOCUMENTS					
BOM: <BOM DOC NO>	SIZE	CAGE CODE	DWG NO.	REV	
ASSY DWG: <ASSY DWG NO>	B	?????	<SCH DWG NO>		
FAB DWG: <FAB DWG NO>					
PCB DWG: <PCB DWG NO>	SCALE:	FILENAME	Upstream_Port.SchDoc	SHEET 5 OF 9	
				1	

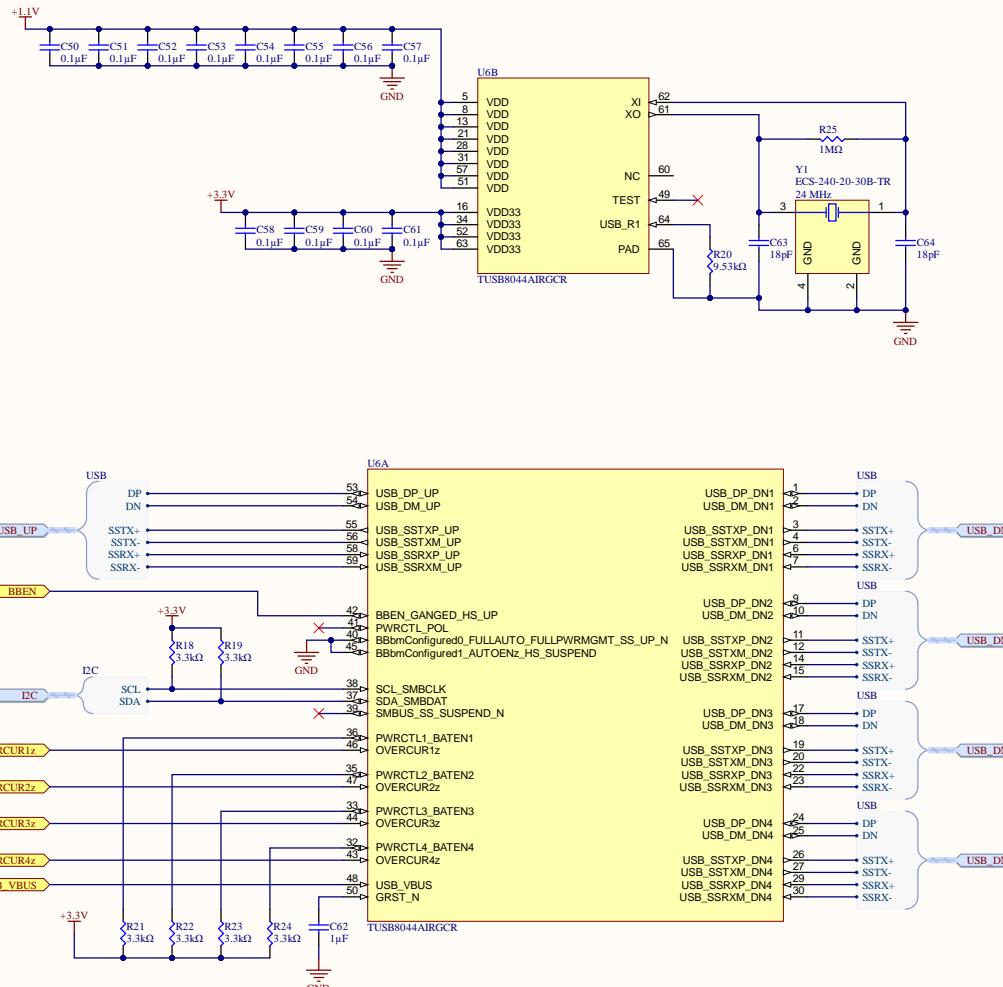
8	7	6	5	4	3	<SCH DWG NO>	6	REVISION	DESCRIPTION	DATE	APPROVED
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.											



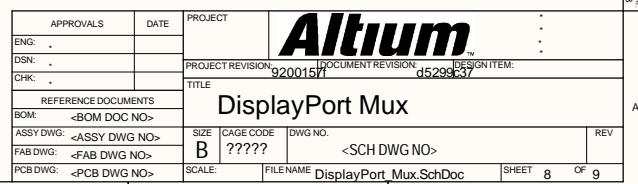
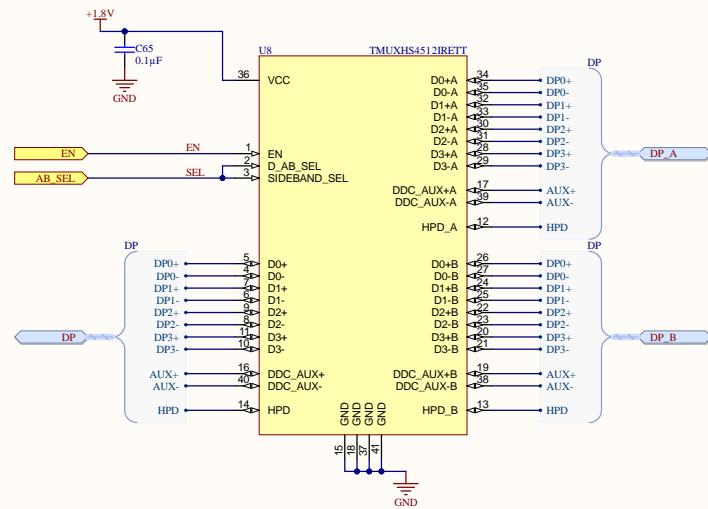
APPROVALS	DATE	PROJECT	Altium		
ENG: .			PROJECT REVISION: 920015/1		
DSN: .			DOCUMENT REVISION: 920015/1 [Locally Modified]		
CHK: .			DESIGN ITEM:		
REFERENCE DOCUMENTS					
BOM: <BOM DOC NO>	SIZE		CAGE CODE	DWG NO.	REV
ASSY DWG: <ASSY DWG NO>	B		?????	<SCH DWG NO>	
FAB DWG: <FAB DWG NO>	PCB DWG:		<PCB DWG NO>	SCALE: FILENAME Board_Power.SchDoc	SHEET 6 OF 9

8	7	6	5	4	3	<SCH DWG NO> Rev. No.	7	2	1		
						REV	SHEET				
								REVISION	DESCRIPTION	DATE	APPROVED

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARRANTY GIVEN.



THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARRANTY GIVEN.



8	7	6	5	4	3	<SCH DWG NO> DWG NO. REV 9 SHEET	2	1
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.						REVISION	DESCRIPTION	DATE APPROVED

F

F

E

E

D

D

C

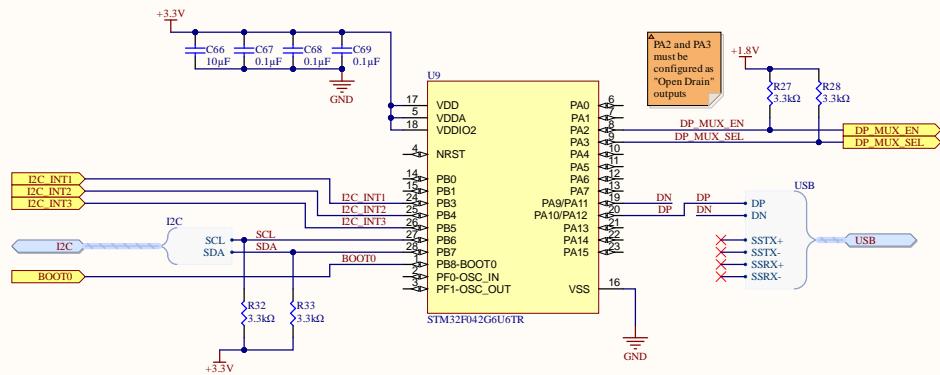
C

B

B

A

A



APPROVALS	DATE	PROJECT	Altium
ENG: .			
DSN: .			
CHK: .			
PROJECT REVISION: 920015V1 DOCUMENT REVISION: 89f6aae DESIGN ITEM:			
TITLE: MCU			
REFERENCE DOCUMENTS		CAGE CODE: B ???? DWG NO: <ASSY DWG NO>	
BOM: <BOM DOC NO>		SIZE: ???? REV: <PCB DWG NO>	
ASSY DWG: <ASSY DWG NO>		FAB DWG: <FAB DWG NO>	
FAB DWG: <FAB DWG NO>		PCB DWG: <PCB DWG NO>	
SCALE: 1		FILENAME: MCU.SchDoc	SHEET 9 OF 9

