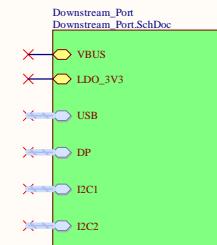
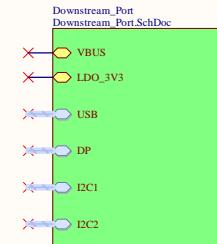
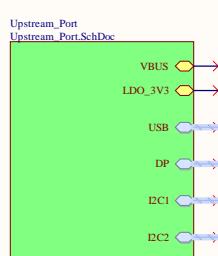
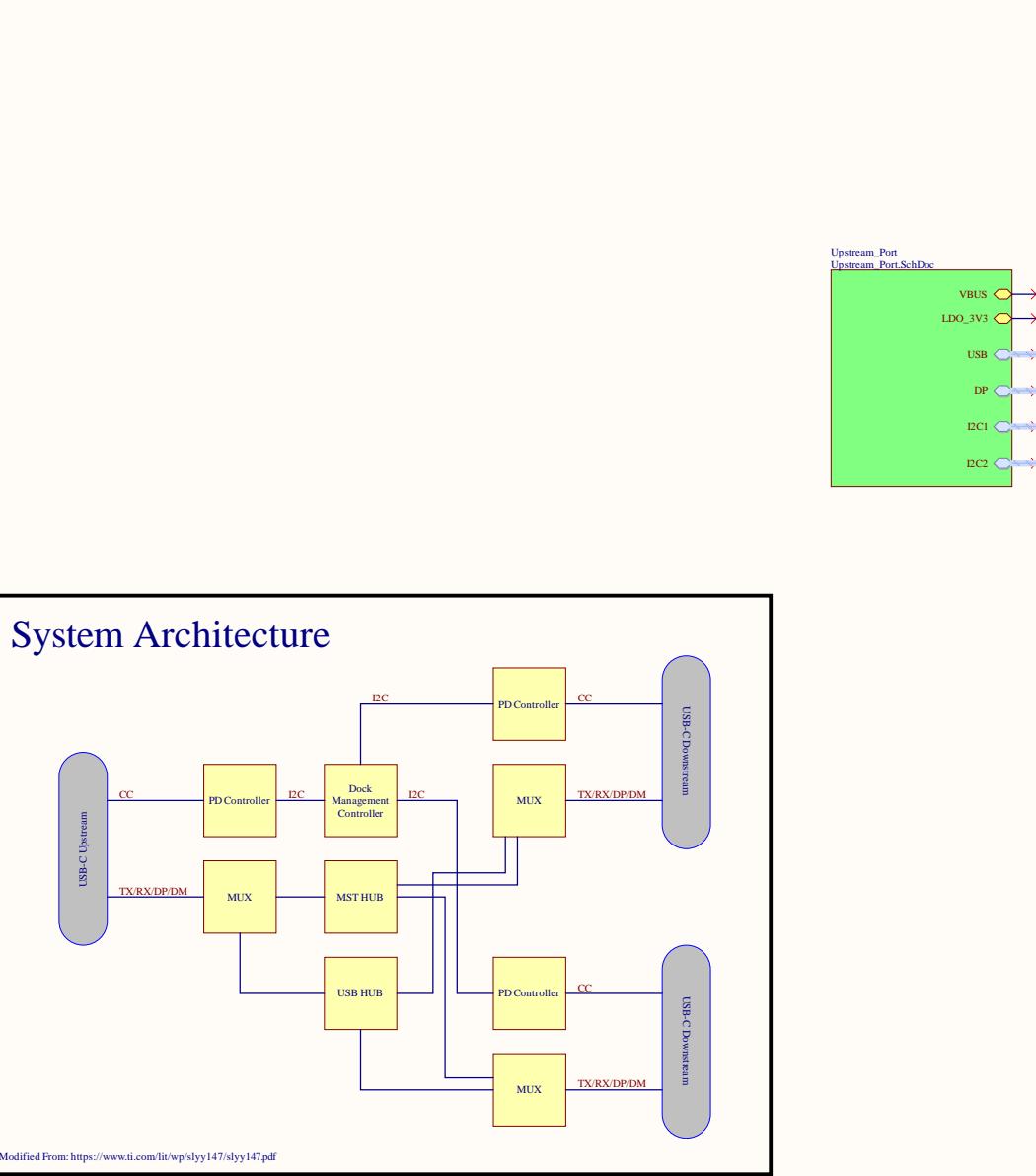


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APPROVALS	DATE	PROJECT	Altium		
ENG: .		PROJECT REVISION:	DOCUMENT REVISION: 6c1a145f 1ae5d13e		
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CHK: .					
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PCB DWG: <PCB DWG NO>					

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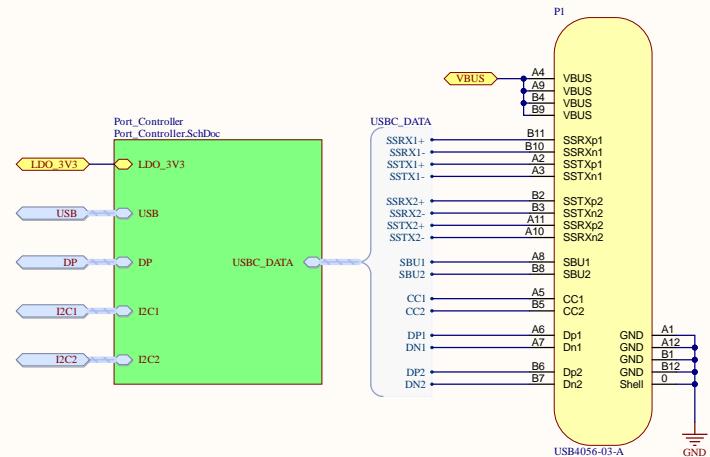
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APPROVALS	DATE	PROJECT	Altium		
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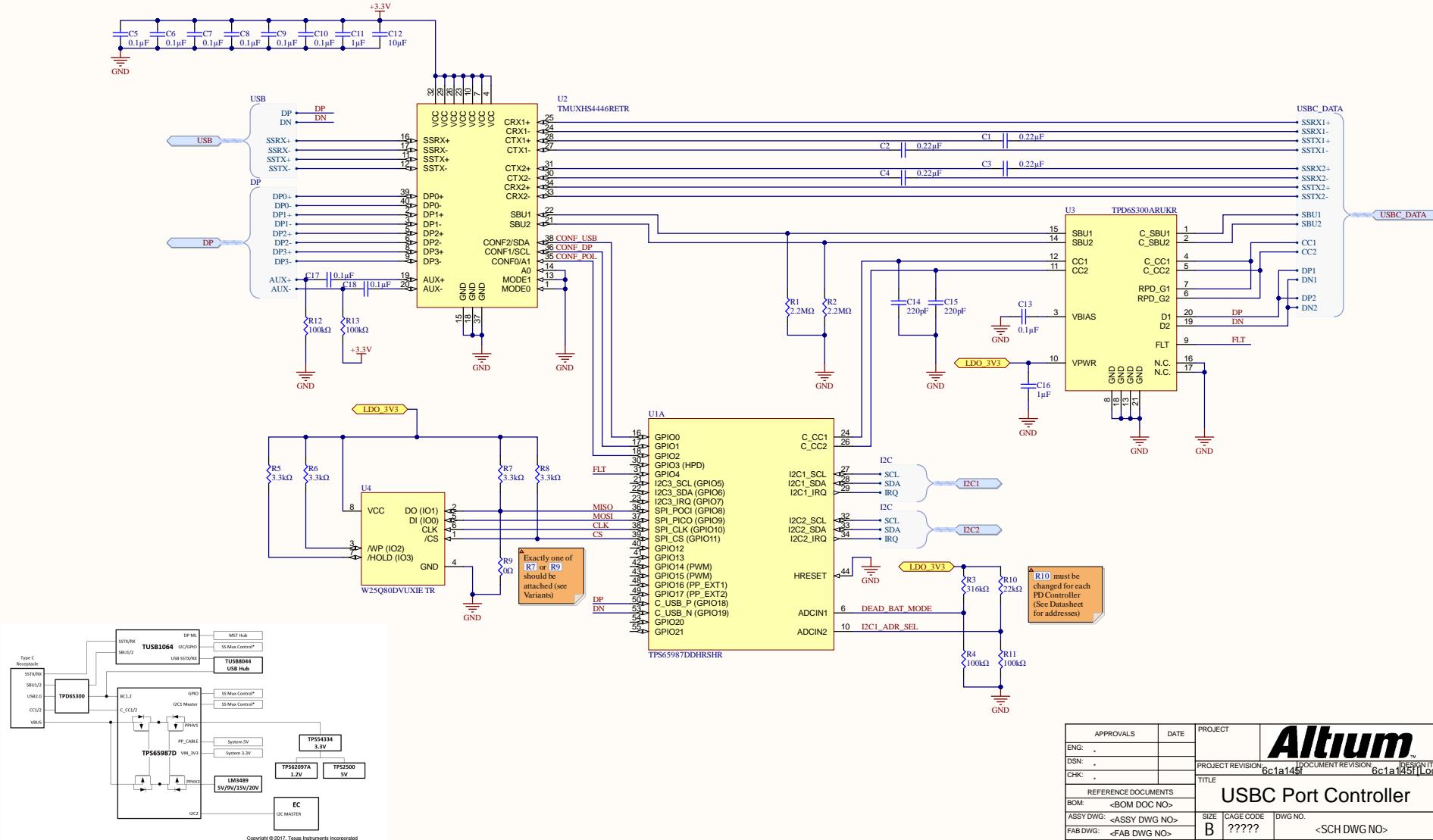
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SCALE:	FILE NAME	Port_Controller.SchDoc	SHEET	3 OF 4

## USBC Port Controller

DOCUMENT REVISION: 6c1a145f | DESIGN ITEM: 6c1a145f [Locally Modified]

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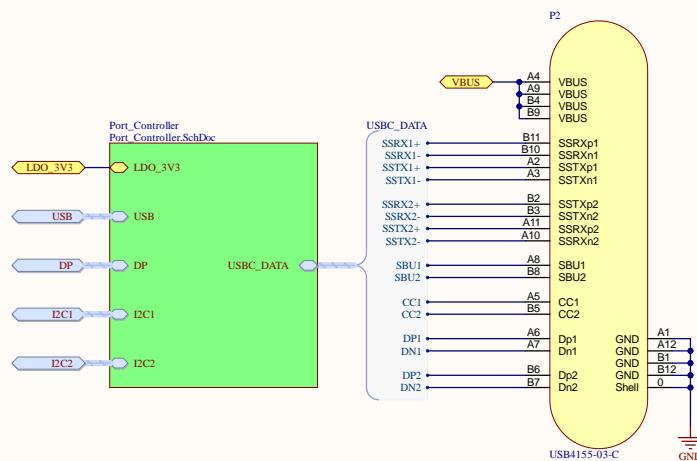
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