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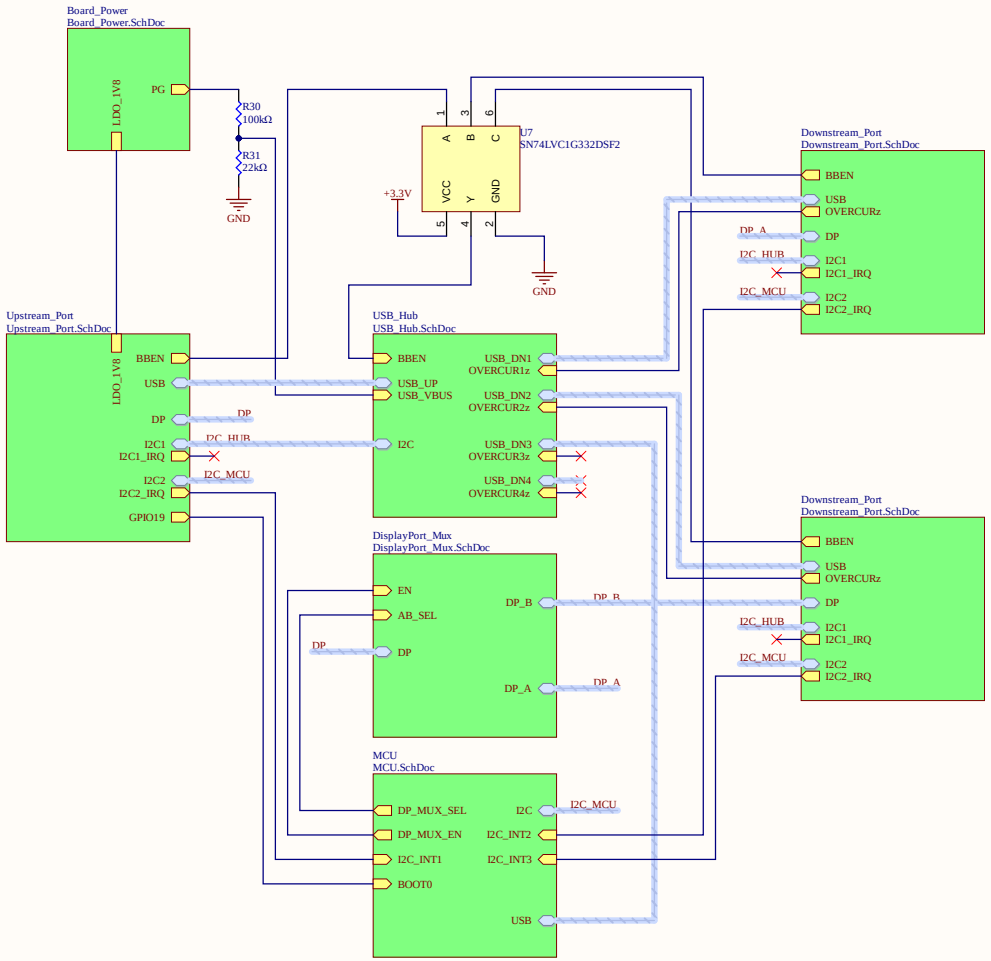


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BOM: <BOM DOC NO>
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FAB DWG: <FAB DWG NO>
PCB DWG: <PCB DWG NO>

PROJECT

Altium

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: | DESIGN ITEM: 709b0c68

TITLE

# USBC Hub

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME Top.SchDoc | SHEET 1 OF 9

<SCH DWG NO> DWG. NO. | REV | 1 SHT.

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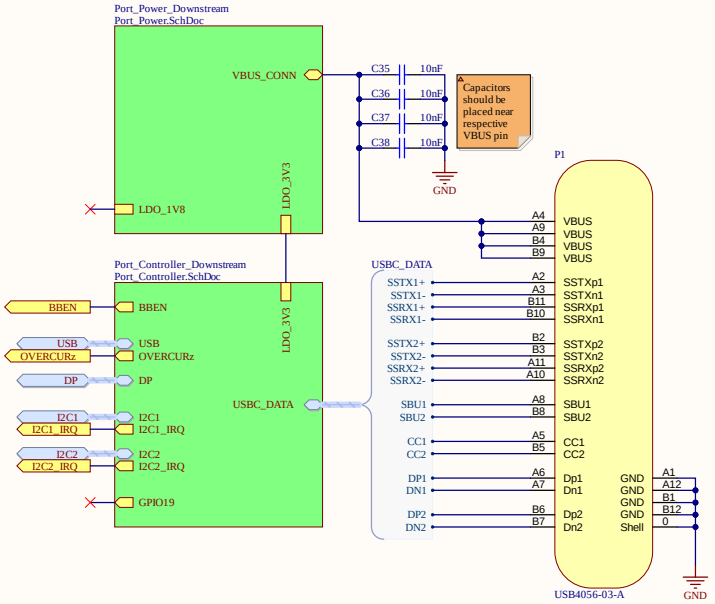


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PCB DWG: <PCB DWG NO>

PROJECT

Altium

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: 3905d703 [Locally Modified] | DESIGN ITEM:

TITLE

# Downstream Port

| SIZE | CAGE CODE | DWG NO. | REV |
|---|---|---|---|
| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME Downstream_Port.SchDoc | SHEET 2 OF 9

<SCH DWG NO> REV 2

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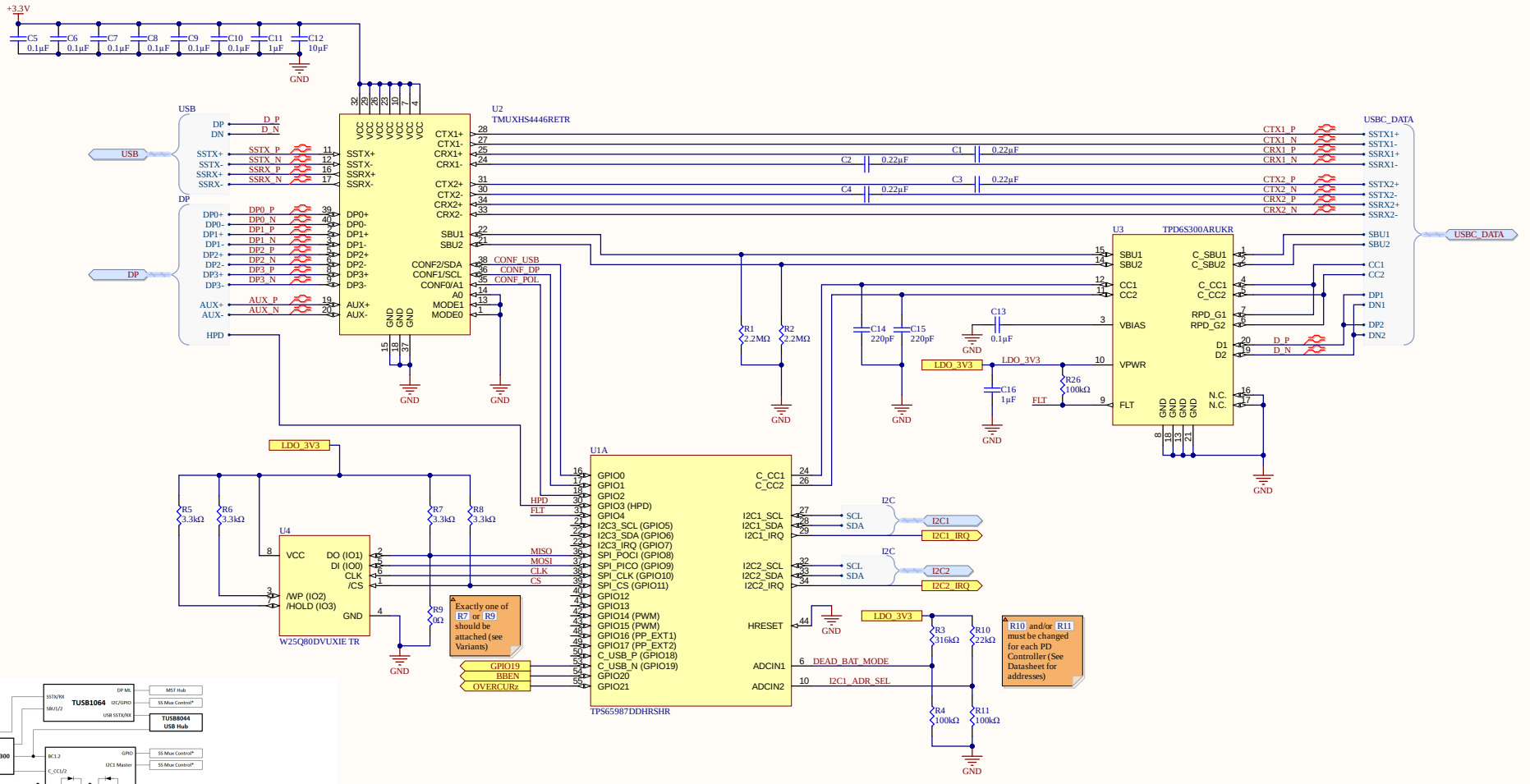


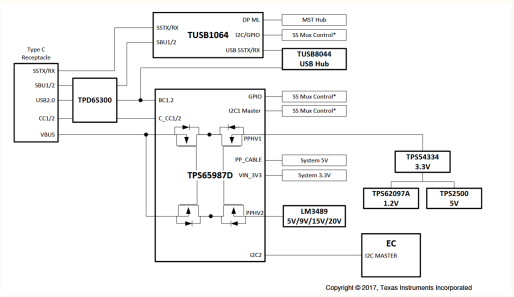


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| PCB DWG: | <PCB DWG NO> |

PROJECT: Altium

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: | DESIGN ITEM: 3905d703 [Locally Modified]

TITLE: USBC Port Controller

| SIZE | CAGE CODE | DWG NO. | REV |
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| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME: Port_Controller.SchDoc | SHEET 3 OF 9

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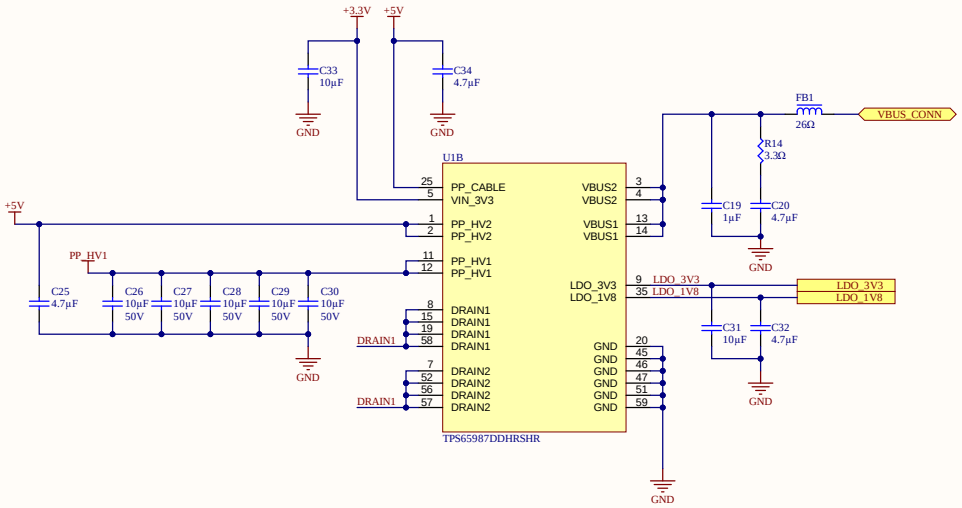


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| FAB DWG: <FAB DWG NO> | |
| PCB DWG: <PCB DWG NO> | |

PROJECT: Altium™

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: 709b0c68 | DESIGN ITEM:

TITLE: Port Power

| SIZE | CAGE CODE | DWG NO. | REV |
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SCALE: | FILE NAME: Port_Power.SchDoc | SHEET 4 OF 9

<SCH DWG NO> DWG. NO. | 4 REV | SHT.

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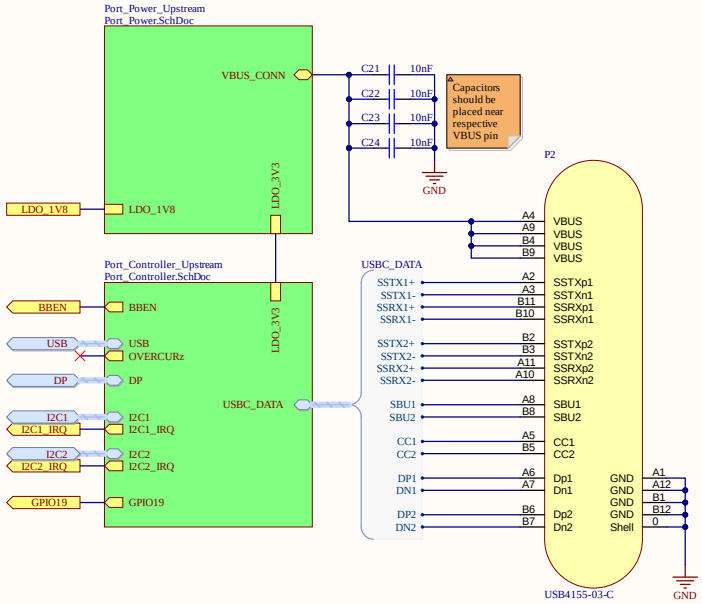


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PROJECT

Altium

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: 3905d703 [Locally Modified] | DESIGN ITEM:

TITLE

# Upstream Port

| SIZE | CAGE CODE | DWG NO. | REV |
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SCALE: | FILE NAME Upstream_Port.SchDoc | SHEET 5 OF 9

<SCH DWG NO> 5

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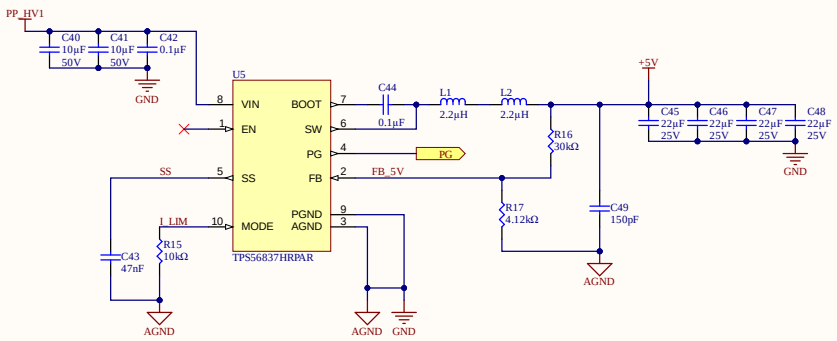
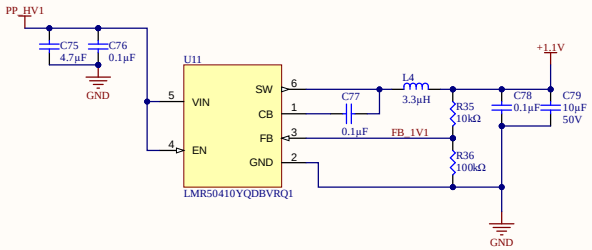
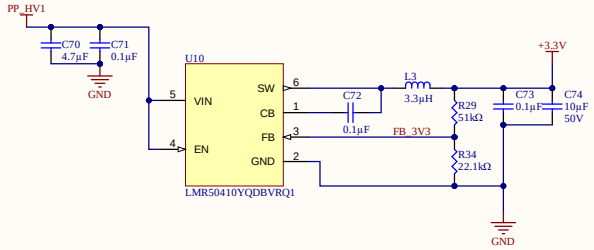



## 5V Buck




## 1.1V Buck




## 3.3V Buck

1.8V is supplied via integrated LDO in upstream PD Controller, LDO_1V8_3

## 1.8V LDO

| APPROVALS | DATE |
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| DSN: * | |
| CHK: * | |

REFERENCE DOCUMENTS

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PROJECT

Altium

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: | DESIGN ITEM: 709b0c68

TITLE: Board Power

| SIZE | CAGE CODE | DWG NO. | REV |
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| B | ????? | <SCH DWG NO> | |

SCALE: | FILE NAME: Board_Power.SchDoc | SHEET 6 OF 9

<SCH DWG NO> 6 9

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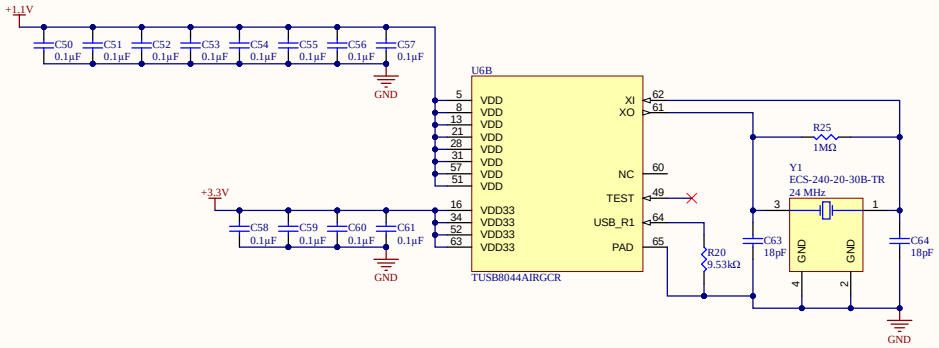


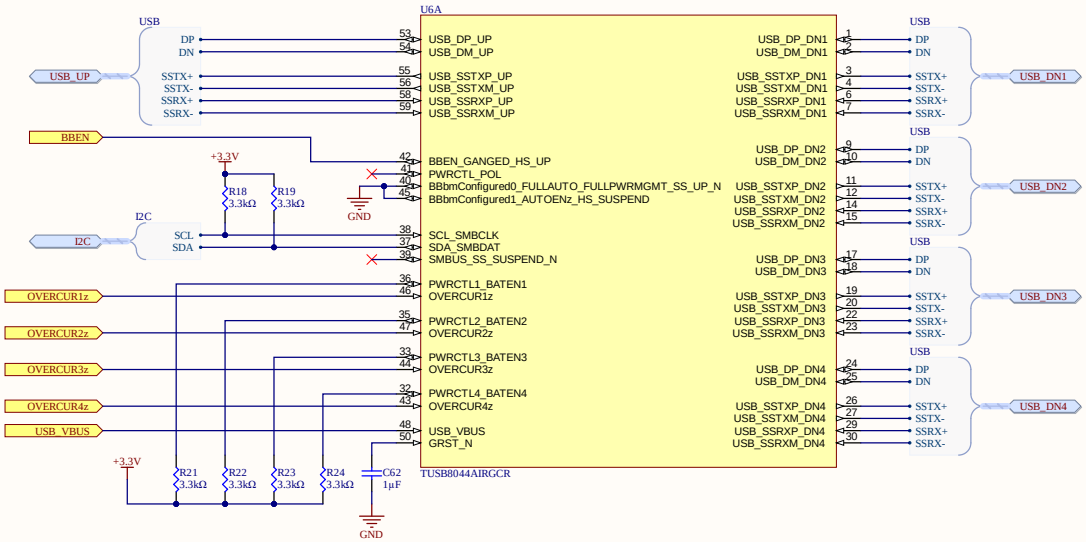


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| PCB DWG: <PCB DWG NO> | |

PROJECT

PROJECT REVISION: 3905d703 DOCUMENT REVISION: 3905d703 DESIGN ITEM:

TITLE

# USB Hub

| SIZE | CAGE CODE | DWG NO. | REV |
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SCALE: FILE NAME USB_Hub.SchDoc SHEET 7 OF 9

<SCH DWG NO>

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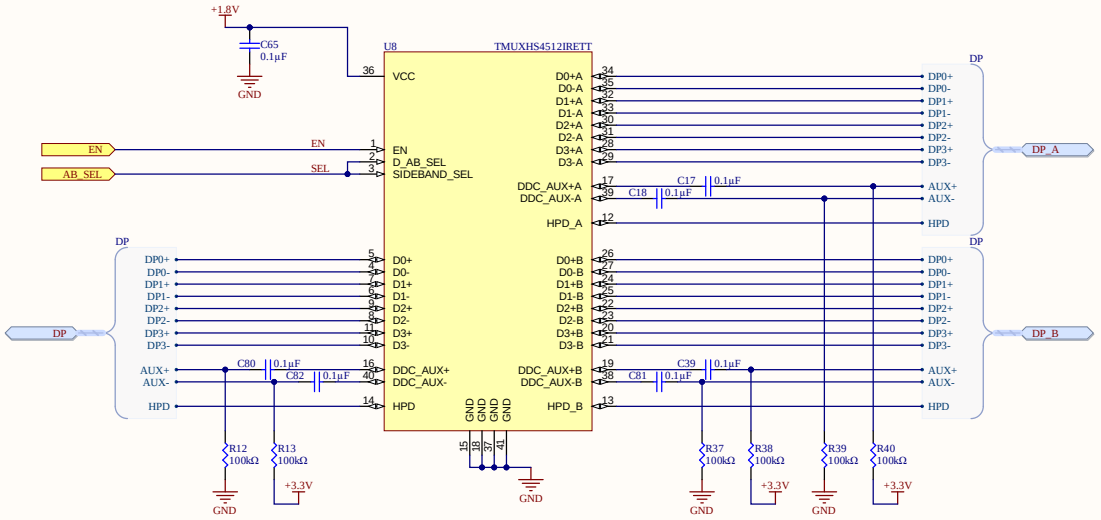


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| PCB DWG: | <PCB DWG NO> |

PROJECT

Altium™

PROJECT REVISION: 3905d703 | DOCUMENT REVISION: 1d01b86e | DESIGN ITEM:

TITLE

# DisplayPort Mux

| SIZE | CAGE CODE | DWG NO. | REV |
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SCALE: | FILE NAME DisplayPort_Mux.SchDoc | SHEET 8 OF 9

<SCH DWG NO> 8

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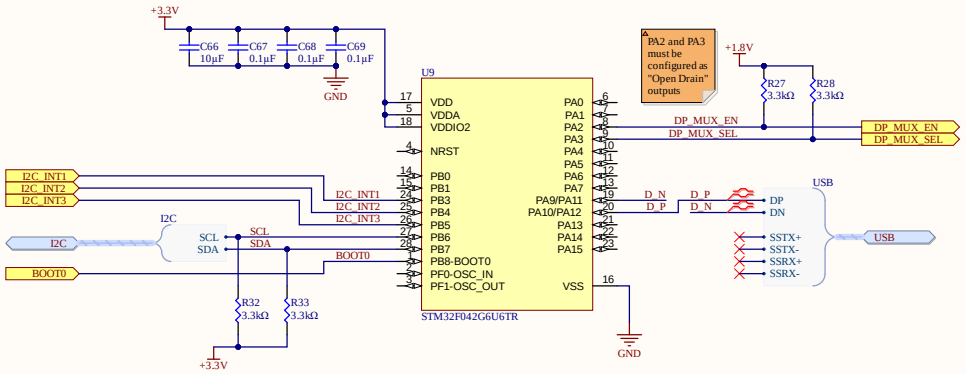


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PROJECT

PROJECT REVISION: 3905d703 DOCUMENT REVISION: 3905d703 DESIGN ITEM:

TITLE MCU

| SIZE | CAGE CODE | DWG NO. | REV |
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SCALE: FILE NAME MCU.SchDoc SHEET 9 OF 9

<SCH DWG NO> 9