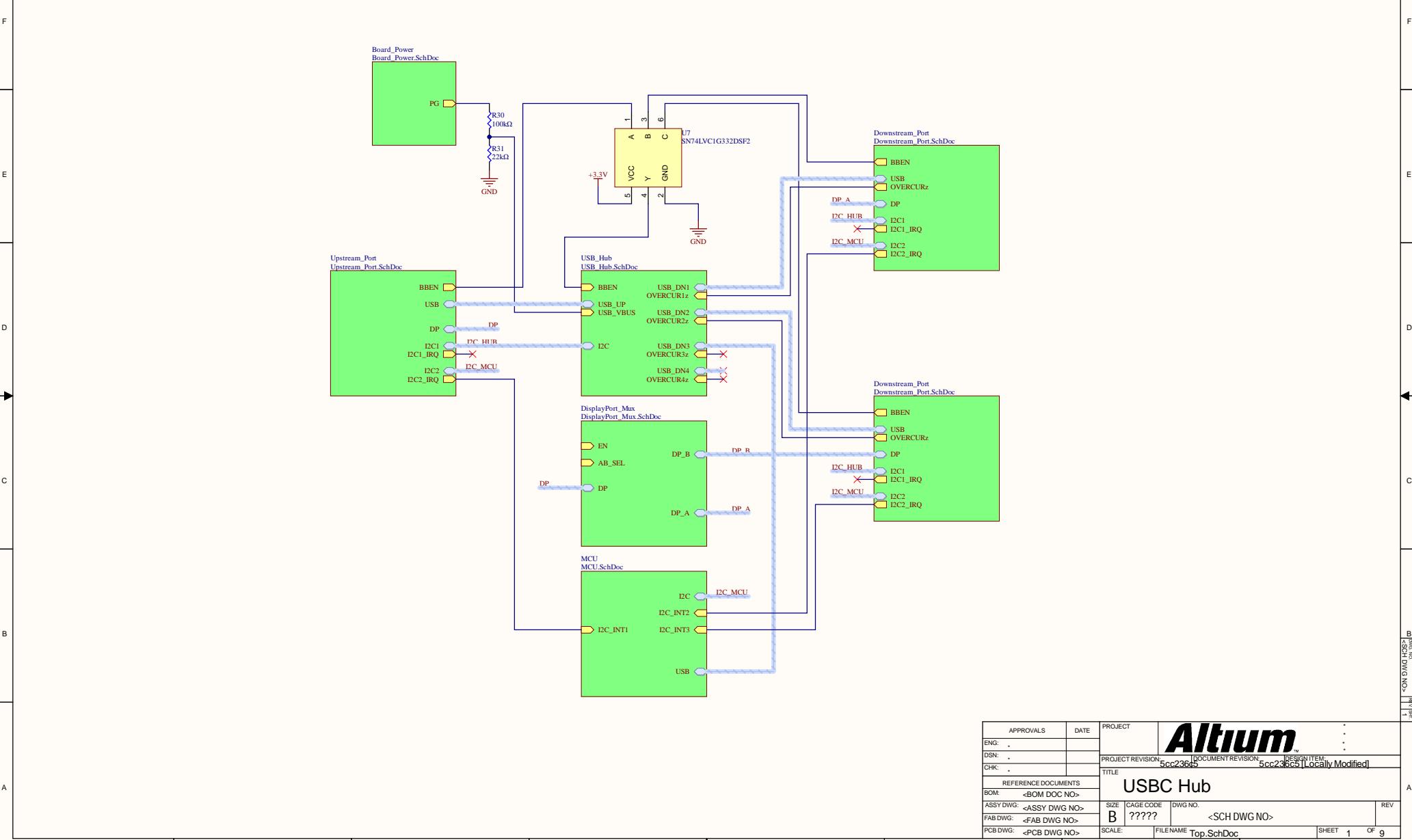


8	7	6	5	4	3	<SCH DWG NO> REV NO	1	2	1		
				↓		REV	SHT	REVISION	DESCRIPTION	DATE	APPROVED
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.											



8
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

7

6

5

4

3

2

1

<SCH DWG NO>		REV	2	1
REVISION	DESCRIPTION	DATE	APPROVED	

F

F

E

E

D

D

C

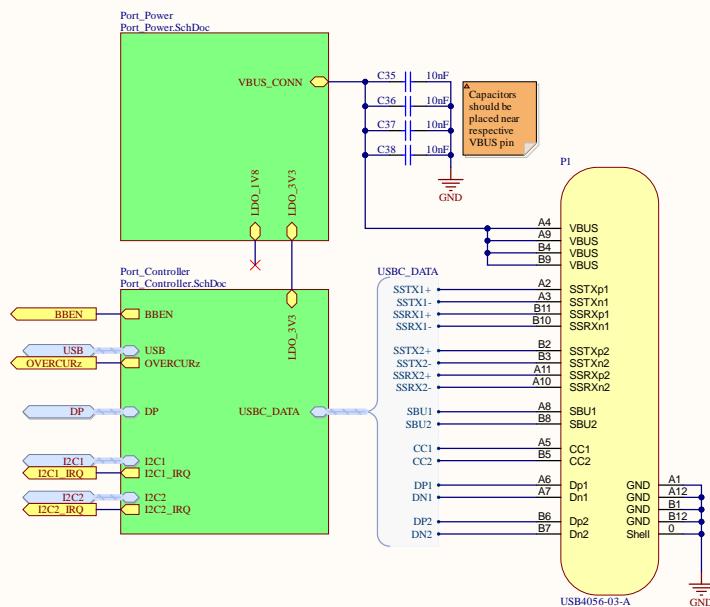
C

B

B

A

A



APPROVALS	DATE	PROJECT	Altium		
ENG:			PROJECT REVISION: 5cc236d DOCUMENT REVISION: 5cc23bc5 DESIGN ITEM: 5cc23bc5		
DSN:					
CHK:					
REFERENCE DOCUMENTS					
BOM: <BOM DOC NO>					
ASSY DWG: <ASSY DWG NO>					
FAB DWG: <FAB DWG NO>					
PCB DWG: <PCB DWG NO>					
SIZE	CAGE CODE	DWG NO.	REV		
B	?????	<SCH DWG NO>			
SCALE:	FILENAME	Downstream_Port.SchDoc	SHEET	2	OF 9

8

7

6

5

4

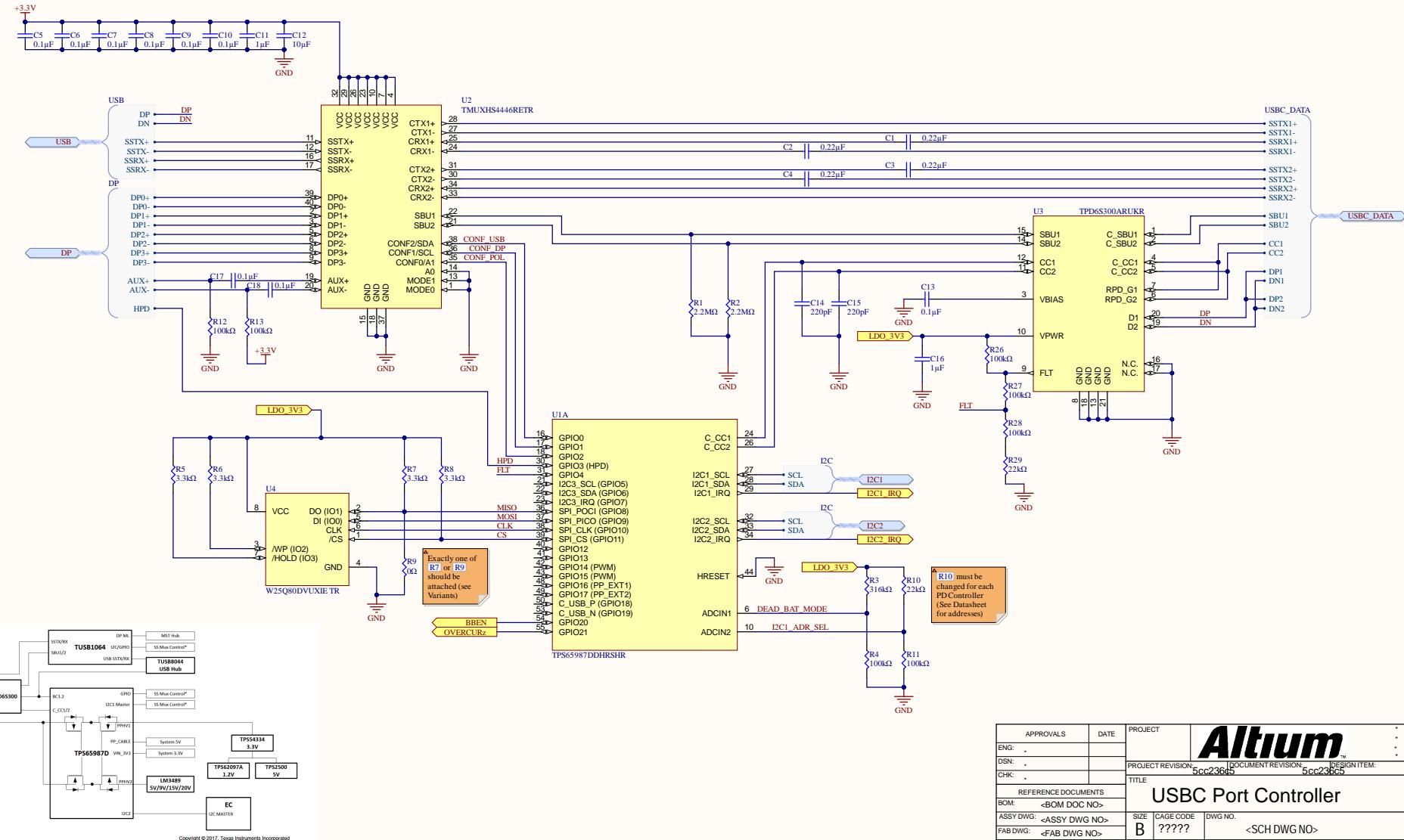
3

2

1

2

10



8 7 6 5 4 3 2 1

8	7	6	5	4	3	<SCH DWG NO> DWG NO. REV	4 SHEET	2	1
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.						REVISION	DESCRIPTION	DATE	APPROVED

F

F

E

E

D

D

C

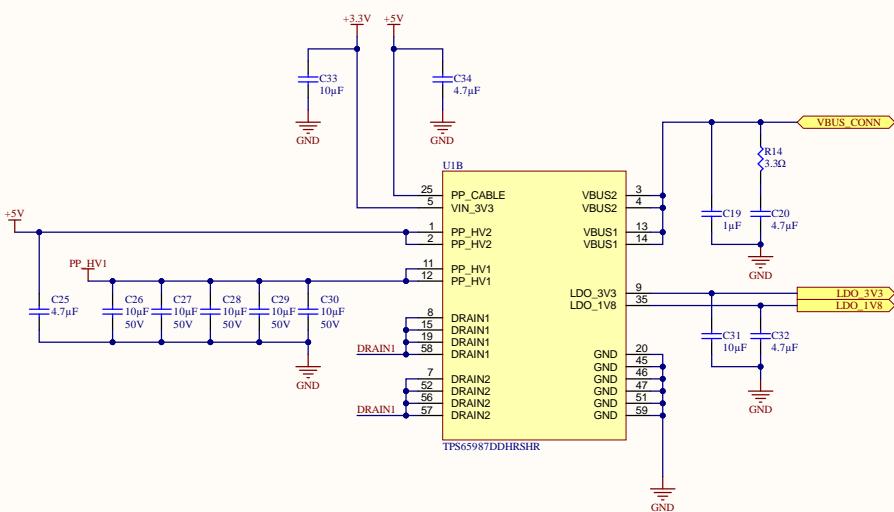
C

B

B

A

A



APPROVALS	DATE	PROJECT	Altium		
ENG: .			PROJECT REVISION: 5c2c36d5 DOCUMENT REVISION: a2d2cb4a DESIGN ITEM: .		
DSN: .			TITLE: Port Power		
CHK: .			REFERENCE DOCUMENTS		
BOM: <BOM DOC NO>			SIZE	CAGE CODE	DWG NO.
ASSY DWG: <ASSY DWG NO>			B	?????	<SCH DWG NO>
FAB DWG: <FAB DWG NO>			REV		
PCB DWG: <PCB DWG NO>			SCALE:	FILENAME	SHEET 4 OF 9
				Port_Power.SchDoc	

8
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.

7

6

5

4

3

<SCH DWG NO>

REV

5

SHEET

2

1

REVISION	DESCRIPTION	DATE	APPROVED

F

F

E

E

D

D

C

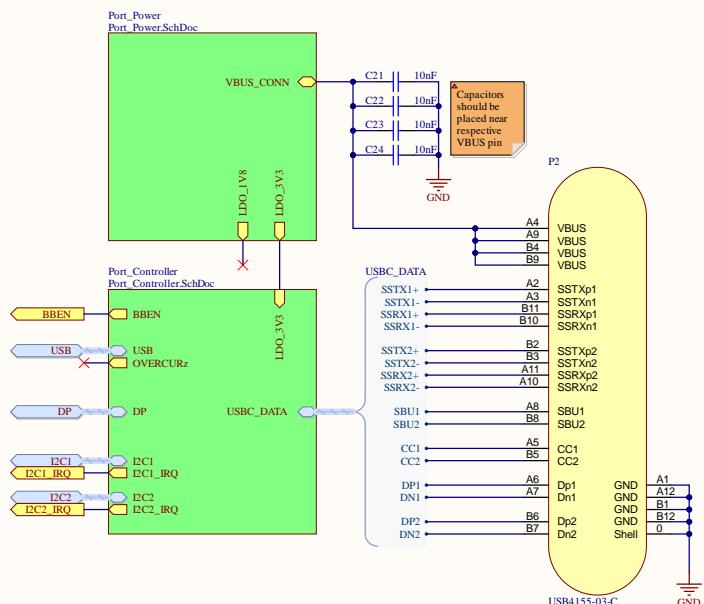
C

B

B

A

A



APPROVALS	DATE	PROJECT	Altium		
ENG:			PROJECT REVISION: 5cc23bc5		
DSN:			DOCUMENT REVISION: 5cc23bc5		
CHK:			DESIGN ITEM: 5cc23bc5		
REFERENCE DOCUMENTS					
BOM: <BOM DOC NO>					
ASSY DWG: <ASSY DWG NO>					
FAB DWG: <FAB DWG NO>					
PCB DWG: <PCB DWG NO>					
SIZE	CAGE CODE	DWG NO.	REV		
B	?????	<SCH DWG NO>			
SCALE:	FILENAME	Upstream_Port.SchDoc	SHEET 5 OF 9		
			1		

8

7

6

5

4

3

2

1

8	7	6	5	4	3	<SCH DWG NO> Rev. No.	6	2	1
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.						REVISION	DESCRIPTION	DATE	APPROVED

C39
0.1uF

F

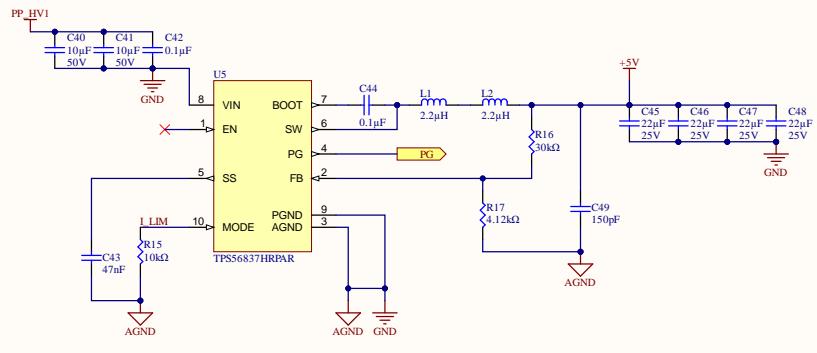
E

D

C

B

A

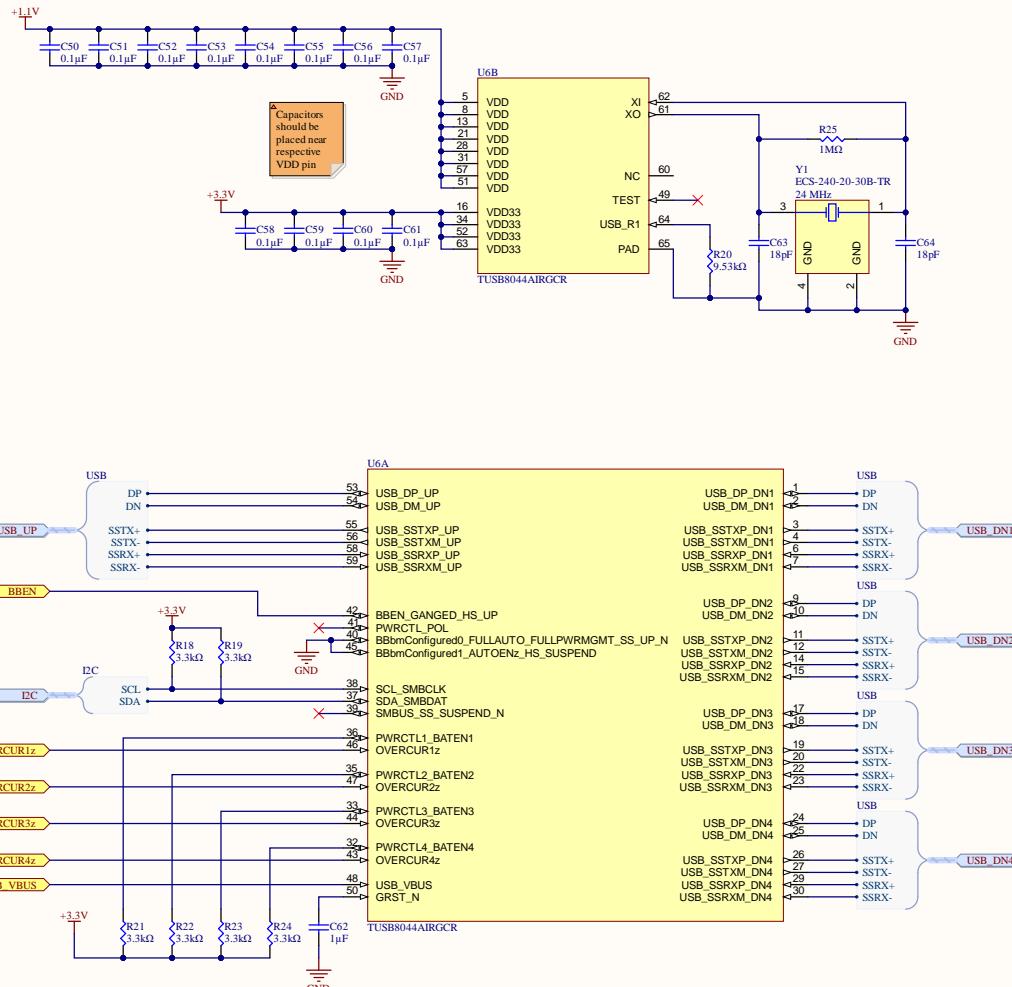


APPROVALS	DATE	PROJECT	Altium	
ENG: .			.	
DSN: .		PROJECT REVISION:	5c2236d	DOCUMENT REVISION: adcbab5b
CHK: .		TITLE:	Board Power	
REFERENCE DOCUMENTS				
BOM: <BOM DOC NO>	SIZE		CAGE CODE	DWG NO.
ASSY DWG: <ASSY DWG NO>	B		?????	<SCH DWG NO>
FAB DWG: <FAB DWG NO>	SCALE:		FILENAME	BOARD_POWER.SchDoc
PCB DWG: <PCB DWG NO>	REV		SHEET	6 OF 9

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

8	7	6	5	4	3	<SCH DWG NO> Rev. No.	7	2	1
						Project		REVISION	
						DESCRIPTION		DATE	APPROVED

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARRANTY GIVEN.



APPROVALS	DATE	PROJECT	Altium	
ENG: .			PROJECT REVISION: 5cc236d5 DOCUMENT REVISION: 5cc23bc5	
DSN: .			DESIGN ITEM: .	
CHK: .			TITLE: USB Hub	
REFERENCE DOCUMENTS				
BOM: <BOM DOC NO>		SIZE	CAGE CODE	DWG NO.
ASSY DWG: <ASSY DWG NO>		B	?????	<SCH DWG NO>
FAB DWG: <FAB DWG NO>				
PCB DWG: <PCB DWG NO>		SCALE:	FILENAME	USB_Hub.SchDoc
			SHEET	7 OF 9

8	7	6	5	4	3	<SCH DWG NO> DWG NO. REV 8 SHEET	2	1
THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR HEREWITH IS THE PROPERTY OF ALTIUM LIMITED AND MAY BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.						REVISION	DESCRIPTION	DATE APPROVED

F

F

E

E

D

D

C

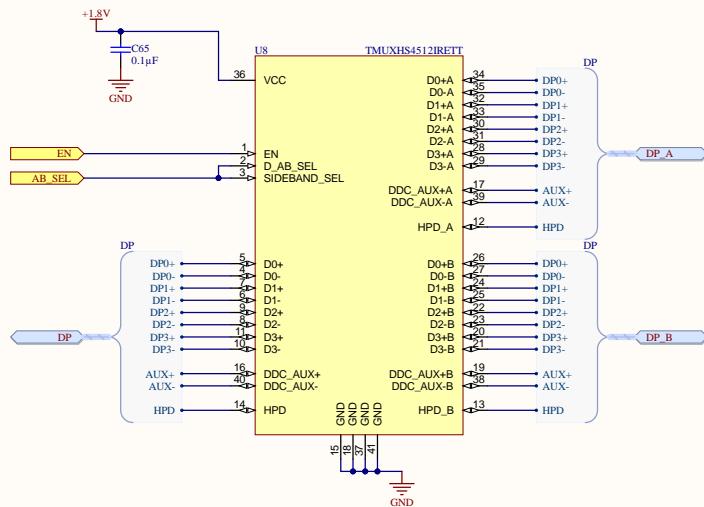
C

B

B

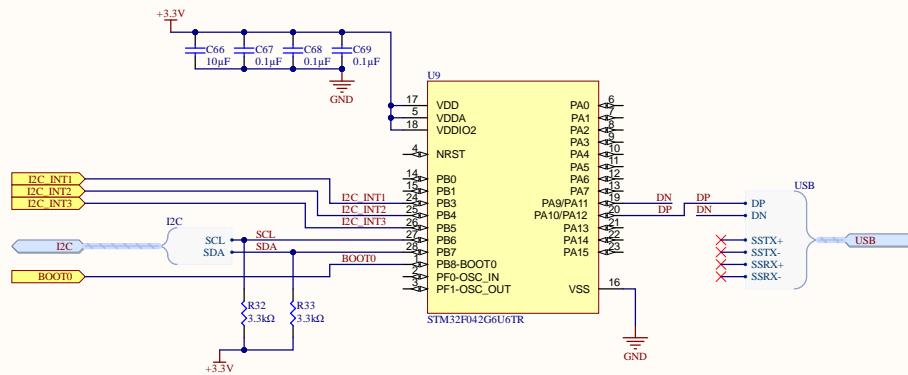
A

A



APPROVALS	DATE	PROJECT	Altium		
ENG: .		PROJECT REVISION:	5cc236d5	DOCUMENT REVISION:	36c16b66
DSN: .		TITLE:	DisplayPort Mux		
CHK: .		REFERENCE DOCUMENTS			
BOM: <BOM DOC NO>		ASSY DWG:	<ASSY DWG NO>		
FAB DWG: <FAB DWG NO>		FAB DWG:	<FAB DWG NO>		
PCB DWG: <PCB DWG NO>		PCB DWG:	<PCB DWG NO>		
SHEET 8 OF 9	SCALE: 1:1	FILE NAME:	DisplayPort_Mux.SchDoc	REV: B	SIZE: CAGE CODE: DWG NO: ???? <SCH DWG NO>

THIS DOCUMENT AND THE DATA DISCLOSED HEREIN OR
HEREWITH IS THE PROPERTY OF ALTIMUM LIMITED AND MAY
BE FREELY DISTRIBUTED IN WHOLE. NO RIGHTS ARE
RESERVED OR EXPRESS OR IMPLIED WARANTEE GIVEN.



APPROVALS	DATE	PROJECT	Altium™		
ENG: .		PROJECT REVISION:	5cc2365	DOCUMENT REVISION:	[DESIGN ITEM]
DSN: .		TITLE:	MCU		
CHK: .		BOM:	<BOM DOC NO>		
REFERENCE DOCUMENTS					
ASSY DWG: <ASSY DWG NO>					
FAB DWG: <FAB DWG NO>					
PCB DWG: <PCB DWG NO>					
SIZE		CAGE CODE	DWG NO.	REV	
B		?????	<SCH DWG NO>		
SCALE:	FILENAME			SHEET	OF
	MCU.SchDoc			9	9

