
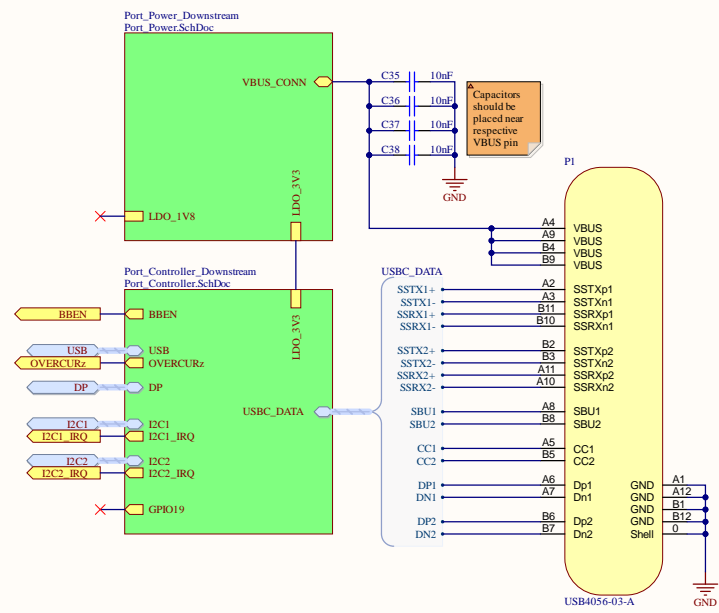



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CHK:	-		FAB DWG:	-		
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PCB DWG: <PCB DWG NO>			SCALE:		FILENAME: Top.SchDoc	
SIZE: B			CAGE CODE: ?????		DWG NO: <SCH DWG NO>	
					SHEET 1 OF 9	

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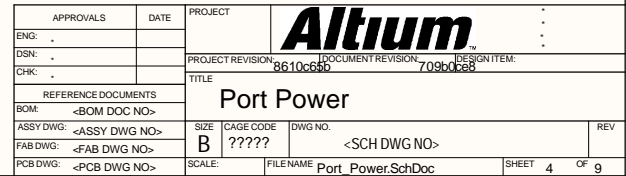
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APPROVALS		DATE	PROJECT			
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DSN:	•		DOCUMENT REVISION: 3905d703			
CHK:	•		DESIGN ITEM:			
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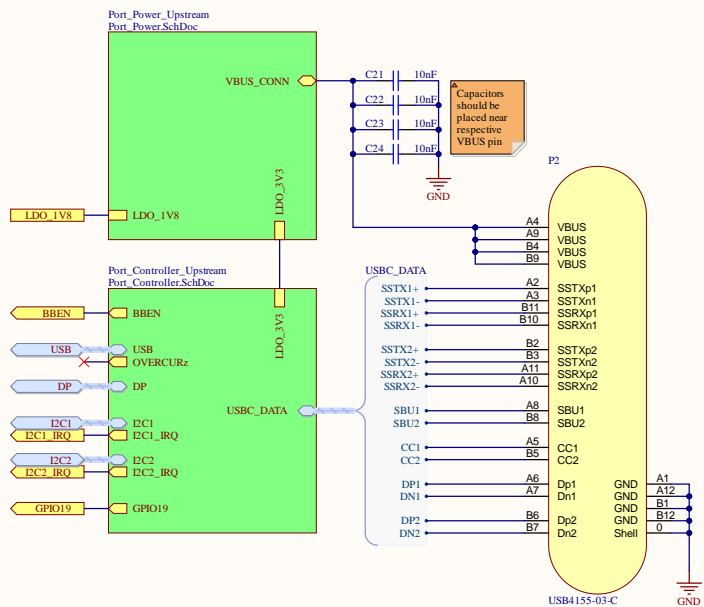


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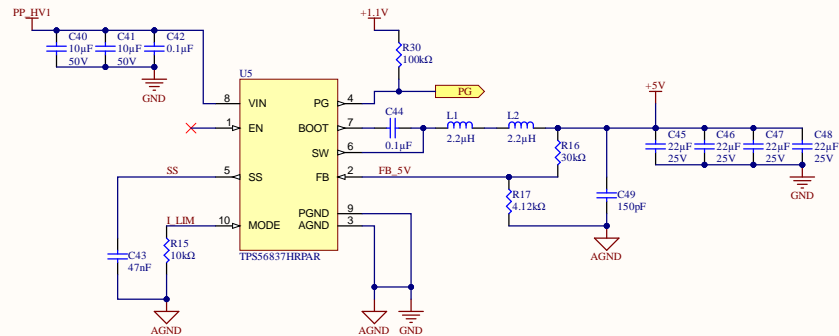
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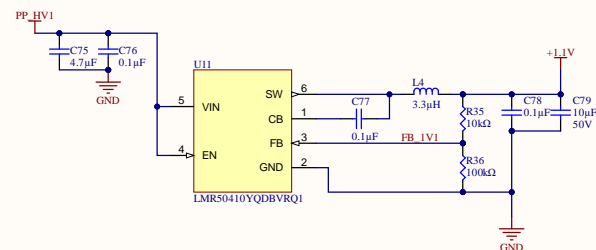
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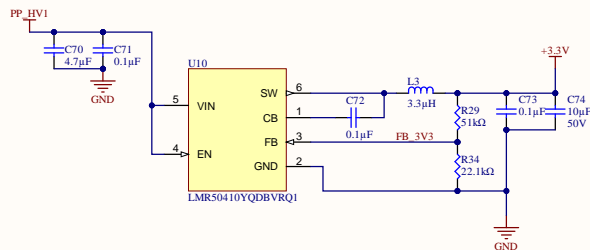
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5V Buck



1.1V Buck



3.3V Buck

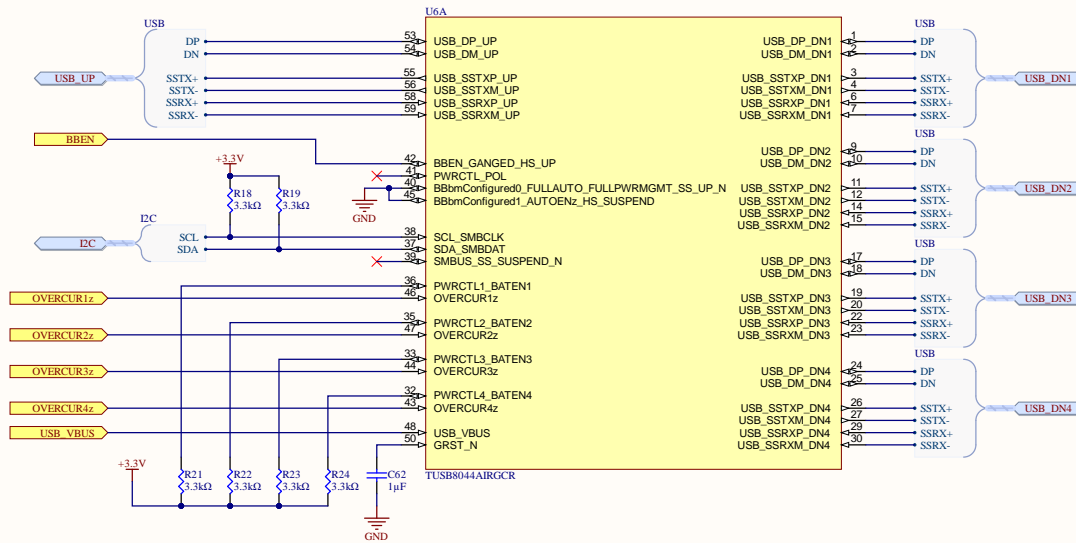
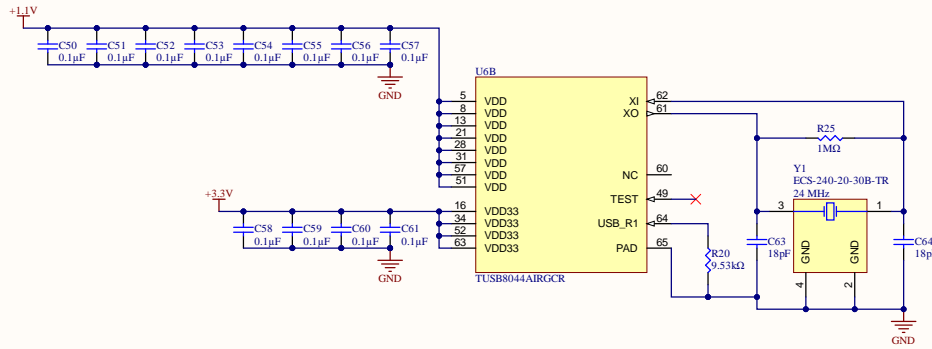


1.8V is supplied via integrated LDO in upstream PD Controller, LDO_1V8_3

1.8V LDO

APPROVALS	DATE	PROJECT	Altium	
ENG: -		PROJECT REVISION: 8610c680	DOCUMENT REVISION: 709bdc81 (Locally Modified)	DESIGN SYSTEM
DSN: -		TITLE		
CHK: -		Board Power		
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FAB DWG: <FAB DWG NO>		SCALE:	FILENAME: Board_Power.SchDoc	SHEET 6 OF 9
PCB DWG: <PCB DWG NO>				

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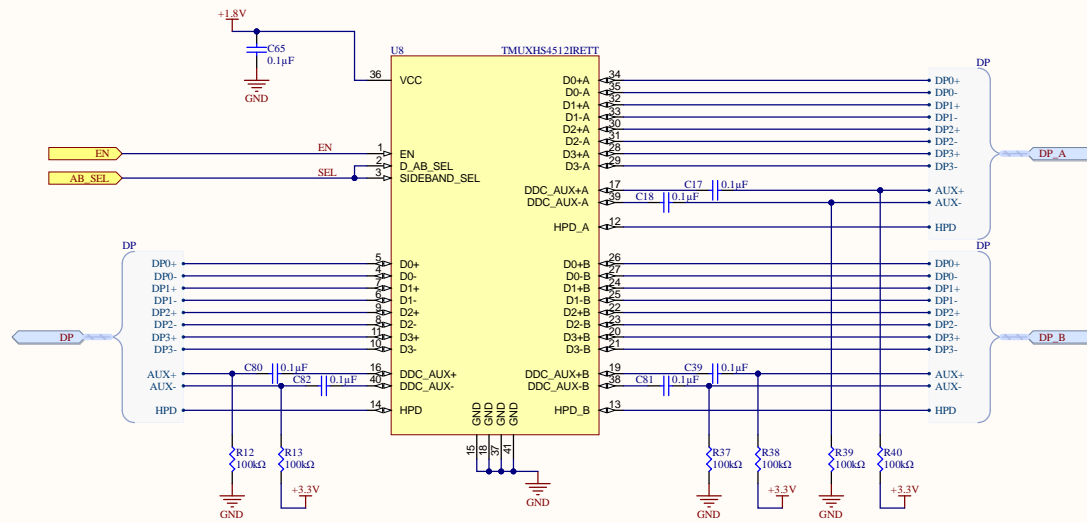


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<SCH DWG NO>
DWG NO. REV. 8

REVISION	DESCRIPTION	DATE	APPROVED



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	DWG. NO.	REV / BHT				
			REVISION	DESCRIPTION	DATE	APPROVED

