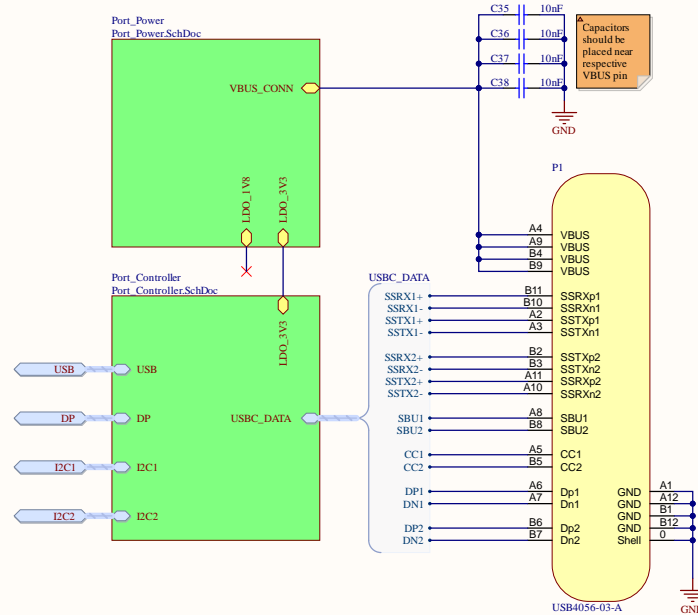


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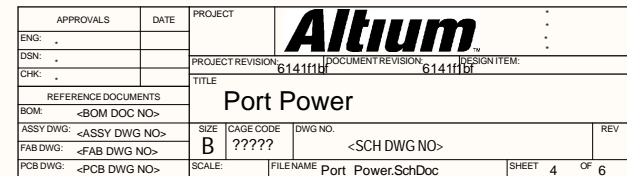
<SCH DWG NO>
DWG NO. REV. 2

REVISION	DESCRIPTION	DATE	APPROVED



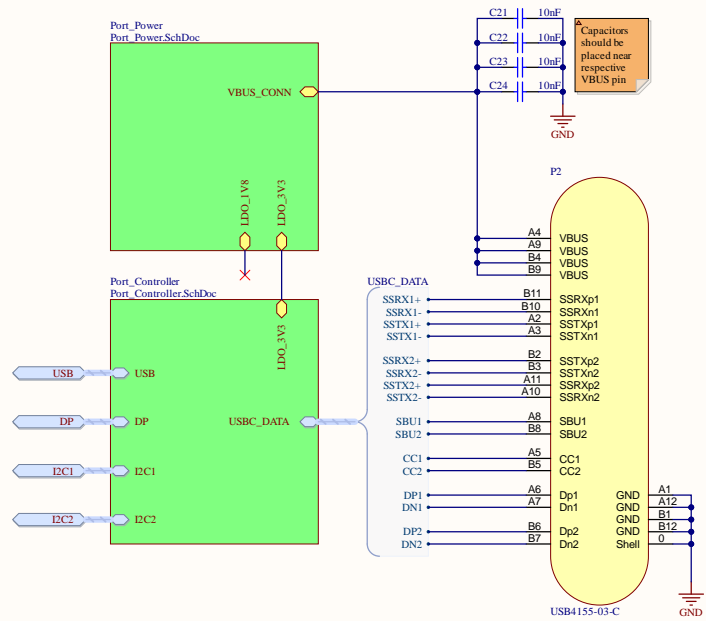
APPROVALS	DATE	PROJECT	Altium	
ENG: *		PROJECT REVISION: 61411111	DOCUMENT REVISION: 61411111	DESIGN ITEM: *
DSN: *		TITLE		
CHK: *		Downstream Port		
REFERENCE DOCUMENTS		SIZE	CAGE CODE	DWG NO.
BOM: <BOM DOC NO>		B	?????	<SCH DWG NO>
ASSY DWG: <ASSY DWG NO>		SCALE	FILENAME	SHEET 2 OF 6
FAB DWG: <FAB DWG NO>		Downstream Port.SchDoc		
PCB DWG: <PCB DWG NO>				

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						REVISION		DESCRIPTION	
								DATE	
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APPROVALS		DATE		PROJECT		Altium	
ENG:	•			PROJECT REVISION: 61411111		DOCUMENT REVISION: 61411111	
DSN:	•			DESIGN ITEM:			
CHK:	•			TITLE		Upstream Port	
REFERENCE DOCUMENTS							
BOM: <BOM DOC NO>							
ASSY DWG: <ASSY DWG NO>				SIZE: B		CAGE CODE: ?????	
FAB DWG: <FAB DWG NO>				DWG NO: <SCH DWG NO>		REV	
PCB DWG: <PCB DWG NO>				SCALE:		FILENAME: Upstream_Port.SchDoc	
				SHEET 5 OF 6			

8		7		6		5		4		3		<div><SCH DWG NO> DWG NO</div>		<div>6 REV</div>		2		1					
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APPROVALS		DATE		PROJECT		<div>Altium</div>	
ENG: *				PROJECT REVISION: 61411111		DOCUMENT REVISION: 61411111	
DSN: *				DESIGN ITEM:			
CHK: *				TITLE			
REFERENCE DOCUMENTS		IC Power					
BOM: <BOM DOC NO>							
ASSY DWG: <ASSY DWG NO>		SIZE		CAGE CODE		DWG NO.	
FAB DWG: <FAB DWG NO>		B		?????		<SCH DWG NO>	
PCB DWG: <PCB DWG NO>		SCALE:		FILENAME		IC_Power.SchDoc	
				SHEET		6 OF 6	

