




OPCODE = 16b
 REGS = 4b
 INM = 16b
 DIR SALTO = 10b

| INSTRUCCIÓN | OPCODE | | | |
|-------------|--------|------------|----------|----------|
| LI | 1000xx | RDEST (25) | INM (15) | |
| NOTI | 1001xx | RDEST(25) | INM (15) | |
| ADDI | 1010xx | RDEST(25) | R2 (21) | INM (15) |
| SUBI | 1011xx | RDEST (25) | R2 (21) | INM (15) |
| ANDI | 1100xx | RDEST (25) | R2 (21) | INM (15) |
| ORI | 1101xx | RDEST (25) | R2 (21) | INM (15) |
| NEGI | 1110xx | RDEST (25) | INM (15) | |
| MOV | 010000 | RDEST (25) | R1 (17) | |
| NOT | 010001 | RDEST (25) | R1 (17) | |
| ADD | 010010 | RDEST (25) | R2 (21) | R1 (17) |
| SUB | 010011 | RDEST (25) | R2 (21) | R1 (17) |
| AND | 010100 | RDEST (25) | R2 (21) | R1 (17) |
| OR | 010101 | RDEST (25) | R2 (21) | R1 (17) |
| NEG | 010110 | RDEST (25) | R2 (21) | |
| J | 001000 | DIR (9) | | |
| JZ | 001001 | DIR (9) | | |
| JNZ | 001010 | DIR (9) | | |
| JCALL | 001011 | DIR (9) | | |
| JR | 001100 | DIR (9) | | |


OPCODE


RDEST


R2


R1


INM


DIR

LI RDEST, INM



NOTI RDEST, INM



ADDI RDEST, R2, INM



SUBI RDEST, R2, INM



ANDI RDEST, R2, INM



ORI RDEST, R2, INM



NEGI RDEST, INM



MOV RDEST, R1



NOT RDEST, R1



ADD RDEST, R2, R1



SUB RDEST, R2, R1



AND RDEST, R2, R1



OR RDEST, R2, R1



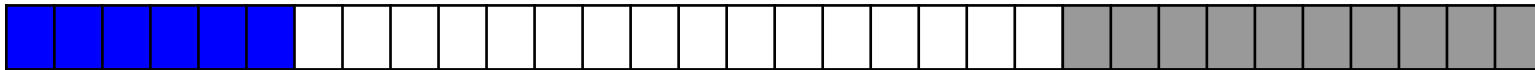
NEG RDEST, R2



J DIR



JZ DIR



JNZ DIR



JCALL DIR



JR DIR

