

# Experiment 7-two 16 bit multiplication

GNUSim8085 - 8085 Microprocessor Simulator

File Reset Assembler Debug Help

Registers

| Register | Value |
|----------|-------|
| A        | 1E    |
| BC       | 00 00 |
| DE       | 00 05 |
| HL       | 00 1E |
| PSW      | 00 00 |
| PC       | 42 1D |
| SP       | FF FF |
| Int-Reg  | 00    |

Flag

| Flag | Value |
|------|-------|
| S    | 0     |
| Z    | 1     |
| AC   | 0     |
| P    | 1     |
| C    | 0     |

Load me at

```
1 1hld 2001
2 xchg
3 1hld 2005
4 mov o,h
5 mvi a,00h
6 loop: add d
7 dor c
8 jnz loop
9 mov h,a
10 mov b,l
11 mvi a,00h
12 lp: add e
13 dor b
14 jnz lp
15 mov l,a
16 shld 2010
17 hit
```

Decimal - Hex Conversion

| Decimal | Hex |
|---------|-----|
| 0       | 0   |

I/O Ports

| Port | Value |
|------|-------|
| 0    | 00    |

Memory

| Address | Data |
|---------|------|
| 07D1    | 2001 |
| 07D2    | 2002 |
| 07D3    | 2003 |
| 07D4    | 2004 |
| 07D5    | 2005 |
| 07D6    | 2006 |
| 07D7    | 2007 |
| 07D8    | 2008 |
| 07D9    | 2009 |
| 07DA    | 2010 |
| 07DB    | 2011 |
| 07DC    | 2012 |
| 07DD    | 2013 |
| 07DE    | 2014 |

Simulator: Idle

Line No Assembler Message

```
0 Program assembled successfully
```