**DAILY ASSESSMENT FORMAT**

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| **Date:** | **27-5-2020** | **Name:** | **Archana H N** |
| **Course:** | **Logic Design** | **USN:** | **4AL18EC007** |
| **Topic:** | **1.Boolean equations for digital circuits. Combinational circuits: Conversion of MUX and Decoders to logic gates.**  **2.design of 7 segment decoder with common anode display** | **Semester & Section:** | **4th sem**  **A section** |
| **Github Repository:** | **Archana-course** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |
| **Report – Report can be typed or hand written for up to two pages.** |

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| **Date:** | **27-5-2020** | **Name:** | **Archana H N** |
| **Course:** | **Python** | **USN:** | **4al18ec007** |
| **Topic:** | **1.Application 5: Build a Desktop Database Application** | **Semester & Section:** | **4th sem A section** |
| **AFTERNOON SESSION DETAILS** | | | |
| **Image of session** | | | |
| **Report – Report can be typed or hand written for up to two pages.** | | | |