DAILY ASSESSMENT FORMAT

Date:	04/06/2020	Name:	Nichenametla Bhargavi
Course:	DIGITAL DESIGN USING HDL	USN:	4AL17EC061
Topic:	1. Hardware modelling using verilog 2. FPGA and ASIC Interview questions	Semester & Section:	6th Sem A sec
Github Repository:	Bhargavi_Nichenametla		

FORENOON SESSION DETAILS
Image of session
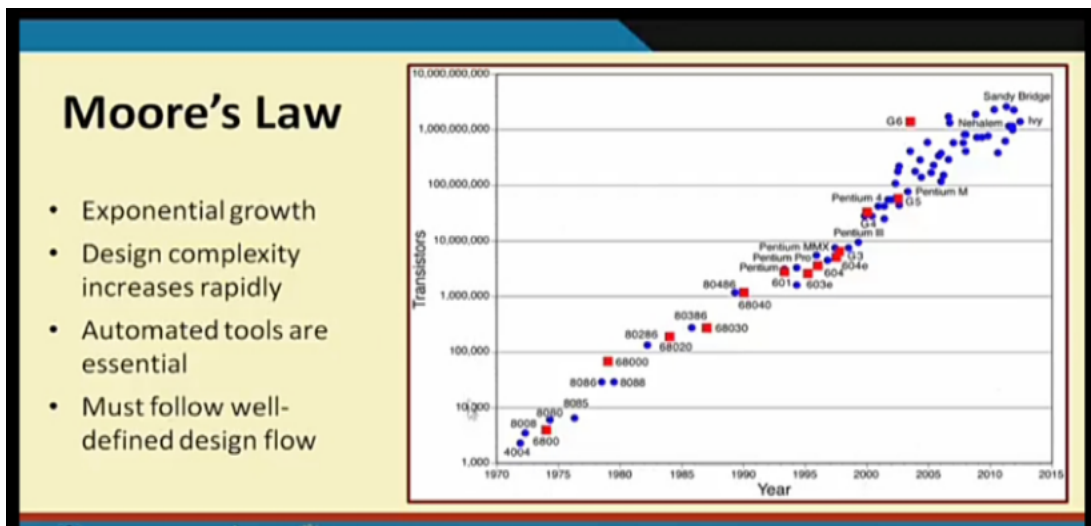
Report – Report can be typed or hand written for up to two pages

VLSI DESIGN FLOW:

1. Standardized deisgn Procedure.

2. Starting from the design idea down to the actual implementation.

3. Encompasses many steps:

  – Specification
  – Synthesis
  – Simulation
  – Layout
  – Testability Analysis

VLSI DESIGN PROCESS:

1. Design Complexity increasing rapidly:

  – Increased size and complexity
  – Fabrication Technology improving
  – CAD tools are essential
  – Conflicting requirements like area, speed and energy consumption.

2. The present trend:

  – Standardize the design flow
  – Emphasis on low power design, and increased performance.

Task:

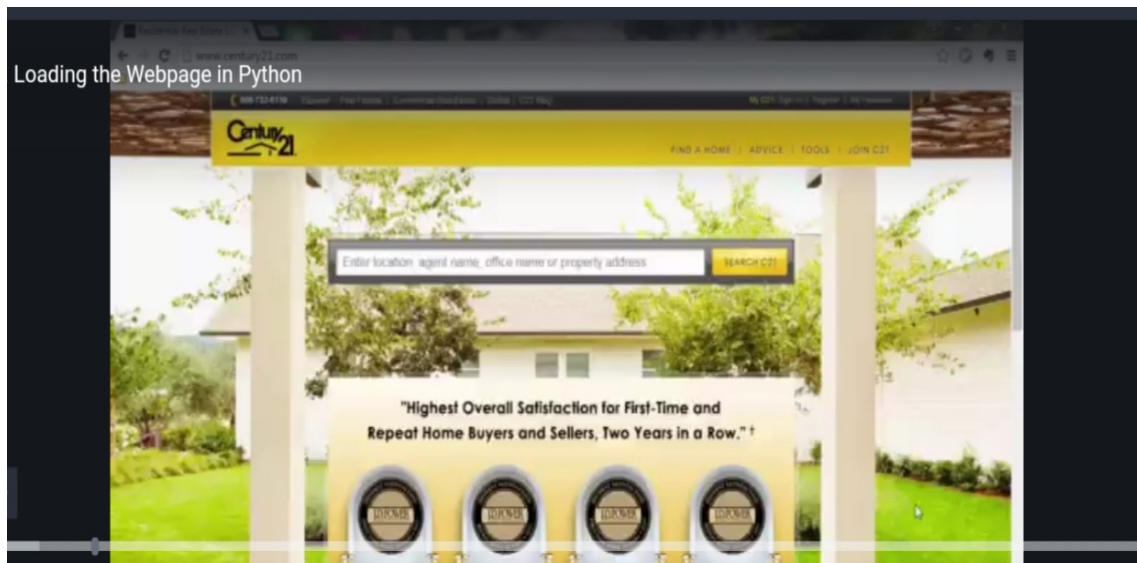**Implement a simple T Flipflop and test the module using a compiler.**
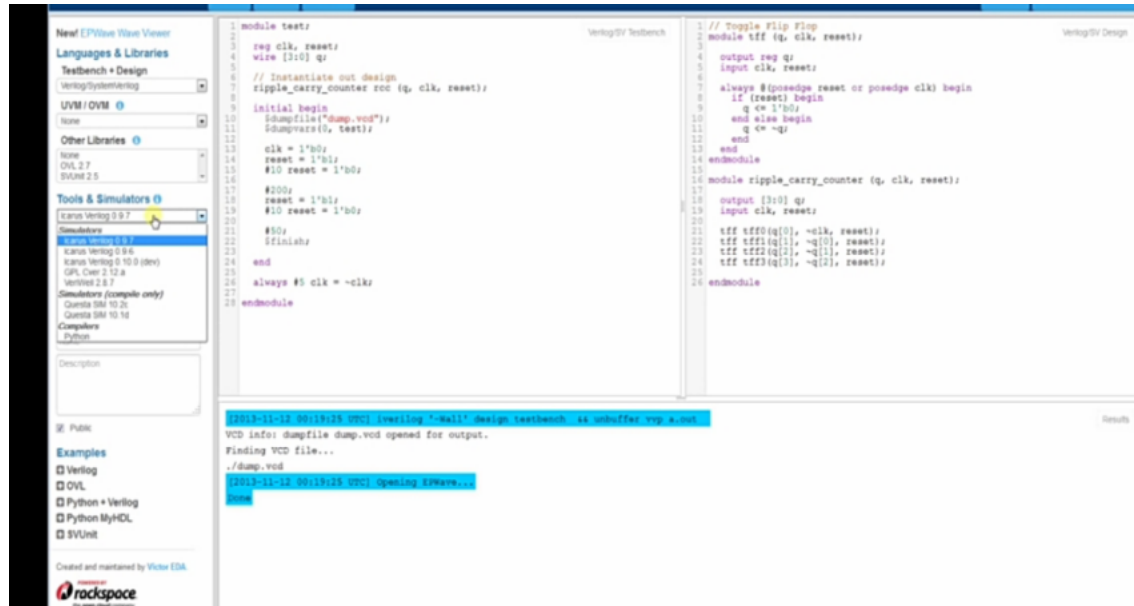
OUTPUT:

Date:	04/06/2020	Name:	Nichenametla Bhargavi
Course:	Python	USN:	4AL17EC061
Topic:	Application 8: Scrape Real Estate Property Data from the Web	Semester & Section:	6th Sem A sec

AFTERNOON SESSION DETAILS

Image of session

Report – Report can be typed or hand written for up to two pages.

Application 8: Scrape Real Estate Property Data from the Web:

1. Scraped Website Data - How The Output Will Look Like.

2. Loading the Web page in Python.

3. Extracting "div" Tags.

4. Extracting Addresses and Property Details.

5. Extracting Elements without Unique Identifiers.

6. Saving the Extracted Data in CSV Files.

7. Crawling through web pages use web scraping to collect email ID.

8. Web scraping is used to collect a large set of data.

9. Email address gathering.

10. Web scraping is used to collect data from Social Media websites.

11. Services such as ParseHub use web scraping to collect data from online shopping.

12. Details regarding job openings, interviews are collected from different websites.

Date:	04/06/2020	Name:	Nichenametla Bhargavi
Course:	Lecture on Electrical Engineering	USN:	4AL17EC061
Topic:	By Prof: Aniruddhan, IIT Madras	Semester & Section:	6th Sem A sec

Image of the session: