**DAILY ASSESSMENT**

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| **Date:** | **11/06/2020** | **Name:** | **Dhavala** |
| **Course:** | **Learn KiCad. Printed circuit Board Design** | **USN:** | **4AL17EC027** |
| **Topic:** | * **Add Footprint search path** * **Prepare production files.** | **Semester & Section:** | **6TH SEM & A Section** |
| **Github Repository:** | **Dhavala27** |  |  |

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| **FORENOON SESSION DETAILS** |
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| **Report**  Add footprint search path:  When creating a custom PCB footprint for a component, it is stored somewhere on your computer. In order for Design Entry CIS to find where a custom footprint is stored and associate it with a schematic component, the library search path must be changed so that Design Entry CIS knows where to look. Save your custom footprints in the symbols folder on your computer. Depending on how Cadence is installed on your computer, the full path should be similar to:  C:\Program Files\Cadence\SPB\_17.2\share\pcb\pcb\_lib\symbols  When creating a new footprint drawing, the New Drawing dialog box will show the default pathLaunch Design Entry CIS. Note the full path for the  Capture.ini file shown on the Start Page (see Figure 2). Depending on how Cadence is installed on your computer, the full path should be similar to:C:\Cadence\SPB\_Data-Silent\cdssetup\OrCAD\_Capture\17.2.0\Capture.ini  or, if you made a custom HOME variable:%HOME%\cdssetup\OrCAD\_Capture\17.2.0\Capture.iniThe  Capture.ini file will open in Notepad. Under the [Allegro Footprints] section, add the full library search path from step 1 above if it is not already listed (see Figure 4). Note that you must increment the number after Dir for  each path added (e.g.,Dir0,Dir1,Dir2).  Do not delete any existing paths from the list  You have successfully added a library search path to Design Entry CIS. If you are still not able to attach your custom footprints to schematic symbols, re-check the above steps and make sure your custom footprint name is correct.  What is a Gerber file?  The most widely used file format for PCB manufacturing is called Gerber.When manufacturers request “Gerbers” or “Gerber files,” they are referring to ASCII files that contain Gerber- formatted data. A Gerber file knows nothing about design rules, net connectivity, or component libraries; it is simply two-dimensional artwork that indicates where the manufacturing equipment will place copper,  solder mask, or silkscreen. One Gerber file provides information for one PCB feature on one layer.  Thus, if you have a two-layer board and each side has copper, solder mask, and silkscreen, you will need six Gerber files. You may also need a separate Gerber file to identify the board outline. Generating Gerber files can be somewhat complicated. The process involves various configuration details, and different manufacturers have different requirements. The following screen capture shows the options that you have to consider when generating Gerber files with DipTrac If you don’t have  much experience with Gerber generation, I suggest the following approach: First, choose a manufacturer that provides specific instructions on how to generate Gerber files with specific CAD tools. Second, use one of these CAD tools to design your board. If you follow the instructions carefully, you will almost certainly avoid the two potential consequences of improper. Gerber files: a delay in the manufacturing process (more likely), or a nonfunctional PCB (nowadays probably quite rare).  The drill file:  You will also need to generate a file that indicates the position and size of every hole that will be drilled through your board, i.e., both through-holes (for mounting components) and vias. This is called the NC (numeric control) drill file; you may also see “Excellon drill file” (which comes from Excellon Automation, a company that makes equipment used in PCB manufacturing). Again, the safest approach here is to follow specific instructions provided by a PCB manufacturer.  Project files vs. Manufacturing files:  If you’d prefer to avoid generating any type of manufacturing file, you can look for a PCB manufacturer that accepts your CAD software’s project files. I assume that the manufacturer uses some sort of automated procedure to generate Gerbers from the project file; this is beneficial not only because it saves you time but also because the fab house technicians will (presumably) know exactly how to generate files that are compatible with their equipment. The list in the next section gives some  information about one manufacturer that accepts project files |

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| **Date:** | **11/06/2020** | **Name:** | **Dhavala** |
| **Course:** | **mysql** | **USN:** | **4AL17EC027** |
| **Topic:** | **Section 10: PHP Functions**  **Section 11: Using External Files, And Images** | **Semester & Section:** | **6TH SEM & A Section** |
| **Github Repository:** | **Dhavala27** |  |  |

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| **Report** |