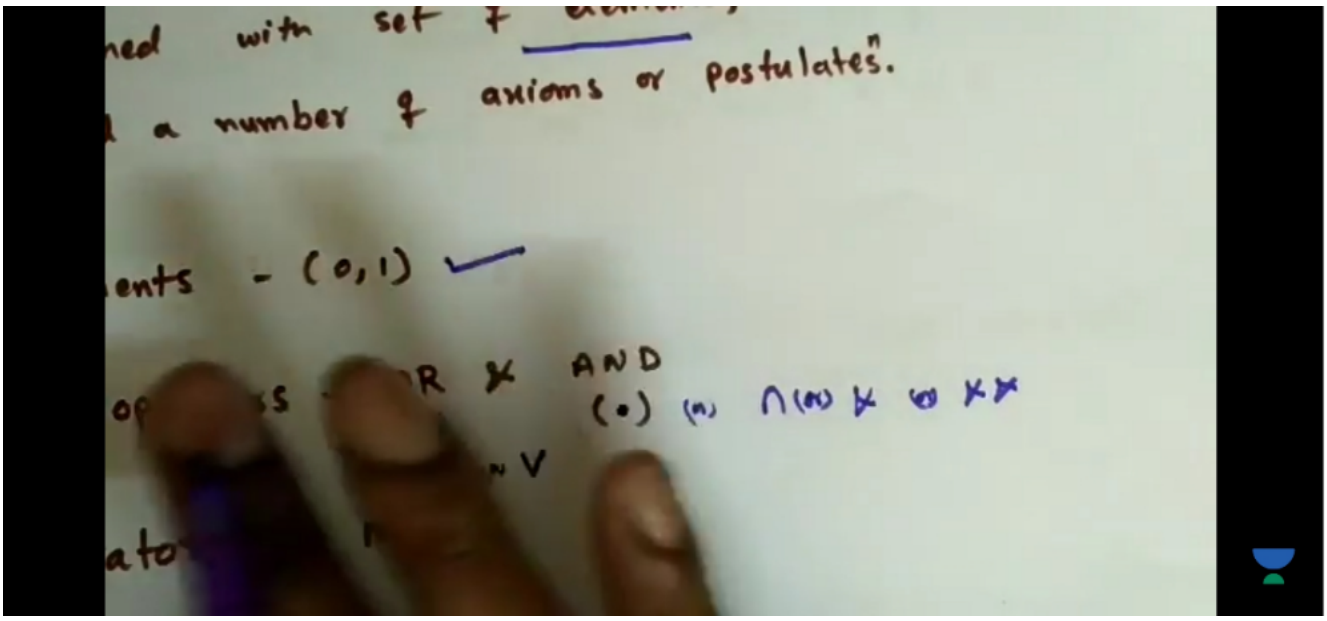
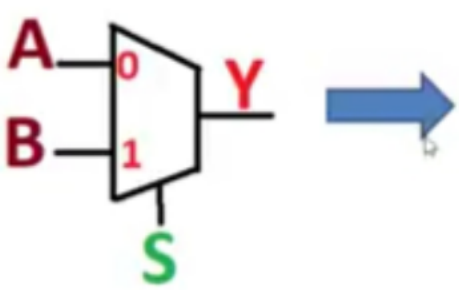
DAILY ASSESSMENT FORMAT

Date:	28-05-2020	Name:	Jagadeesha Hegde
Course:	Logic Design	USN:	4AL17EC036
Topic:	Boolean equations for digital circuits. Combinational circuits: Conversion of MUX and Decoders to logic gates. Design of 7 segment decoder with common anode display	Semester & Section:	6th A-sec
Github Repository:	Jagadeesha-036		

FORENOON SESSION DETAILS
Image of session





SELECTION (S)	OUTPUT(Y)
0	A
1	B

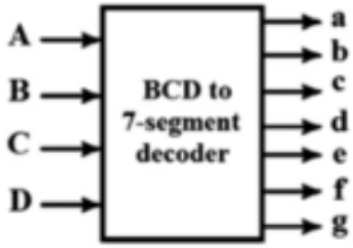
$2^n - 1$

2^n =inputs

n =selection lines

$Y = A\bar{S} + BS$

BCD to 7-segment decoder



A	B	C	D	a	b	c	d	e	f	g
0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	0	1	1	0	0	0	0
0	0	1	0	1	1	0	1	1	0	1
0	0	1	1	1	1	1	1	0	0	1
0	1	0	0	0	1	1	0	0	1	1
0	1	0	1	1	0	1	1	0	1	1
0	1	1	0	1	0	1	1	1	1	1
0	1	1	1	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1	1	1	1
1	0	0	1	1	1	1	1	0	1	1

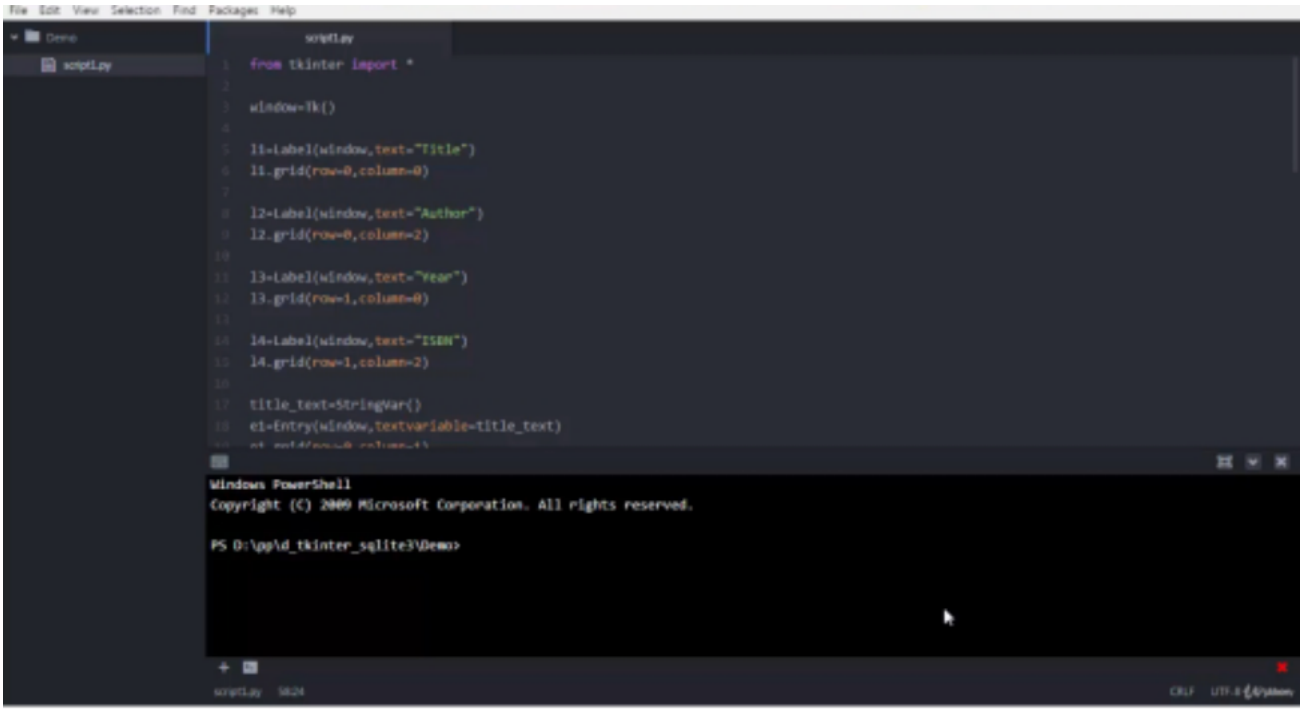
Report – Report can be typed or hand written for up to two pages.

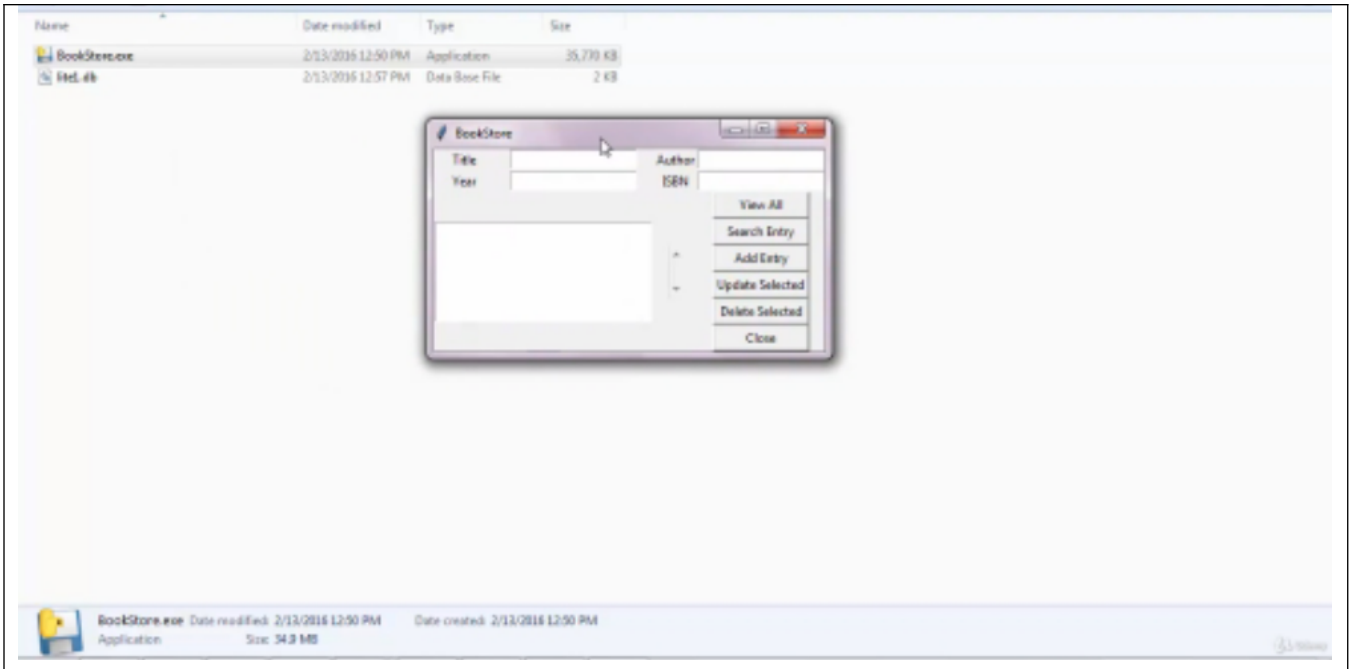
Boolean algebra is used to simplify Boolean expressions which represent combinational logic circuits. It reduces the original expression to an equivalent expression that has fewer terms which means that less logic gates are needed to implement the combinational logic circuit.

Date:	28-05-2020	Name:	Jagadeesha Hegde
Course:	The Python Mega Course	USN:	4AL17EC036
Topic:	Application 5: Build a Desktop Database Application	Semester & Section:	6th A-sec

AFTERNOON SESSION DETAILS

Image of session

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Date modified
Type
Size
BookStore.exe
2/13/2016 12:50 PM
Application
35,770 KB
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BookStore
Title
Year
Author
ISBN
View All
Search Entry
Add Entry
Update Selected
Delete Selected
Close
BookStore.exe Date modified: 2/13/2016 12:50 PM
Date created: 2/13/2016 12:50 PM
Application
Size: 34.8 MB

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Application 5: Build a Desktop Database Application

```
l1 = Label (window, text="title")
l1.grid (column=0, row=0)
l2 = Label (window, text="Author")
l2.grid label (row =0, column= 2)
bt = Button (window, text="view all", widtg = 12)
```