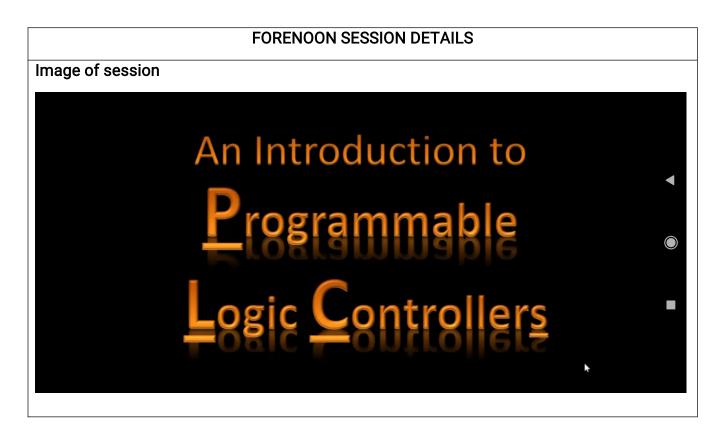
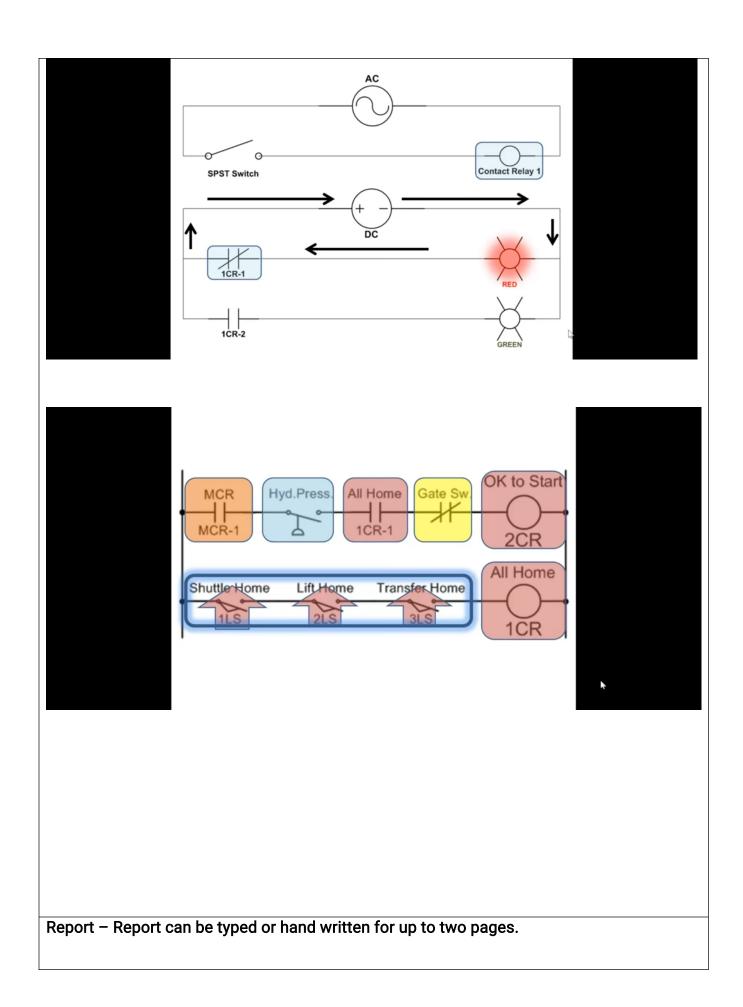
DAILY ASSESSMENT FORMAT

Date:	30-05-2020	Name:	Jagadeesha Hegde
Course:	Logic Design	USN:	4AL17EC036
Topic:	Applications of Programmable logic controllers:	Semester & Section:	6th A-sec
Github Repository:	Jagadeesha-036		





Programmable logic controllers:		
PLC is a special form of microprocessor- based controller. It includes a programmable		
memory to store instructions and to implement functions such as logic, sequencing,		
timing, counting.		
A PLC program is usually written on a computer and then is downloaded to the controller		
Most PLC programming software offers programming in Ladder Logic, or "C".		

Date: 30-05-2020 Name: Jagadeesha Hegde

Course: The Python Mega USN: 4AL17EC036

Course

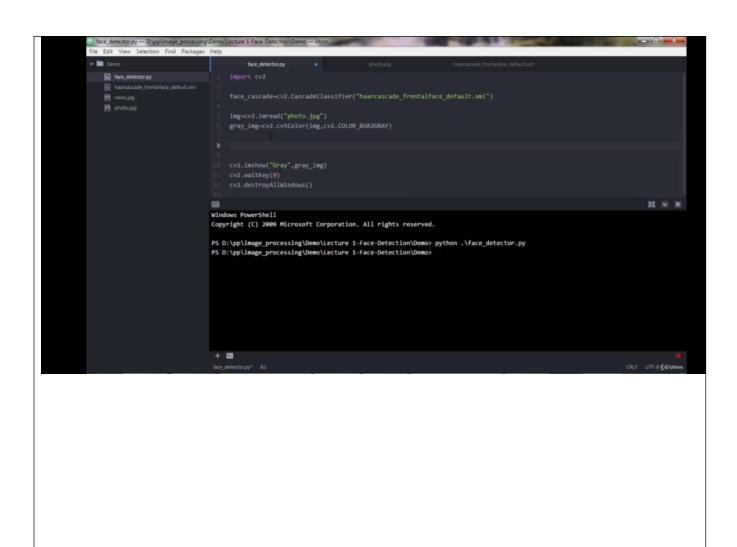
Topic: Python for Image and Semester & 6th A-sec

Video Processing with Section:

OpenCV

AFTERNOON SESSION DETAILS

Image of session



Report – Report can be typed or hand written for up to two pages.		
•	Processing a video means, performing operations on the video frame by frame. Frames are nothing but just the particular instance of the video in a single point of time. We may have multiple frames even in a single second. Frames can be treated as similar to an image.	
•	Image Processing - It focuses on image manipulation.	
•	Pattern Recognition - It explains various techniques to classify patterns.	
•	Photogrammetry is concerned with obtaining accurate measurements from images.	

