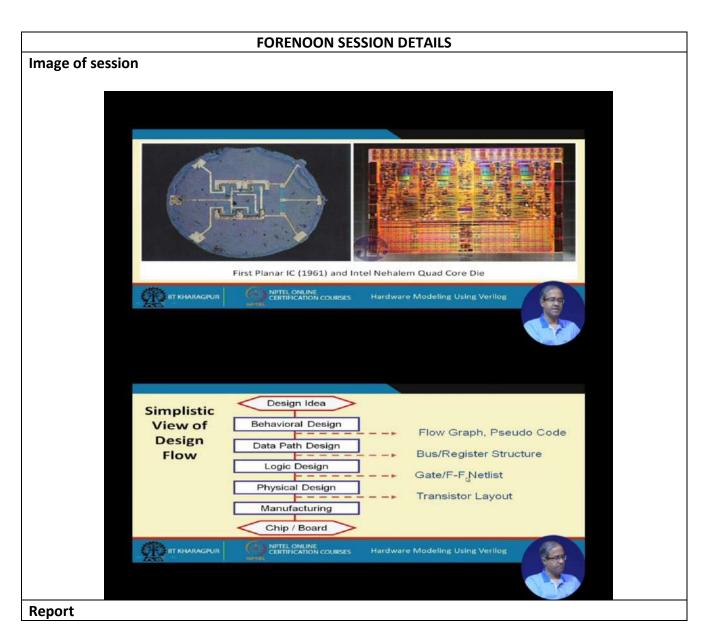
## **DAILY ASSESSMENT FORMAT**

Date:	04/06/2020	Name:	Lavanya B
Course:	Verilog HDL	USN:	4al17ec043
Topic:	Hardware modeling using verilog	Semester	6th A
	FPGA & ASIC interview questions	& Section:	
Github	Lavanya-B		
Repository:			



```
Hardware modelling using laiting .

VIST - basic handware chip
    * Design complexity invitating napidly

- Tourisated first and templexity

- Fabrication steknology impopuling

- corp foots are executed

- confirmed inclusionable like area, speed a margy

constant priors

- The priess friend
    VIST Disgre process
             - Standardise the duign stem.
- Europeanis on his-power duign, increased performance
    Moore's law
          Exponential growth

Design compliancy increase napidly
Automated took are throughly
Muse follow but defined design flow
   COMOS - FINEET - Quantum
 VIST Traign Flow
  - specification
  - Synthesis
  - Layout
  - Telabitity analysis
      a sampating title
     FPOLA and ASIC TECHNOLOUS SUMMONE
    1) Doop contents by a req
      e with temp mig =
             almys a (povidge ums)
             temp- b
       b-a:
a funp:
but
* without temp reg:
            abrays (posidas don)
begin

be-a

ond
  a) Blotting(=): The whole ideterment is done before control passes on the most protessing
   soon-blottings -) Evaluate all the right-hand order for the currence three unit and assigns the ligh hand order at the and of the form unit
3) Task That our capable of mothing a function assurant exacting other various of task function. These are made to enable a task however
     functions can enable other functions
```

```
+ gainly of Evenion graphs one commence - with an memory
                                                      changes_
          * Verilog full care - The which all south care - agreement
                      known patters can be matched to a core term or too.
            verilag parallel case. The which is is only parable to make a case expression to one and entry one case them.
        + signals updated first those variable
     * Executivity litt - Endicate that when a thomas occurs to any one of demand in the list change begin and stockness include that all get received
     * System take - fairplay , surplays, sauchaya, saichlayo
                                        sports sporteb, funiteh funite
4 P.7.5 - Papuliku milihaminin 40 accus inturnal diababasis of the
                   simulator from the Eprogram
                                 module of law, mand, 1,00);
import case, mand, 1;
evaport region
                                        biglise.
 by the second of the second of
                                          14 u. (.ax(etx), . non (non), +(1), 0(0))
                                      If up (.dx(tx), naw (rate), t(s), b(a))

shows #5 ax = rax;

initial begin

finat ax x + (xo;

finerals) & (raining c.cx),

not (xo;

for (ivigan ivo; (xo; i) iv) begin

axis (ivi) & (raining c.cx);

end (v) & (raining c.cx);

for (ivigan ivo; (xo; i) iv) begin

axis (ivi) & (raining c.cx);

end (v) & (raining c.cx);

and (v) & (raining c.cx);
```

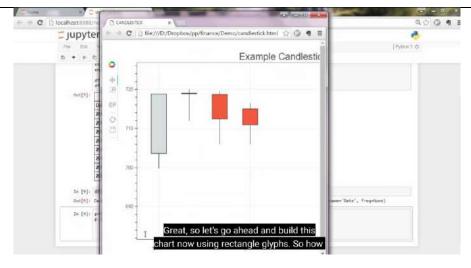
Date: 04/06/2020 Name: Lavanya B Course: Python USN: 4al17ec043

Topic: Application 09 Semester 6th A

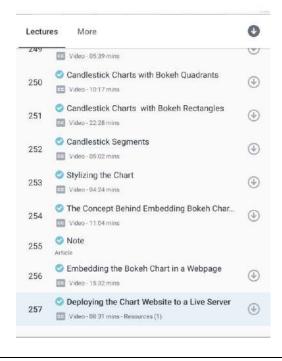
& Section:

## **AFTERNOON SESSION DETAILS**

Image of session







## Report

## Application 09: Build a web-based functional graphs

```
Application on Built a webband financial groups
          -condition charming is used for available in that we
           · Downstanding Total very with python-
factor foundar datornal importated a
                                     impone dozetine
               resort masters pato condition charts
                             Remi - dardini datetine (2016 3 .)
                                         tred = datetime datetime(2011,310)
            toud = datetime datetime(2016,370)

dy - date addressina (memor = 5000, 3000, 30000 = bjahoo.

Serbedding - doe 8000 = 1000 + 100 = 1000

- Joan - Hase Impore flate, under tempatt

Opp - flate ( vane -)

Out - (100)

dy teme ( )

quettin tender tempate (Thomas Lemi)

Out - 1000 ( 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1000 + 1
                           dy about ( ) tude template ( "about ham! )
actions touch temporal about him

if "nown - 2 = "main"

app thin (allow thinks think temporal

from flow impose flark temporal

app = Flow [ xame ]

app = flow [ xame ]

app = flow [ xame ]
                       arapp moure (*/plot*)?

different pondas dataseades longons data
immarti datatime
from basels ploting import faces, thou, our pushfall
from bosch switch import amponed;
from bosch switch import amponed;
                 cione dateline dateline (sele (1,1)

Lua - Adeline dateline (sele (10)

de doia data riadin (nam. " Coort, data selent - yakar",

Roose selent (nam. and ).
* platting { rayous lider! " }
            I'm blue comment!
                    can class "about";
can my about page(14.)
can my about page(14.)
                      4 / mid block / 4
```