**DAILY ASSESSMENT FORMAT**

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| --- | --- | --- | --- |
| **Date:** | **01/06/2020** | **Name:** | **Lepakshi T V** |
| **Course:** | **Digital system design using HDL** | **USN:** | **4AL17EC044** |
| **Topic:** | **Industrial application of FPGA** | **Semester & Section:** | **6th sem A sec** |
| **Github Repository:** | **Lepakshi-044** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |
| **Report – Report can be typed or hand written for up to two pages.** |

**DAILY ASSESSMENT FORMAT**

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| --- | --- | --- | --- |
| **Date:** | **01/06/2020** | **Name:** | **Lepakshi T V** |
| **Course:** | **Python** | **USN:** | **4A117EC044** |
| **Topic:** | **Motion detection** | **Semester & Section:** | **6th sem A sec** |
| **Github Repository:** | **Lepakshi-044** |  |  |

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| **FORENOON SESSION DETAILS** |
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