**DAILY ASSESSMENT FORMAT**

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| **Date:** | **28/05/2020** | **Name:** | **Lepakshi T V** |
| **Course:** | **Logic design** | **USN:** | **4AL17EC044** |
| **Topic:** | **Boolean design for digital circuits, Laws and MUX to logic gate conversions** | **Semester & Section:** | **6th sem, A sec** |
| **Github Repository:** | **Lepakshi-044** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Report – Report can be typed or hand written for up to two pages.** |

**DAILY ASSESSMENT FORMAT**

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| **Date:** | **28/05/2020** | **Name:** | **Lepakshi T V** |
| **Course:** | **Python** | **USN:** | **4AL17EC044** |
| **Topic:** | **Build a desktop database application** | **Semester & Section:** | **6th sem, A sec** |
| **Github Repository:** | **Lepakshi-044** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Report – Report can be typed or hand written for up to two pages.** |