

## DAILY ASSESSMENT FORMAT

Date:	04-06-2020	Name:	M V Ramya
Course:	logic design	USN:	4AL17EC045
Topic:	hardware modeling using verilog	Semester & Section:	6th sem, A sec
Github Repository:	M V Ramya-045		

### FORENOON SESSION DETAILS





### Verilog Hardware Description Language

- Dhw behavioral and structural design styles
- To write test benches and analyze simulation results
- To model combination and sequential circuits
- To distinguish the good and coding practices

Task :- Implement a simple T-FlipFlop and test module using a compiler

```
module tff (input clk, input rstn, input t, output reg q);
always @ (posedge clk)
begin
if (rstn)
q <= 0;
else
if (t) q <= ~q;
else
q <= q;
end
endmodule
```

### Test bench code

```
module tb;
reg clk;
reg rstn;
reg t;
tff u0 (.clk(clk), .rstn(rstn), .t(t), .q(q));
always #5 clk = ~clk;
initial
begin
{rst, clk, t} <= 0;
$monitor ("T = %0t rstn = %0b t = %0d q = %0d", $time,

```

```
repeat (2) @ (posedge clk);
rstn <= 1;
```

```
for (integer i = 0; i < 20; i = i + 1)
```

```
begin
```

```
reg [4:0] dly = $random;
```

```
#(dly) t <= $random;
```

```
end
```

```
#20 $finish
```

```
end
```

```
endmodule
```

Date: 04 June 2020

Name: MV Ramya

Course: python

USN: 4AL17EC045

Topic: Application to  
build a web based  
financial graph

Semester &  
Section:

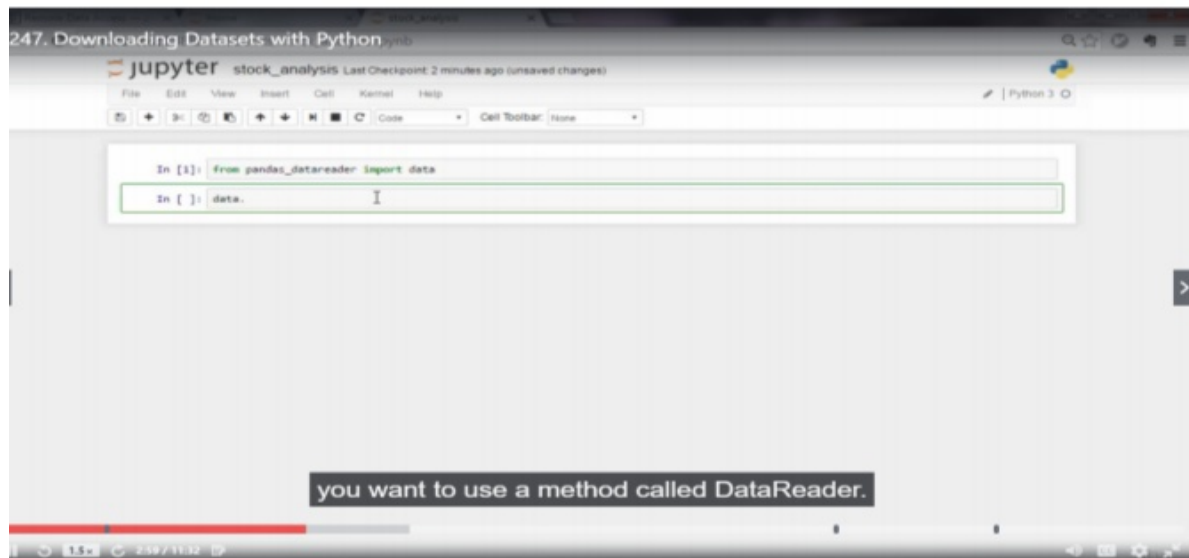
6th sem Asec

### AFTERNOON SESSION DETAILS

Image of session



Edit with WPS Office



## Python

04/06/2020

- In this section we learnt analyzing stock market Data, plotting stock market Data candlestick charts, Updating candlestick charts with Bolek Quadrants.
- learnt a plot candlestick charts with Bolek Rectangles, creating candlestick segments, Stylizing the obtained chart.
- learnt the concept Behind Embedding Bolek, Sharing the chart in a Flask Webpages
- learnt how to Embed the Bolek chart in a webpage and also learnt to Deploy the chart website to a Live server.

