

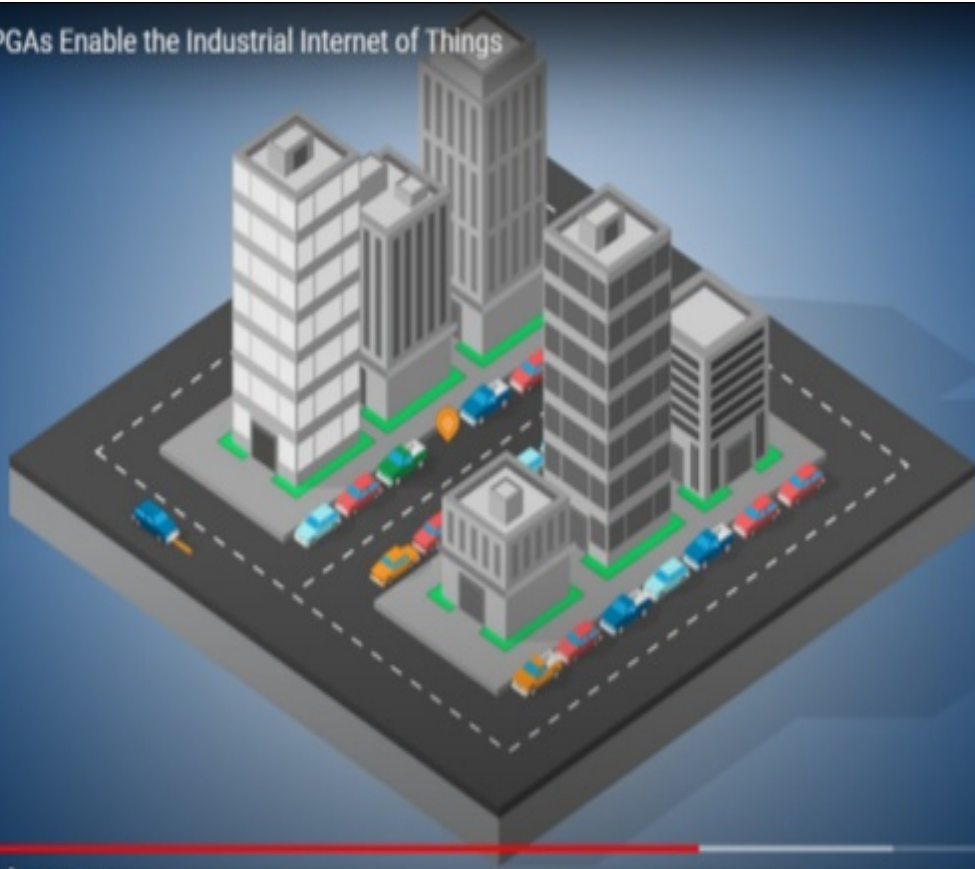
DAILY ASSESSMENT FORMAT

Date:	01-06-2020	Name:	M V Ramya
Course:	logic design	USN:	4AL17EC045
Topic:	industry application of FPGA	Semester & Section:	6th sem, A sec
Github Repository:	M V Ramya-045		

FORENOON SESSION DETAILS



How Intel FPGAs Enable the Industrial Internet of Things



1:29 / 2:15



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Report

11/06/2020

- FPGA stands for Field Programmable Gate Array. It is an integrated circuit which can be 'field' programmed to work as per the intended design.
- Automation - Intel FPGA and SoC industrial automation solution enable industrial system designers to reduce costs and time to market significantly for factory automation system designs.
- Embedded vision - Intel FPGA and SoC solutions provided a rapid development path with the flexibility to adapt evolving challenges and solutions for a wide range of video and intelligent vision application.
- Industrial IoT - Intel FPGA or SoC, you can better meet evolving standards for your design while increasing performance and scalability demands for mission-critical system function.
- Accelerating innovation - In 3 minutes, see how Intel FPGAs enable industry 4.0 and Internet of Things across a wide variety of industry application like industrial automation, smart energy and intelligent vision.
- FPGA is not suited for high-volume mass production while ASIC is suited for very high-volume mass production.
- Analog designs are not possible with FPGA's but ASICs can have complete analog circuitry. for ex:- WiFi transceivers.
- FPGA's are highly suited for applications such as Routers, Cell phone Base station etc while ASICs are definitely not suited for application areas where the design might need to be upgraded frequently.

Write a verilog code to implement NAND gate in different styles.

1] Gate level Code :-

```
module nand-2_gate(output, y, input, A, B);  
    wire yd;  
    and (yd, A, B);  
    not (y, yd);  
endmodule.
```

2] Data-Flow Code:-

```
module NAND-2_dataflow(output, y, input, A, B);  
    assign y = ~(A & B);  
endmodule.
```

3] Behavioral Modelling Code:-

```
module NAND-2_behavioral(dout, y, inp, A, B);  
    always @(A or B)  
    begin  
        if (A == 1'b1 & B == 1'b1) begin  
            y = 1'b0;  
        end  
        else  
            y = 1'b1;  
        end  
end
```

Date: 01 June 2020

Course: python

Topic: build a
webcam motion
detector

Name: MV Ramya

USN: 4AL17EC045

Semester &
Section:

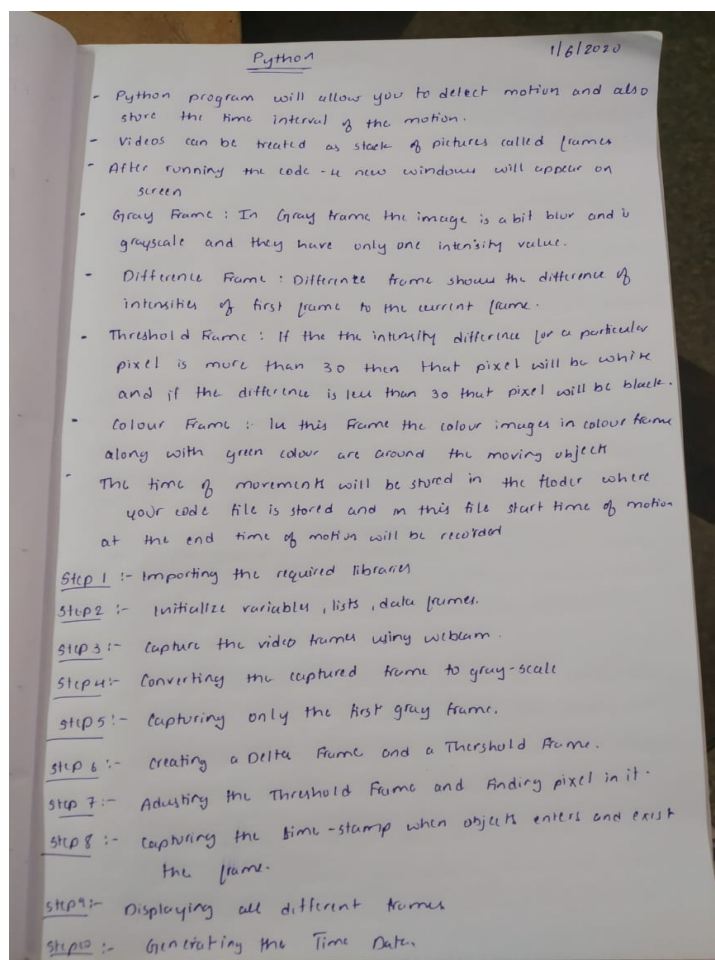
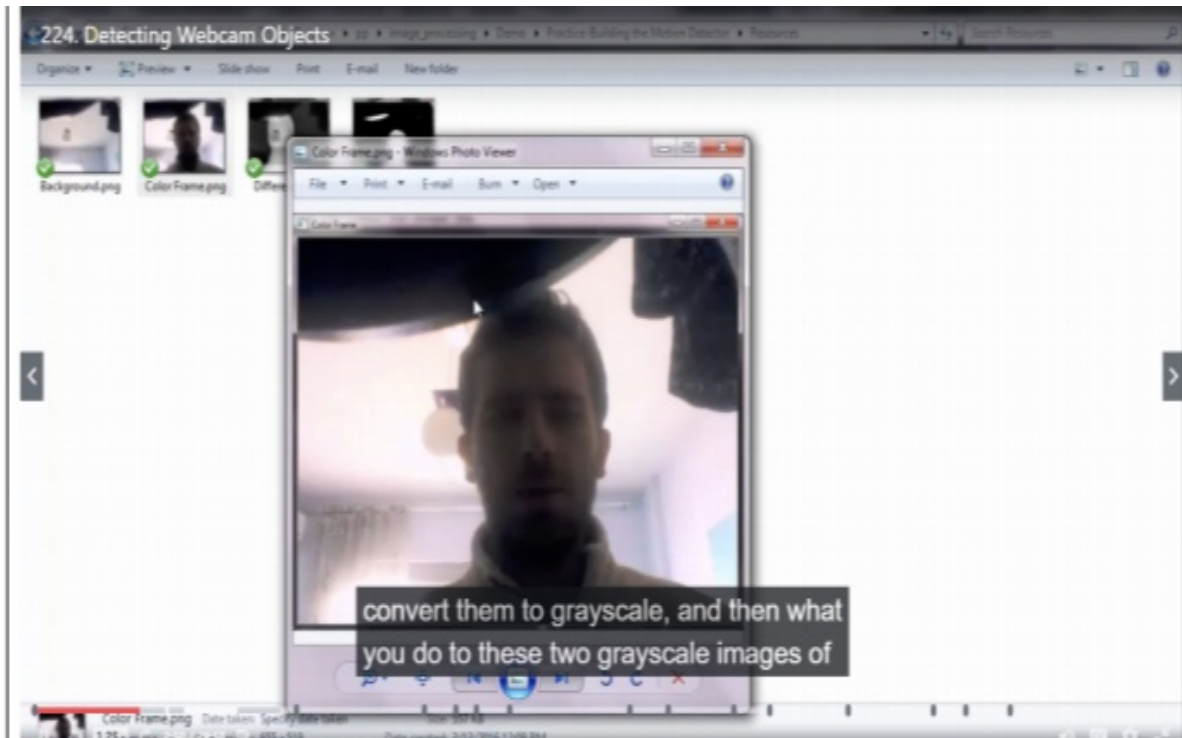
6th sem Asec

AFTERNOON SESSION DETAILS



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M V Ramya

is here by awarded the certificate of achievement for
the successful completion of

Step into Robotic Process Automation

during GUVI's RPA **SKILL-A-THON** 2020



S.P. Balamurugan

Co-founder, CEO

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