

Forenoon Session:

Date: / /

Date: 24th May.

Name: Rakshita, C.U.

Course: Logic Design

USN: 4AM8EC042.

Topic: Boolean equations for digital Circuits.

Sem & sec: IV Sem & A'sec

GitHub Repository: Rakshita.

Report:-

Boolean equations for digital Circuits.

Combinational Circuits: Conversion of MUX and Decoders to logic gates.

→ Cost of the Circuit.

→ Simple realization of a Circuit.

"It is defined with a set of elements, a set of operators, & a NO. of axioms or postulates."

Set of elements = $\{0, 1\}$

Two binary operators - OR & AND
(+) (\cdot)

Unary operator - NOT

Axioms or postulates of Boolean algebra are a set of logical expressions upon which we can build a set of useful theorems.

"AND operation"

"OR operation"

"NOT operation"

$$\rightarrow 0 \cdot 0 = 0$$

$$\rightarrow 0 + 0 = 0$$

$$\rightarrow \overline{0} \text{ or } 0' = 1$$

$$\rightarrow 0 \cdot 1 = 0$$

$$\rightarrow 0 + 1 = 1$$

$$\rightarrow \overline{1} = 0$$

$$\rightarrow 1 \cdot 0 = 0$$

$$\rightarrow 1 + 0 = 1$$

$$\rightarrow 1 \cdot 1 = 1$$

$$\rightarrow 1 + 1 = 1$$

* In Boolean Algebra

In ordinary algebra.

$$A + A = A$$

$$A \cdot A = A$$

$$A + A = 2A$$

$$A \cdot A = A^2$$

$$1 + 1 = 1$$

$$1 \cdot 1 = 1$$

$$1 + 1 = 2$$

$$1 \cdot 1 = 1$$

* In Binary NO. Sys:-

$$1 + 1 = 10$$

$$1 \cdot 1 = 1$$

Axioms and postulates:-

$$\rightarrow x + 0 = x$$

$$\rightarrow x + \overline{x} = 1$$

$$\rightarrow x \cdot 1 = x$$

$$\rightarrow x + 1 = 1$$

$$\rightarrow (\overline{\overline{x}}) \text{ (or) } (x')' = x$$

$$\rightarrow x \cdot x = x$$

$$\rightarrow x + x = x$$

$$\rightarrow x \cdot 0 = 0$$

$$\rightarrow x \cdot \overline{x} = 0$$

1) Commutative law:-

2) Associative law:-

3) Distributive law:-

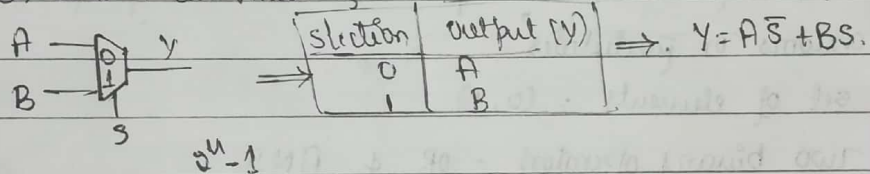
Theorems of Boolean algebra:

1) Absorption Theorem: $x + xy = x$.

$$\rightarrow x(1+y) = x.$$

$$x + \bar{x}y = x + y.$$

MUX TO Logic Gates:

1) NAND, NOR \rightarrow universal gatesInverter design:- $Y = 1 \cdot \bar{A} + 0 \cdot A \Rightarrow Y = \bar{A}$.

Design of 7 Segment decoder with Common anode display.

BCD to 7 Segment decoder.

