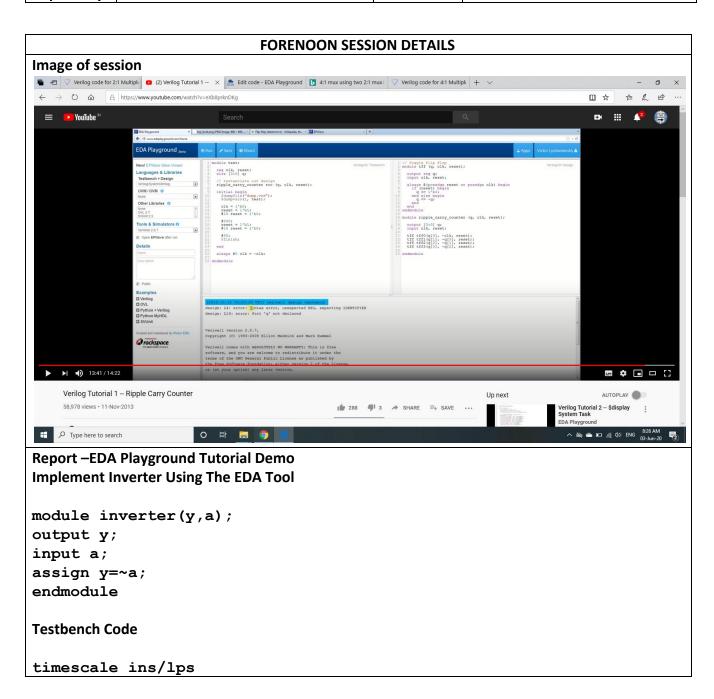
## **REPORT JUNE 03**

Date:	03 JUNE 2020	Name:	Rakshith B
Course:	Digital Design Using HDL	USN:	4AL16EC409
Topic:	EDA Playground Online compiler, EDA Playground Tutorial Demo Video, How to Download And Install Xilinx Vivado Design Suite, Task for Day-3	Semester & Section:	6th SEM B
Github Repository:	Rakshith-B		

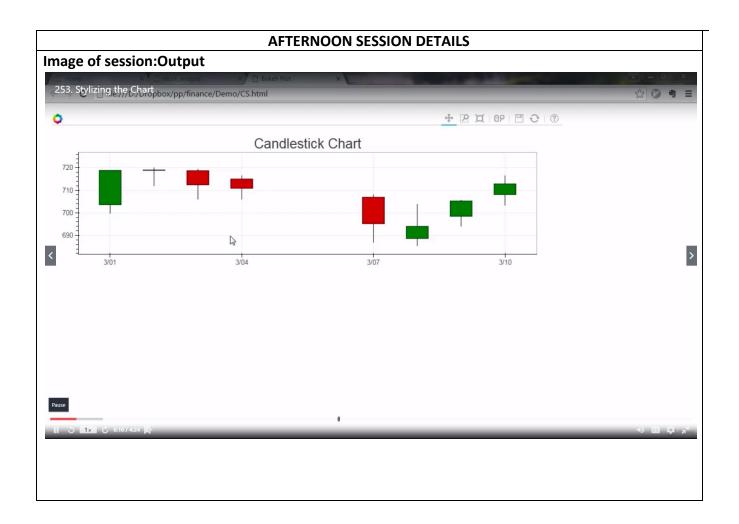


```
module testbench();
reg a1;
wire y1;
inverter inv1(a1,y1);
initial begin
    a1=a'b1;
    $display("a=%b",a1);
end
endmodule
Ripple Carry Counter
module ripple_counter_4_bit(q,clk,reset);
input clk,reset;
output[3:0]q;
T FF tff0(q[0],clk,reset);
T FF tff1(q[1],q[0],reset);
T FF tff2(q[2],q[1],reset);
T FF tff3(q[3],q[2],reset);
endmodule
module T FF(q,clk,reset);
input clk,reset;
output q;
wire d:
D FF dff0(q,d,clk,reset);
not n1(d,q);
endmodule
module D FF(q,d,clk,reset);
input d,clk,reset;
output reg q;
always@(negedge clk or posedge reset)
begin
if(reset)
q \le 1 b0;
else
q \le d;
end
endmodule
TestBench Code
module test
  reg clk,reset;
  wire(3:0)q;
  ripple_carry_counter rcc(q,clk,reset);
  initial begin
```

```
$dumpfile("dump.vcd");
     $dumpvars(1,test);
    clk=1'b0;
    reset=1'b1;
    #10 reset=1'b0;
    #200;
  always #5 clk=~clk;
endmodule
Implement 4 to 1 MUX using structural modelling style and test the module in an
online/offline compiler.
library IEEE;
use IEEE.STD LOGIC 1164.ALL;
entity mux2 1 is
   port(A,B : in STD LOGIC;
   S: in STD LOGIC;
   Z: out STD LOGIC);
   end mux2 1;
architecture Behavioral of mux2 1 is
begin
process (A,B,S) is
begin
if (S = '0') then
 Z \leq A;
 else
 Z \leq B;
end if;
end process;
end behavioral;
library IEEE;
use IEEE.STD LOGIC 1164.ALL;
entity mux4_1 is
port(
A,B,C,D : in STD_LOGIC;
S0,S1: in STD LOGIC;
Z: out STD LOGIC
);
end mux4_1;
architecture Behavioral of mux4 1 is
component mux2 1
port( A,B : in STD LOGIC;
 S: in STD LOGIC;
 Z: out STD_LOGIC);
end component;
signal temp1, temp2: std logic;
begin
m1: mux2_1 port map(A,B,S0,temp1);
m2: mux2_1 port map(C,D,S0,temp2);
m3: mux2 1 port map(temp1, temp2, S1, Z);
end behavioral;
```

Date: 03 JUNE 2020 Name:RAKSHITH B
Course: Python On Udemy USN:4AL16EC409

Topic: Build a Web-based Financial Graph Semester & Section: 6 B



```
Build a Web-based Financial Graph
from flask import Flask, render_template
app=Flask(<u>name</u>)
@app.route('/plot/')
def plot():
    from pandas datareader import data
   import datetime
   import fix_yahoo_finance as yf
   yf.pdr_override()
   from bokeh.plotting import figure, show, output file
   from bokeh.embed import components
   from bokeh.resources import CDN
   start=datetime.datetime(2015,11,1)
    end=datetime.datetime(2016,3,10)
    df=data.get data yahoo(tickers="GOOG", start=start, end=end)
   def inc_dec(c, o):
        if c > o:
            value="Increase"
        elif c < o:
            value="Decrease"
        else:
            value="Equal"
        return value
    df["Status"]=[inc_dec(c,o) for c, o in zip(df.Close,df.Open)]
    df["Middle"]=(df.Open+df.Close)/2
    df["Height"] = abs (df.Close-df.Open)
   p=figure(x axis type='datetime', width=1000, height=300)
   p.title.text="Candlestick Chart"
   p.grid.grid_line_alpha=0.3
    hours 12=12*60*60*1000
```

```
p.segment(df.index, df.High, df.index, df.Low, color="Black")
   p.rect(df.index[df.Status=="Increase"],df.Middle[df.Status=="Increase"],
          hours_12,
df.Height[df.Status=="Increase"],fill_color="#CCFFFF",line_color="black")
   p.rect(df.index[df.Status=="Decrease"],df.Middle[df.Status=="Decrease"],
          hours 12,
df.Height[df.Status=="Decrease"],fill color="#FF3333",line color="black")
   script1, div1 = components(p)
   cdn_js=CDN.js_files[0]
   cdn_css=CDN.css_files[0]
   return render template("plot.html",
   script1=script1,
   div1=div1,
   cdn css=cdn css,
   cdn_js=cdn_js )
@app.route('/')
def home():
    return render_template("home.html")
@app.route('/about/')
def about():
   return render_template("about.html")
if name ==" main ":
   app.run(debug=True)
```