Forenoon Session

Date | Page

Date: 27/5/20
Name: Sneha.G
Course: Logic Design
USN: 4AL18EC080
Topic: • Boolean equation for digital ckt
Sem & Sec: IV, A
• Combinational circuits
• Design of 7 segment decoder
Github Repository: Sneha-G19

Report:

⇒ Boolean equation for digital circuits:

- Variable used can have only two value HIGH or LOW
- Complement of a variable is represented by a bar
- OR opⁿ of the variables is represented by a plus
- Logical AND operation of the two or more variable is represented by writing a dot

⇒ Combinational Circuits: It is a ckt which have i/p's & single o/p depending on control or select i/p's for N. i/p lines, or we can say that for 2^n i/p lines, n selection lines are required. Multiplexers are also known as "Data n selector, parallel to serial converter, may to one circuit

A Decoder is a combinational circuit that converts binary info. from i/p to o/p lines. Apart from the o/p lines, a decoder may also have an Enable i/p line

⇒ Design of 7-segment:

- The basic idea involves driving a common cathode 7-segment LED display using CLC
- The logic circuit is designed with 4 i/p's & 7 outputs, each representing an i/p display
- Using k-map, logic for each i/p to display

Date: 07/5/2020 Afternoon Session Name: Sneha. G

Course: Python USN: 4AL18EC050

Topic: Application: How output will look Sem & Sec: IV, 'A'

Github Repository: Sneha-G19

Report.

⇒ How output will look like:

- User interface design
- Frontend & Backend Interface
- Connecting the Frontend to the Backend fixing the bug
- creating the Standalone Executable Version of the Program