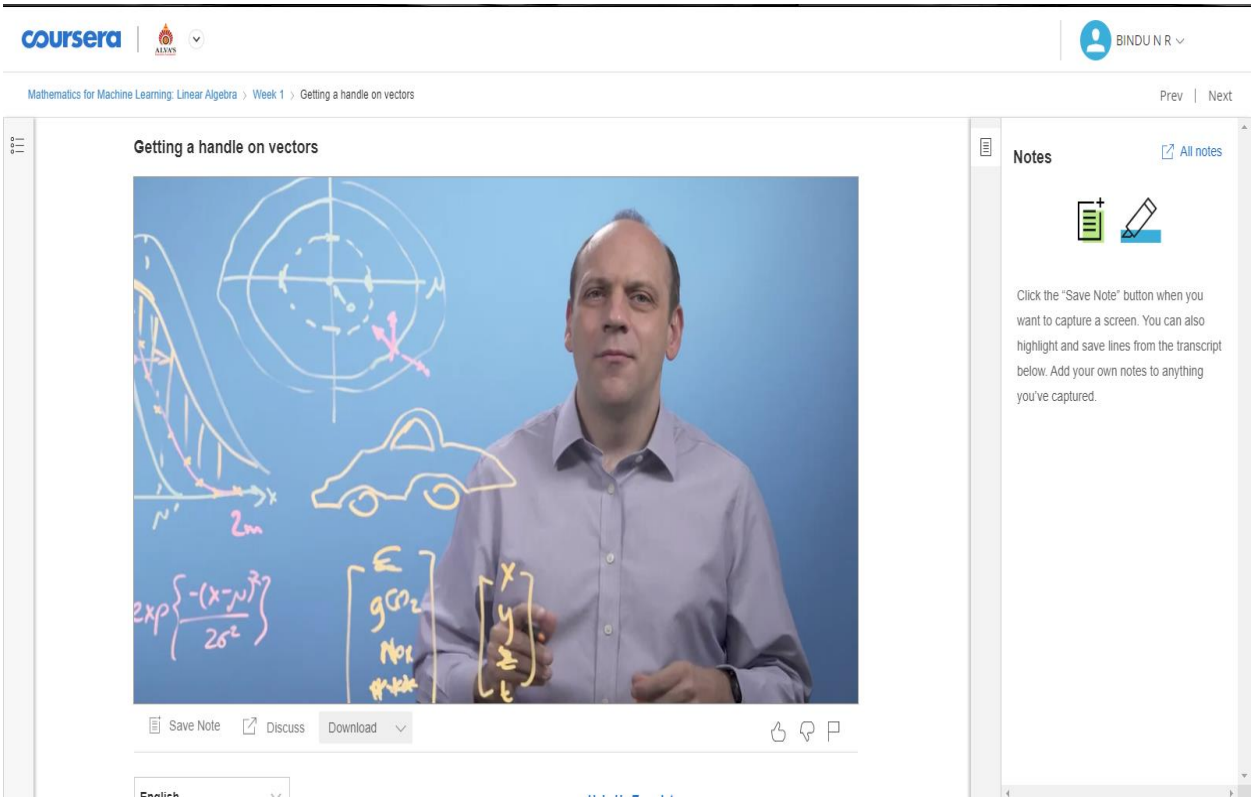
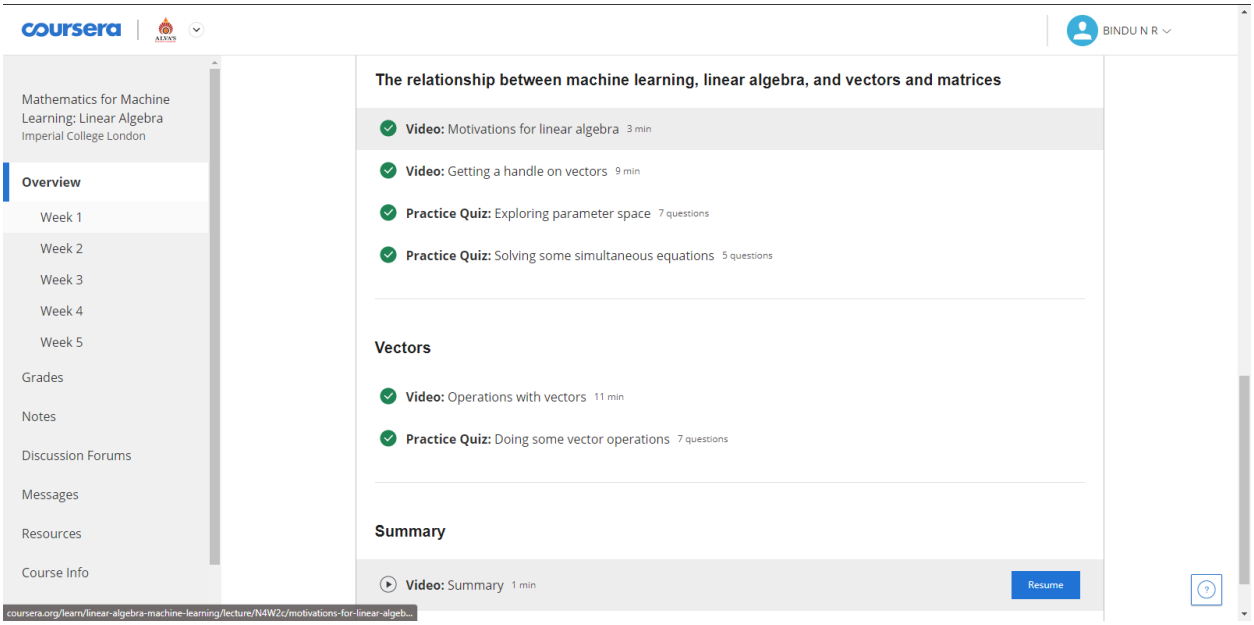
DAILY ASSESSMENT FORMAT

Course:	**Mathematics for Machine learning**	**Name:**	**Bindu.N.R**
Link :	https://www.coursera.org/learn/linear-algebra-machine-learning/home/welcome	**USN:**	**4AL17EC101**
Org By:	**Coursera**	**Semester & Section:**	**6-B**
Github Repository:		**Date:**	**13/07/2020**

Topic Completed Today

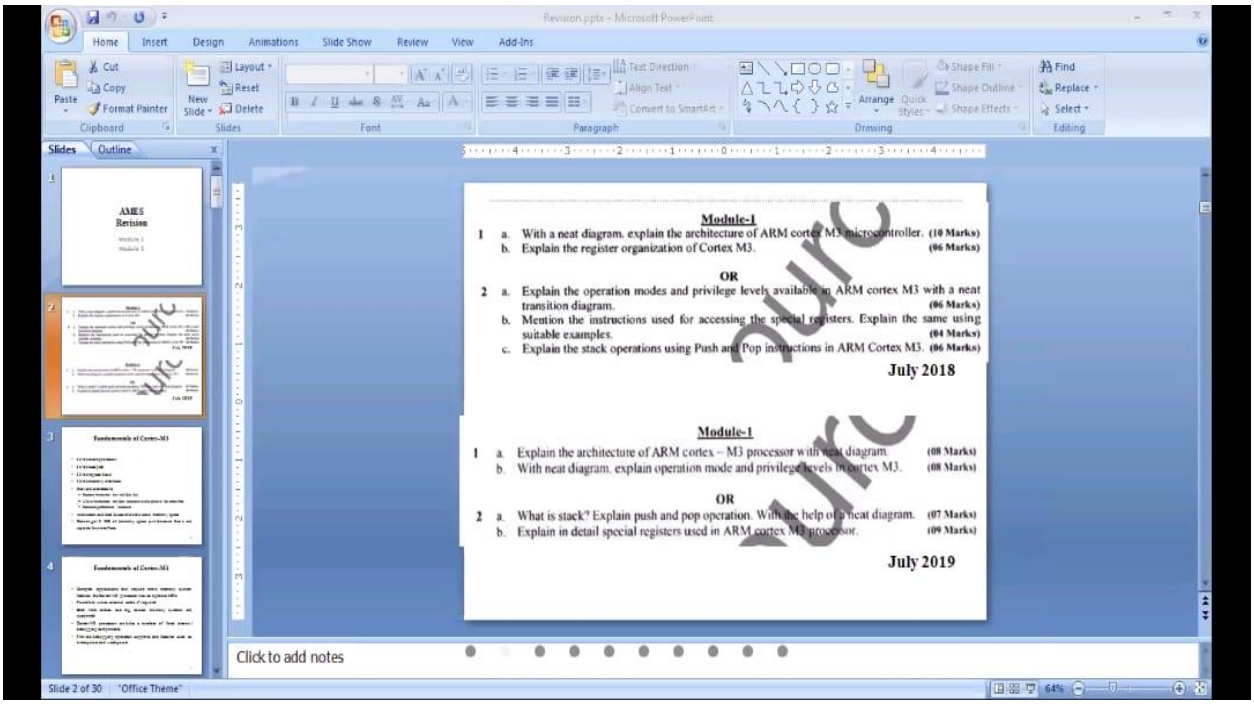


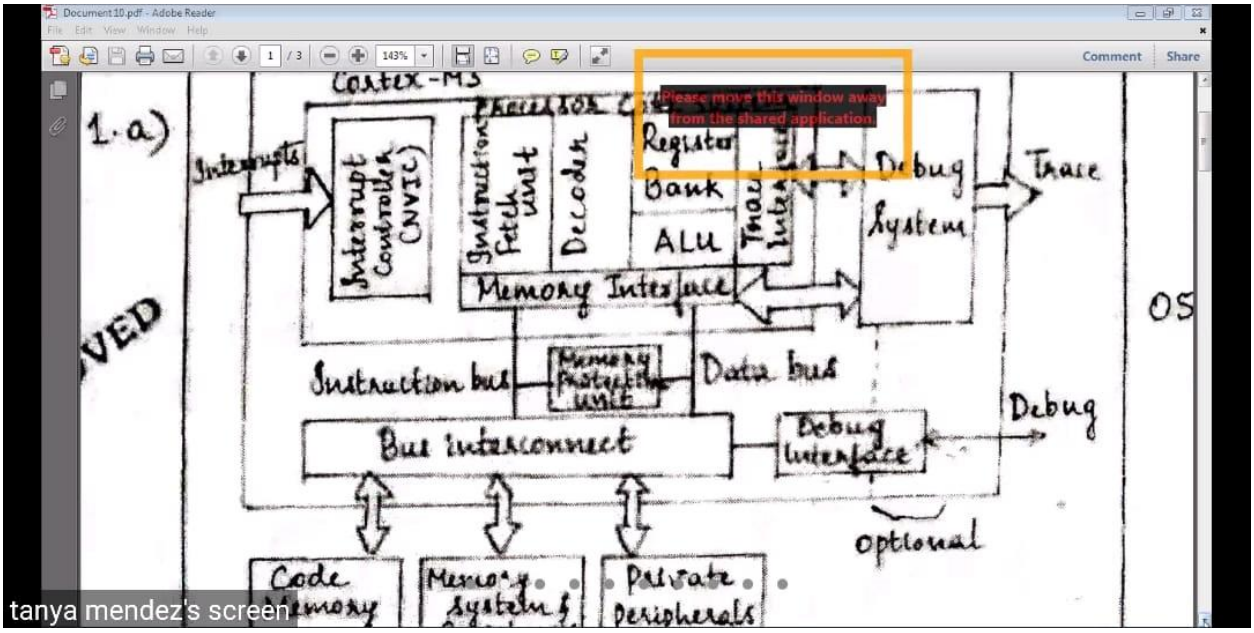


All the quizzes for week 1 are completed.

Revision Class for today (Module 1 and 2): -

By Asst. Prof. Tanya Mendez

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Cortex-M3
1.a)
Interrupts
Interrupt Controller (NVIC)
Instruction Fetch unit
Decoder
Register Bank
ALU
Trace Interface
Debug System
Trace
Memory Interface
OS
Instruction bus
Memory Protection unit
Data bus
Debug
Bus interconnect
Debug Interface
optional
Code Memory
Memory System
Private Peripherals
tanya mendez's screen