DAILY ASSESSMENT REPORT

Date:	**04 June 2020**	**Name:**	Kishan shetty
Course:	**DIGITAL DESIGN USING HDL**	**USN:**	**4AL17EC041**
Topic:	**Hardware modelling using Verilog**	**Semester & Section:**	**6th sem & A sec**
GitHub Repository:	**Kishanshetty-041**		

FORENOON SESSION DETAILS

Objective of Hardware Modeling

- Learn about the Verilog hardware description language.
- Understand the difference between behavioral and structural design styles.
- Learn to write test benches and analyze simulation results.
- Learn to model combinational and sequential circuits,
- Distinguish between good and bad coding practices.
- Case studies with some complex designs.

VLSI Design Process

- Design complexity increasing rapidly
- Increased size and complexity
- Fabrication technology improving
- CAD tools are essential
- Conflicting requirements like area, speed, and energy consumption
- The present trend
- Standardize the design flow
- Emphasis on low-power design, and increased performance

Moore's Law

- Exponential growth
- Design complexity increases rapidly
- Automated tools are essential
- Must follow well defined design flow

Need to use Computer Aided Design (CAD) tools.

- Hardware Description Language (HDL)
- Based on HDL provide formats for representing the outputs of various design steps
- A CAD tool transforms its HDL input into a HDL output that contains more detailed information about the hardware.
    - ❏ Behavioral level to register transfer level
    - ❏ Register transfer level to gate level
    - ❏ Gate level to transistor level
    - ❏ Transistor to the layout

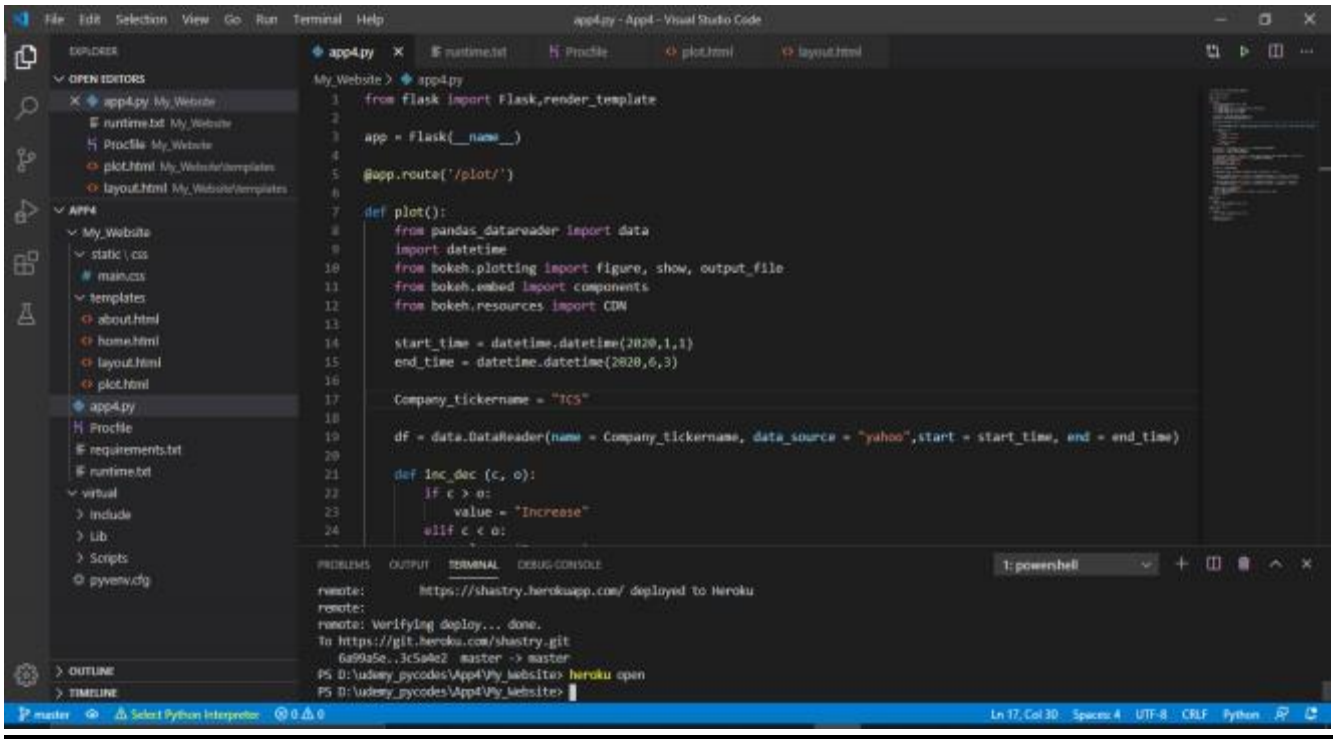
Task

Implement a simple T Flipflop and test the module using a compiler.

```
module t_ff (t,q,clk);
input t,clk;
output reg q = 0;
always @ (posedge clk)
begin
    if (t==1)
       begin
          q=~q;
      end
    else
      begin
         q=q;
      end
end
 endmodule
```

Date:	04/06/2020	Name:	Kishan shetty
Course:	The Python Mega Course: Build 10 Real World Applications	USN:	4AL17EC041
Topic:	Application 8: Build a Web-based Financial Graph	Semester & Section	6 th ‘A’
Github Repository	Kishanshetty-041		

Image of session



Application 8: Build a Web-based Financial Graph

- Python script to plot stock market data using bokeh library and deploy the bokeh plot to a live website

. • A ticker symbol or stock symbol is an abbreviation used to uniquely identify publicly traded shares of a particular stock on a particular stock market. A stock symbol may consist of letters, numbers or a combination of both. "Ticker symbol" refers to the symbols that were printed on the ticker tape of a ticker tape machine.

• Some of the examples are:

- NYSE (New York Stock Exchange) uses the ticker symbol with 3 letters or few – such as 'NYT' for the New York Times Co. or 'T' for AT&T.
- Symbols with 4 or more letters generally denote securities traded on the American stock exchange and NASDAQ.
- Those ending in 'X' indicate mutual funds.
- There are also certain symbols that denote specific status or type of security say, tickers ending in 'Q' indicate issuers which are under bankruptcy and letter 'Y' denotes security is an ADR.