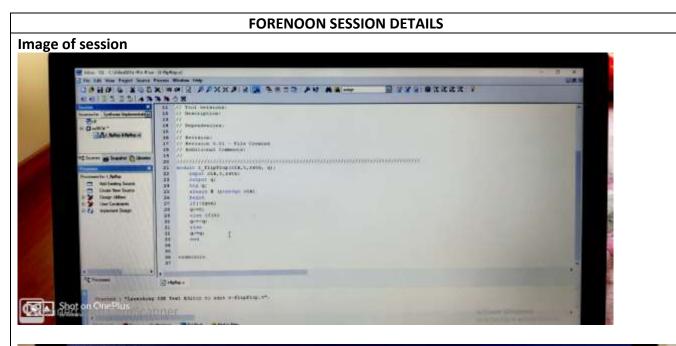
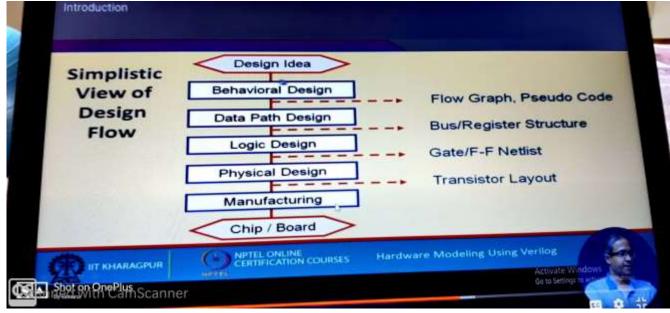
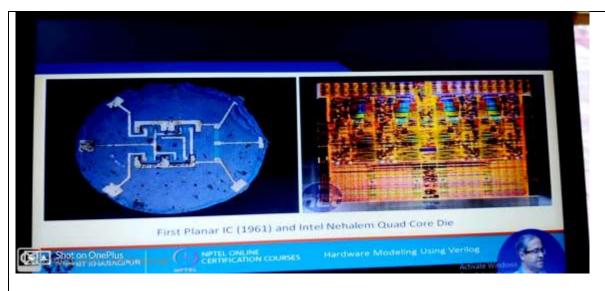
DAILY ASSESSMENT FORMAT

Date:	04 june 2020	Name:	Pooja K S
Course:	HDL	USN:	4AL17EC070
Topic:	Hardware modelling using Verilog	Semester	6 [™] SEM B SEC
	FPGA and ASIC interview questions	& Section:	
Github	pooja-shivanna		
Repository:			













Handware Modelling using Verilin VLSI Draign Protest * Design Complexity increasing socially increased six and complicity - Fabrucation technology improving - CAD took one evential -> Conflicting suggistments are asso. speed and every Consumption Moore's Low Exponential Growth Design complexity inoccuses rapidly Automated tool are eventual Must follow well-defined design flow. VLSI delign flow-Standardized design procedure -> starting from the during idea down to the actual implimentation. Encompanies many stops -- specification -> Sunthuiu -> Simulation -> Layout - Testability analysis Weed to use computer Aided Design (COD) tooks - Based on 4 hardware Description Language > HOLS perovide formats for sepresenting the ou

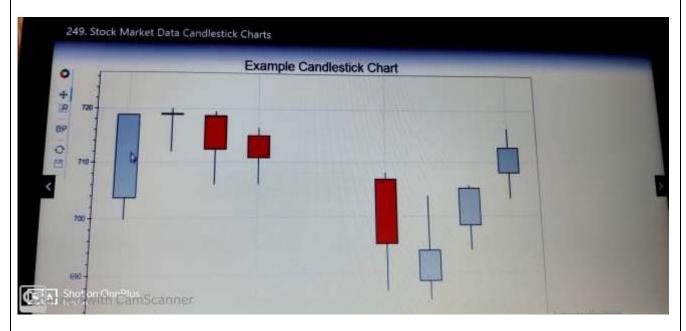
· various design steps I) (AD took transforms to HDL input ento a HDL culput that contains more detailed information about the hardware. withern tolan blocking and the children . Oask Dayu: Emplement a simple of thip flop and test the module using a compiler module tit (input cle, input sign, input to output sug a); always @ (peredge clo) begin of (I sister) 9 L= 0, else if (t) 9 = 9 end endmodule Scanned with CamScanner

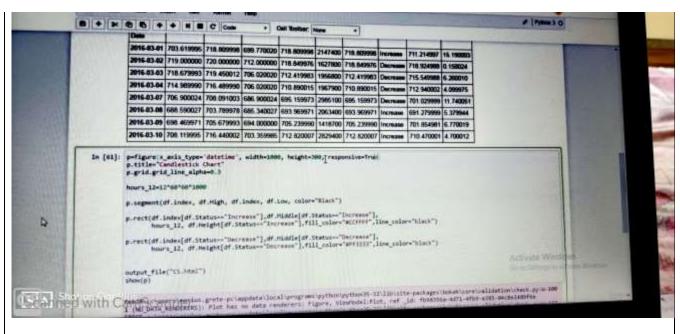
Date:	04 june 2020	Name:	Pooja K S
Course:	Python Programming	USN:	4AL17EC070
Topic:	Application 9: Build a web-based	Semester &	6 th sem B sec
	financial graph	Section:	

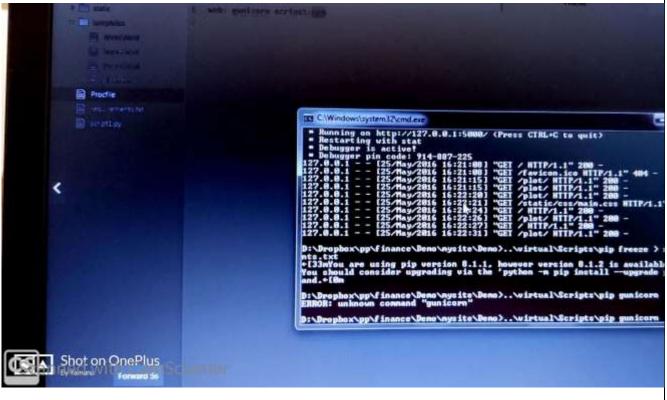
AFTERNOON SESSION DETAILS

Image of session









Application 9 - Build a web - based Day 16 Financial Gaph Downloading Datasets with Tython: from poundas-dataseades import data import datetime start = data time. date time (2016, 3, 1) end = date time - date time (2016, 3,1) data - Data Reader Chame = " AAPL" - data - 2000 = "yahoo", start = storend-on) from teask import teask, sender - template app = Flask (_ name_) Capp. noute ('1') def home (): neturn nender template (" home . html") @ app. noute ('/ about /') del about (): return render - template ("about - hitml") "If - name _ = = " _ main_": app sun (debug - True) Scanned with CamScanner

	Collecting heights Please fill the entries to get population statistics on height		
	Erder your ernall address:		
	Enter your beingst room		
	Character Control of the Control of		
Shot on OnePlus			