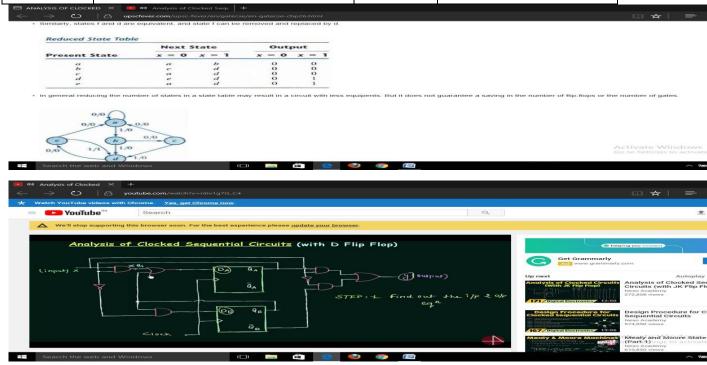
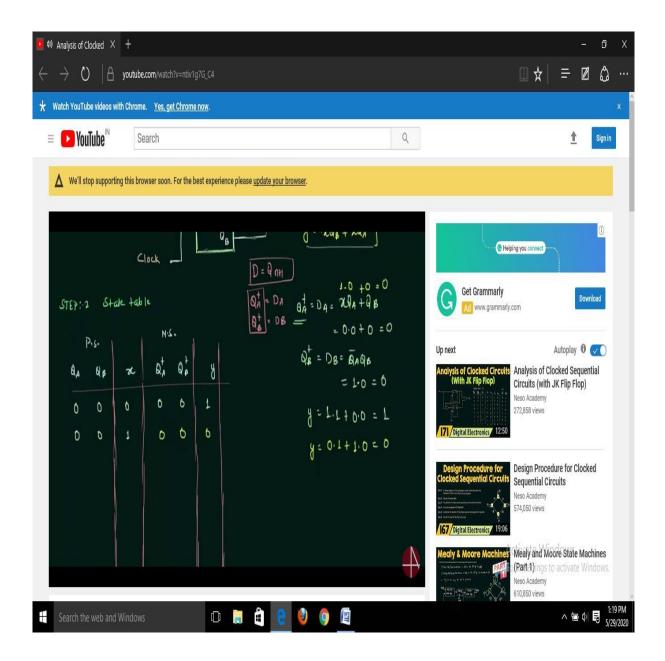
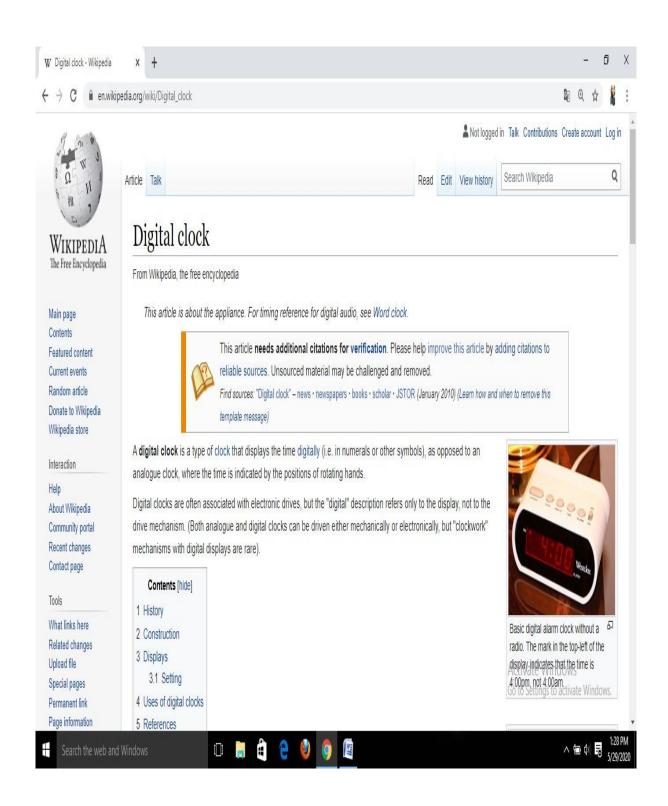
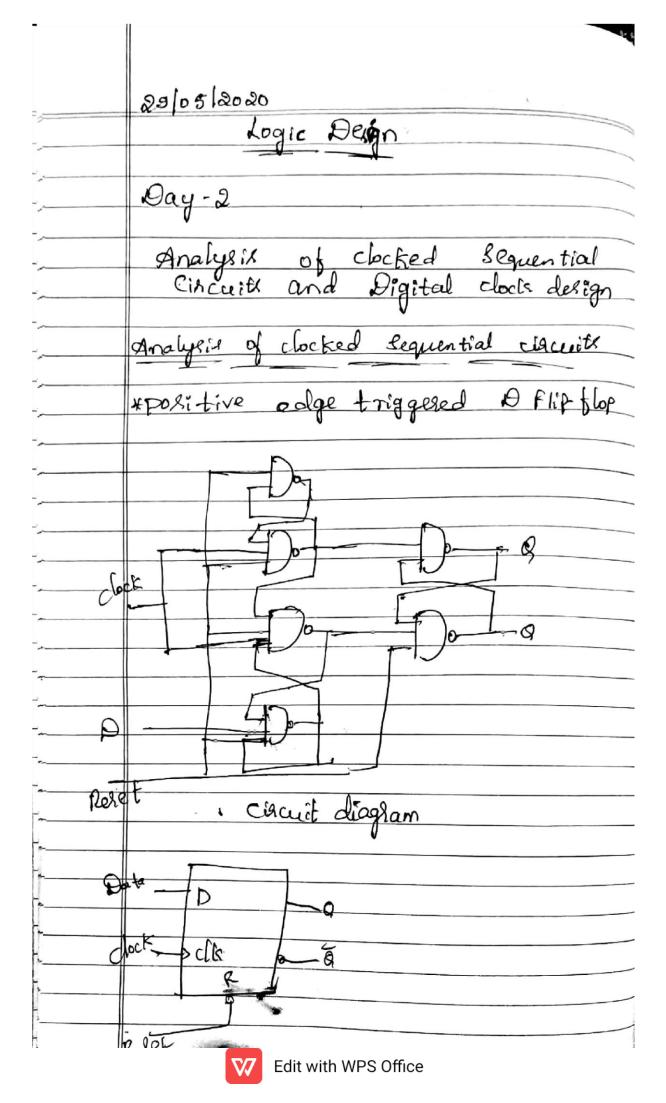
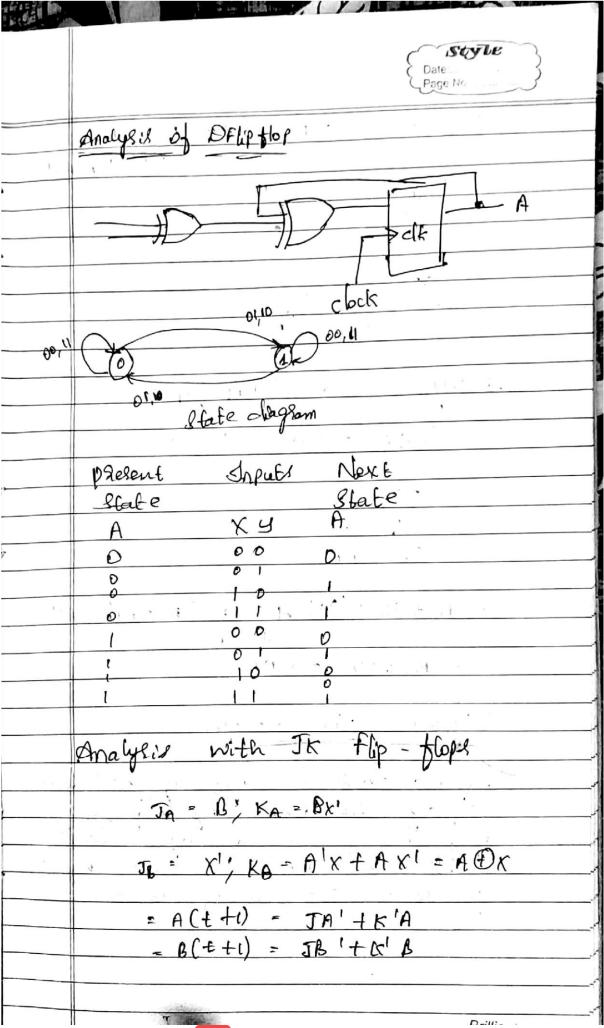
Date:	29-05-2020	Name:	Poorvi j gowad
Course:	Logic design	USN:	4al17ec071
Topic:	Analysis of clocked sequential circuits, Digital clock design	Semester and section	6 th sem'B'section
Github repository:	Poorvi-2000		

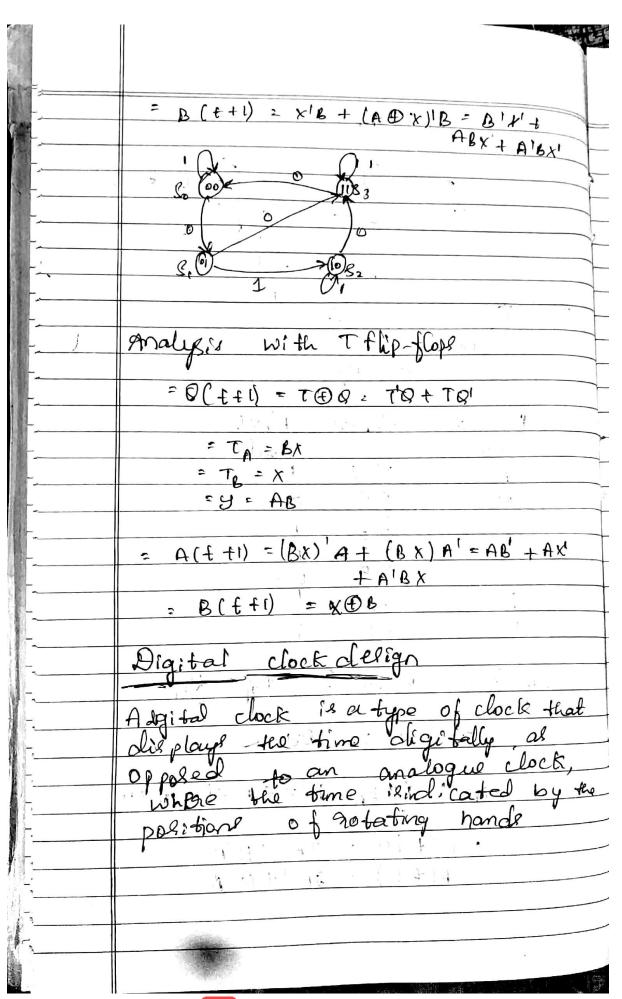












Date:	29-05-2020	Name:	Poorvi j gowda
Course:	Python programming	USN:	4al17ec071
Topic:	Objectoriented programming	Semester and section:	6 th sem b sec

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| Table | Dept |
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