

## DAILY ASSESSMENT FORMAT

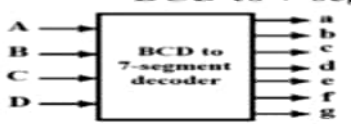
Date:	27/05/2020	Name:	PREETHAM S RAI
Course:	Signal and system	USN:	4AL18EC040
<p>Boolean equation for digital circuits</p> <p>Combinational circuits – Conversion of MUX and Decoders to logic gates</p> <p>Design of 7 segment decoder with common anode display</p>		Semester & Section:	4 th sem Asection
Github Repository:	Psraipreetham		

### FORENOON SESSION DETAILS

Image of session

5:46
VoLTE 4G LTE 94%


#### BCD to 7-segment decoder





A	B	C	D	a	b	c	d	e	f	g
0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	1	1	1	0	0	0	0
0	0	1	0	1	1	0	1	1	0	1
0	0	1	1	1	1	1	1	0	0	1
0	1	0	0	0	1	1	0	0	1	1
0	1	0	1	1	0	1	1	0	1	1
0	1	1	0	1	0	1	1	1	1	1
0	1	1	1	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1	1	1	1
1	0	0	1	1	1	1	1	0	1	1


#### BCD to 7 segment decoder


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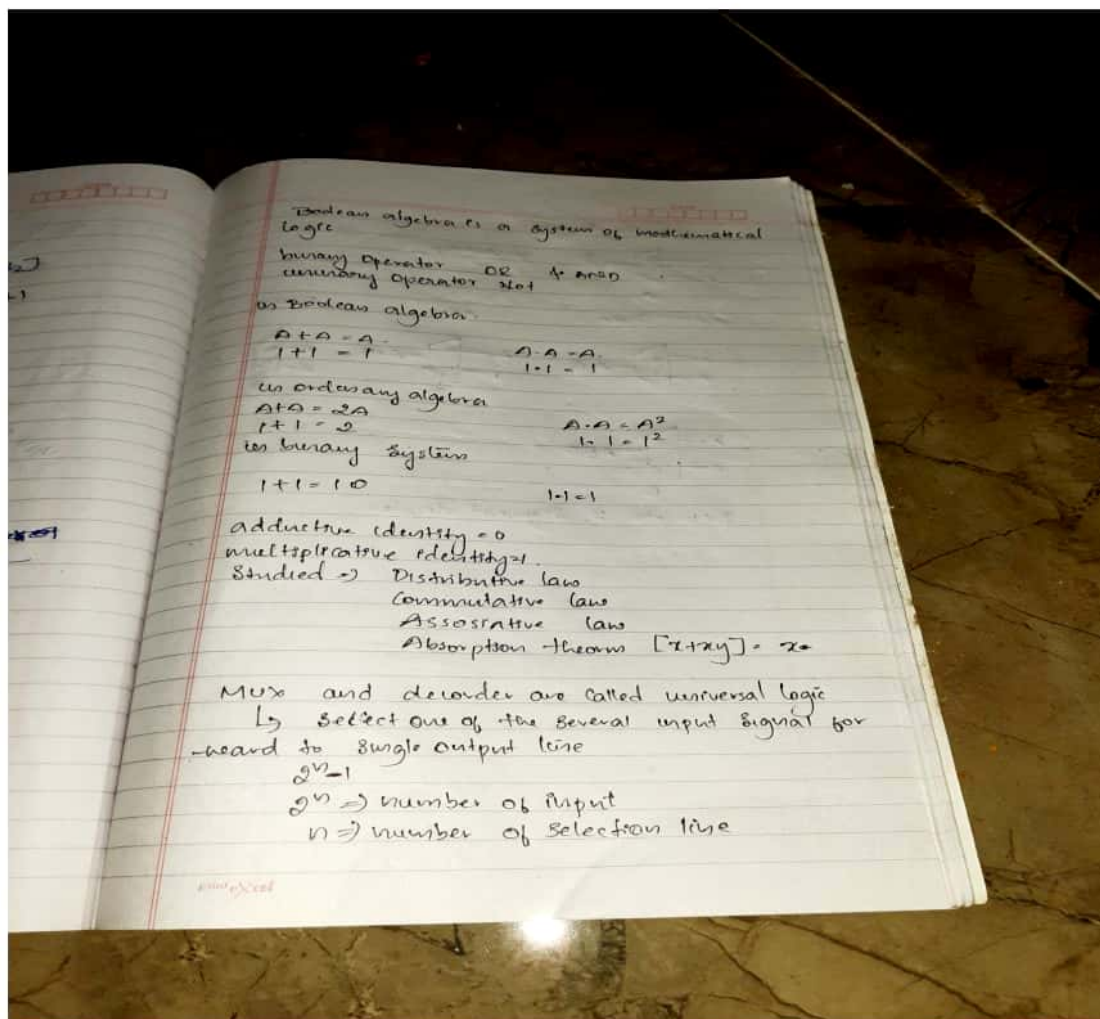
  
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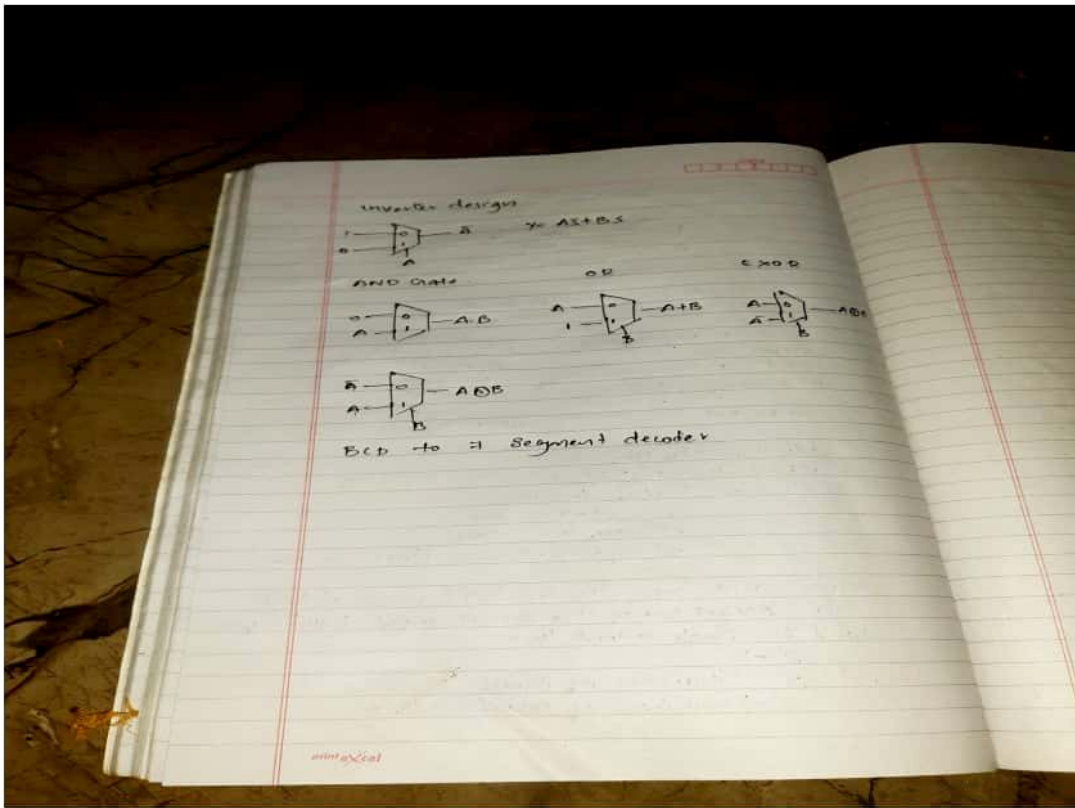
  
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Report – Report can be typed or hand written for up to two page





Date:

Name:

Course:

USN:

Topic:

Semester  
& Section:

#### AFTERNOON SESSION DETAILS

Image of session



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