Aftemoon session

Name - Rashmi
USN - 4AL18EC043
Sem - IV sem

Date - June 5
Coure - Python
Topic - Project
excerise
Repository - Rashmi.k

Report:

How the output should look like:
Illustration of all this applecation

Please upload your csv file. The values conlaing adres shoald lee in a colouma naned adres or Address

Please make sure you hauee an adrees colaumn in your csv file

- go to libraries
  create inder.
  write program
  and upload csv file.
  submit.

A geocoder help to upload your csv file

Morning session

Date - June 5
Course - Network analysis
Repository - Rashmik

Name - Rashmi
USN - 4AL18EC013
Sem - IV Sem.

Report:

* Series RLC circuits

Inductive reactance : $X_L = 2\pi fL = \omega L$

Capacitive reactance : $X_C = 1/2\pi fC = 1/\omega C$

when $X_L > X_C$ the ckt is inductive

when $X_C > X_L$ the ckt is capacitive

Total ckt reactance = $X_T = X_L - X_C$

Total ckt impedance = $Z = \sqrt{R^2 + X_T^2} = R + jX_L$

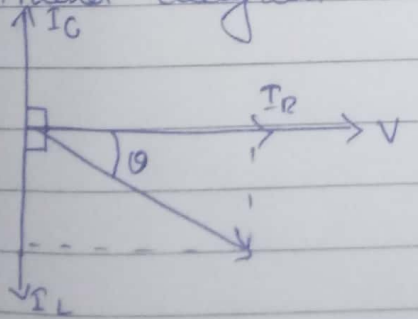
* $\arctan = \frac{X_L - X_C}{R} = 0^\circ$

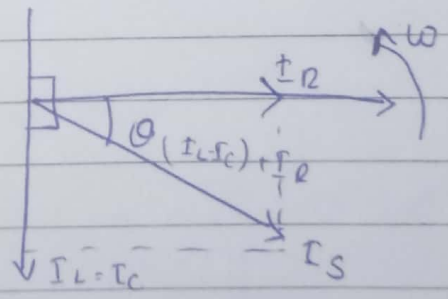
* Parallel series circuits

admittance $Y = \frac{1}{Z}$ Conductance $G = \frac{1}{R}$

Susceptance $B_L = \frac{1}{X_L}$ $B_C = \frac{1}{X_C}$

Phasor diagram for Parallel RLC circuit





RL and RC circuits :

RL & RC circuits are commonly used as filters & also useful for electric signal processing.