Nane: - Gr-Raviteja Nate: - 28 May 2020 ZUSN: - GALL & ECLO) Course logie design Sen: - 6th sem 'B' Topic: Boolian egn's bos Digital ckt's, Pecadess, & Mux \* In 1854, george Bool Perdoped a algebraice system called Booken Algebra. \* Boolean Algebra is a system of Mathienatical logics It is defined With a set of derrests a set of operators and There are 4 laws of Boolean Algebrailommidative lawi-X+Y=Y+X. AtB = B+A. X-Y = Y, x A.B= B.A. Assosant ere las: 2) X+(Y+Z)= (X+Y)+Z A+(B+c)=(A+B)+c.X. (4.2) = (X.4).Z A, (B.c) = (A.B).C. 3/ Distribution lawi-X(Y+Z) = XY+YZA (B+C)= AB+AC. 4) Absasption Theosen:-X+ XY=X. A+AB= A. X+NXY= X+Y.

\* Mux to Logic gates convession:i) NAND, NOR, wireson gates. 2) Mux and decodess are called reniversal Logie. => Mux is devices Which selects one as several digital / Analog I/ps and it Will toseward it to the O/p lines, Which is Single of line, Scanned with CamScanner