

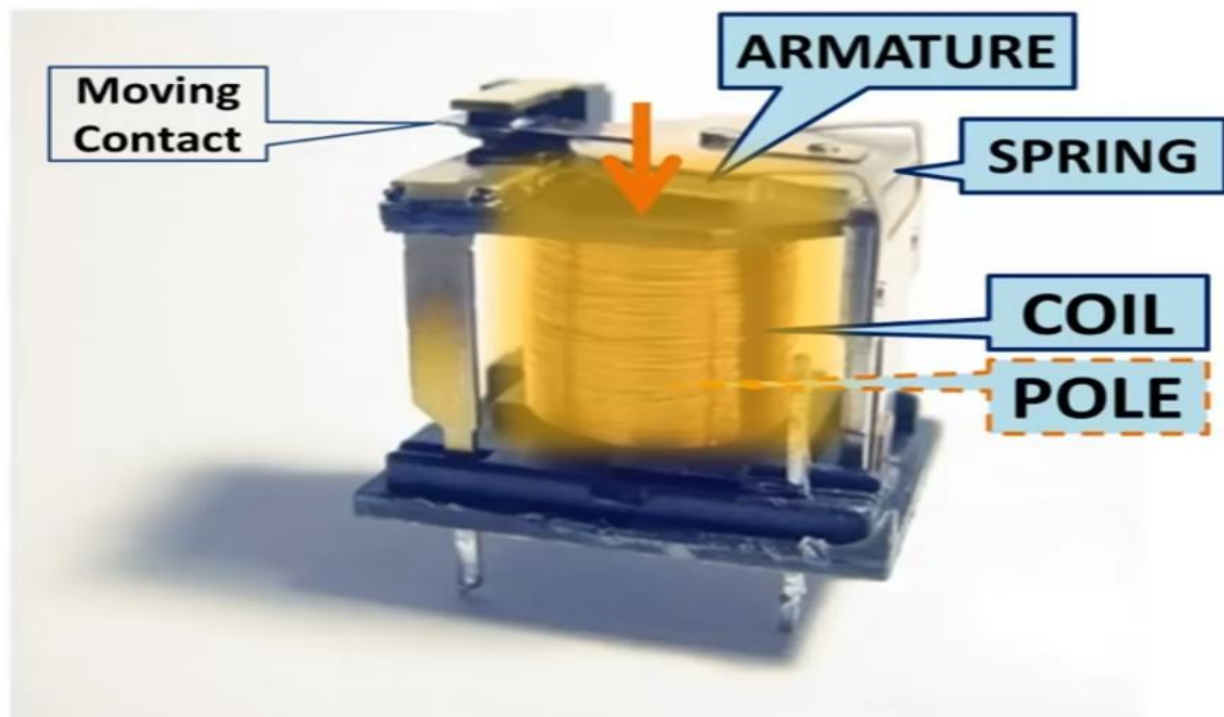
DAILY ASSESSMENT FORMAT

Date:	30-05-2020	Name:	Sahana S R
Course:	Logic Design	USN:	4a17ec083
Topic:	Application of Programmable Logic Controller	Semester & Section:	6 th sem B sec
Github Repository:	sahanasr-course		

FORENOON SESSION DETAILS

Image of session

An Introduction to Programmable Logic Controllers

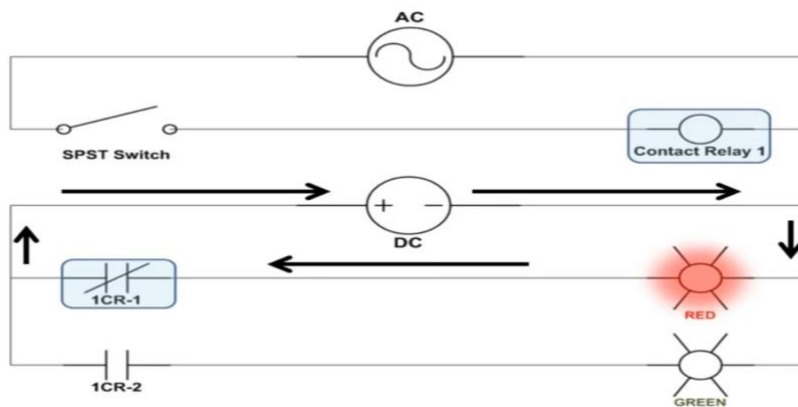
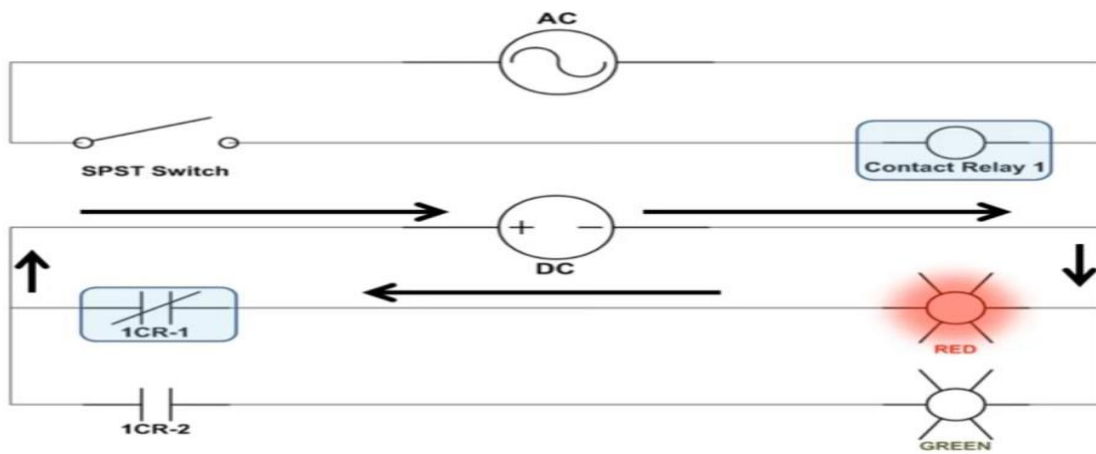


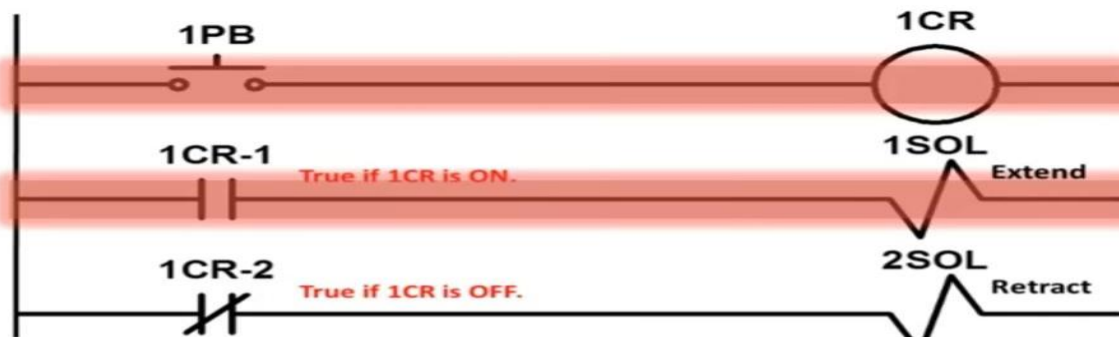
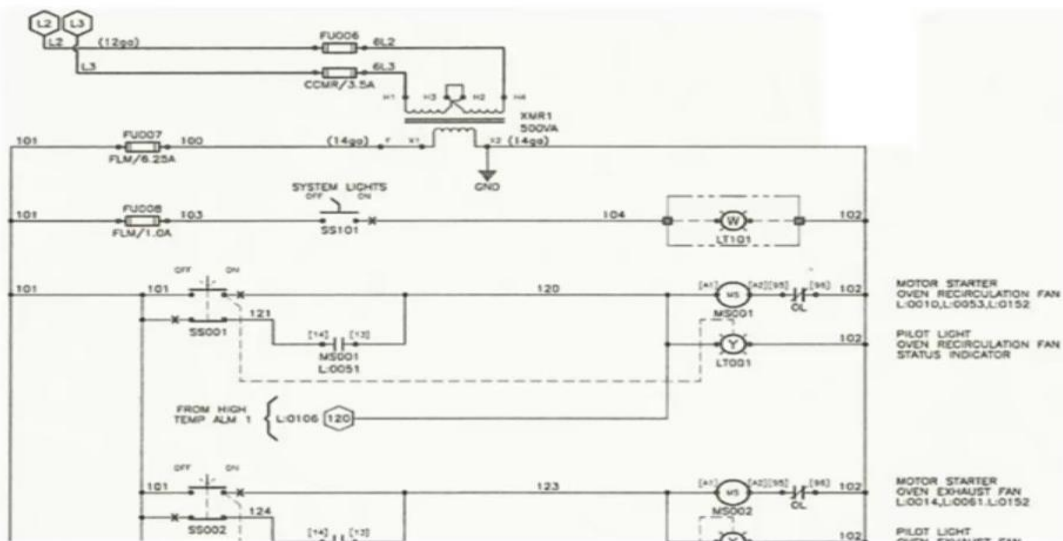
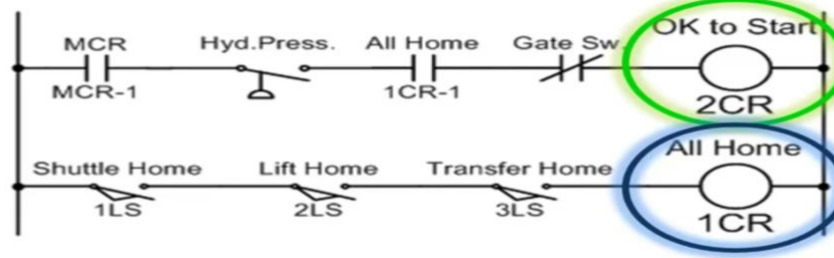
Operator Interface

- SPST Switch
- Red Indicator
- Green Indicator

Power Source

- Alternating Current
- Direct Current



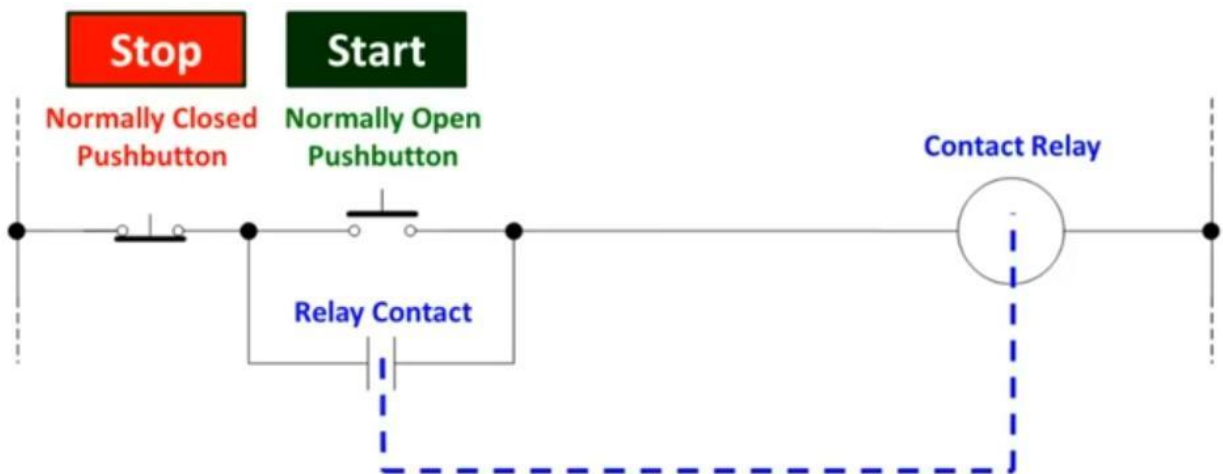


Is anything in this circuit currently energized? 2SOL

Are any of the contacts currently true? 1CR-2 False? 1PB & 1CR-1

With 1PB closed?

Energized? 1CR & 1SOL True? 1PB & 1CR-1 False? 1CR-2



Report – Report can be typed or hand written for up to two pages.

Logic Design Application of Programmable Logic Controller

Introduction Relay to bits

