**DAILY ASSESSMENT FORMAT**

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| **Date:** | **27 MAY 2020** | **Name:** | **Sampatkumar n m** |
| **Course:** | **LOGIC DESIGN** | **USN:** | **4AL19EC401** |
| **Topic:** | **1.BOOLEAN EQUATIONS FOR DIGITAL CIRCUITS**  **2.MUX AND DECODERS**  **3.DESIGN OF 7 SEGMENT DECODER** | **Semester & Section:** | **IV SEM & A SECTION** |
| **Github Repository:** | **Sampatkumar1** |  |  |

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| **FORENOON SESSION DETAILS** | | | |
| **Image of session** | | | |
| **Report – Report can be typed or hand written for up to two pages.**  **TOPICS LEARNT TODAY:**  **logic design :**  **1.digital circuits**  **2.boolean algebra**  **3.basic gates, type of gates**  **4.BCD to 7 segment decoder**  **5.truth table**  **6.construction of mux to logic design**  **7.also about circuits.** | | | |
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