date: 27 may 2020

course: Logic disign

Topic: 1. Boolean equation & convertion of Mux and decoder to logic gates

2. 7 segment decoder

github repository: Shradha-courses

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Semester & Section: 4th sem 'A' section

Boolean algebra:

"In 1854, george Boole developed an algebraic system now called Boolean algebra"

*"Boolean algebra is a system of mathematical logic"

* It is difined with set of elements, a set of operators, and a number of axioms or postulates"

Axioms and laws of Boolean algebra:

Axioms or postulates of Boolean algebra are a logical expressions upon which we can build useful theorms

"AND operation" "OR operation" "NOT operation"

MUX to logic gates:

① NAND, NOR - universal gates.

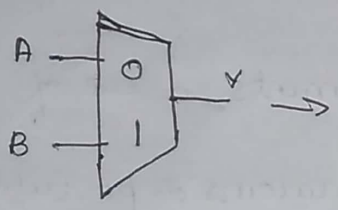
② "Universal logic"

③ MUX and decoders are called "Universal logic"

what is multiplexer?

- multiplexer is a device that selects one of several analog or digital input signals and it forward it to output line that is signal output line here the general equation of MUX is $2^n \times 1$

2:1 MUX



selection (s)	output (Y)
0	A
1	B

with the help of KMAP → $Y = A\bar{S} + BS$

date: 26 may course: python Topic: ~~application~~ ① application: build a desktop database application	Name: SHRADHA USN: 4AL17EC088 Semester & section: 4th sem 'A' sec

Build a desktop database Application

* how the output will look like
* use interface design
* connecting the frontend to the Backend
* fixing the bug
* Creating a standalone Executable version of the program