Name: Shradha

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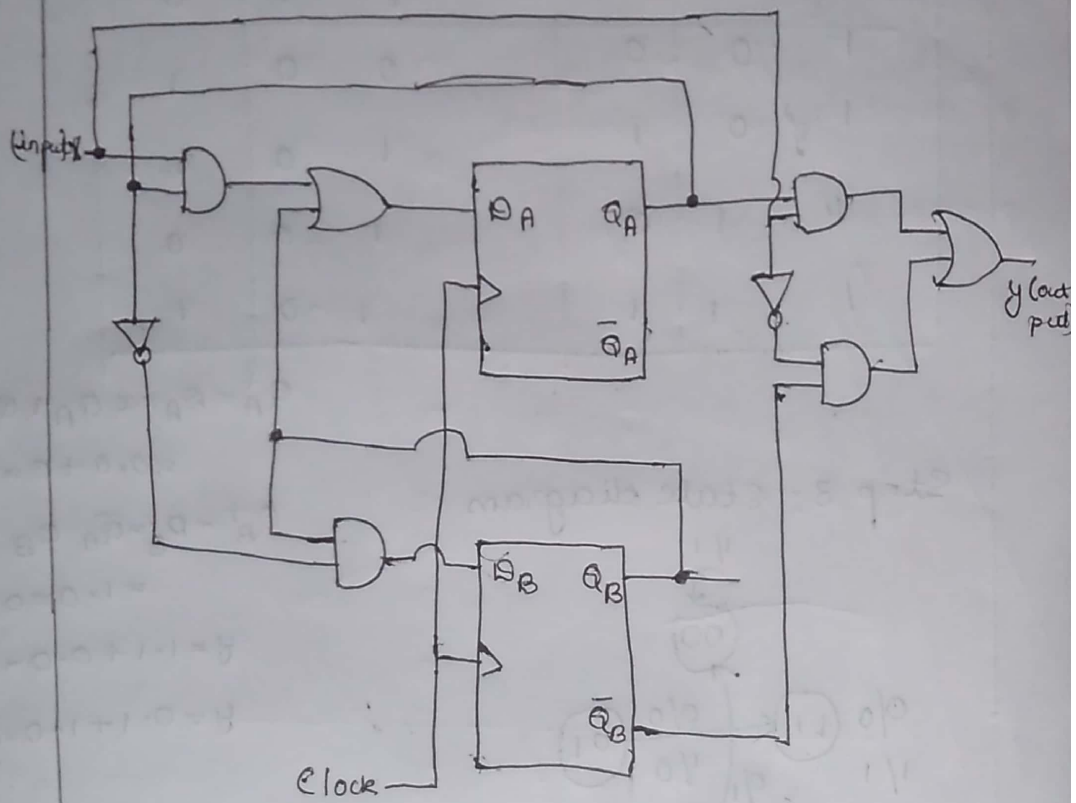
Logic design

Analysis of clocked sequential circuits (with-D-FF)



~~Analysis of~~ clocked sequential

Step 1: find out the input & output equation

$$D_A = x Q_n + Q_B$$

$$D_B = \bar{Q}_n Q_B$$

$$y = \bar{x} \bar{Q}_B + x Q_A$$

Step 2:

Present state | Next state

Q_A	Q_B	x	Q_A^+	Q_B^+	y
0	0	0	0	0	1
0	0	1	0	0	0
0	1	0	1	1	0
0	1	1	1	1	0
1	0	0	0	0	1
1	0	1	1	0	1
1	1	0	1	0	0
1	1	1	1	0	1

$Q_A^+ = D_A = x\,Q_A + Q_B$

$= 0 \cdot 0 + 0 = 0$

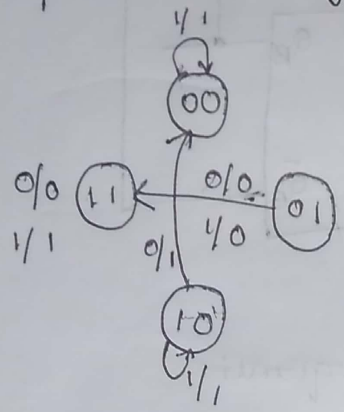
$Q_B^+ = D_B = \bar{Q}_A\,Q_B$

$= 1 \cdot 0 = 0$

$y = 1 \cdot 1 + 0 \cdot 0 = 1$

$y = 0 \cdot 1 + 1 \cdot 0 = 0$

Step 3: State diagram



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PYTHON:

OBJECT ORIENTED PROGRAMMING

* Turning this application into oop style
* creating bank account object
* OOP glossary
* CRUD in OOP design
* python object inheritance
* class checking