	JAY PRAKASH
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V 600 - 7	Introduction to electromagnetism and
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	The two operators Varenot earl because the first one operator on one function, y; the other operates on a different function, D;
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course?-Hardware Description AAL 18EC049 languages fox FP4A Derign 5th son; A? Dode: 10-Sep-2020 BCD DECade Counters 14 bit binary Counters Synchronous 4-bit Binary Counters. NHOL74LS163 Binary Counters Je VHDL Fenête state Machine. Mission 2-005 library icee; Use rede. Std-logic entoty FSM ES port (In 1; en std-logec;
RST: in std-logec; CLK: in std-logic; Out 1: inout Stell - logice end FSM 9 VHOL ALU: -Mission 2-005

LIBRARY TEEES USE IEEE. STD-Logic\_1164.ALLS USE IEEE. Numeric\_STD.ALLS Use IFEE. STD-logic - Un signed. ALL Enfrty ALU IS PORT (bp Code: IN Std-logec-Vector (2 DOWNTO O): A, B: IN STD-Logic-Vector (31 Down+0 O): Y: out std - logic - Vector (31 DOWN TOO) END ALU: