JAY PRAKASH

Date _____ Page _____

Date:- 12-Sep-2020 Sindhu. S

Course:- Electrodynamics: An 4AN18EC049

introduction. 5th sem, 'A' Sect

Y.R:- Sindhu - Course.

- Introduction to Electromagnetism and it's application to Material Science
- e learned topics:-
  - Vector intergals; the line integral of $\nabla\psi$
  - The flux of a vector field
  - The flux from a cube; Gauss' theorm
  - Heat conduction; the diffusion equation.
  - The circulation of a vector field

$$\oint_r C_t\,ds = \oint_r C\cdot ds$$

  - The circulation around a square; stokes' Theorm.

$$\oint_r C\cdot ds = \int_s (\nabla\times C)\cdot n\,da$$

  - Curl-free and divergence free fields

$$\int_s D\cdot n\,da = \int_V (\nabla\cdot D)\,dv.$$

Sindhu. S
4AL18EC049
5th sem, A Sec

Date:-12-Sep-2020
Course:- Hardware description language for FPGA design.

- Basic Verilog:-

Readings:-

→ Hamblen, CH 4 (14P)
Wilson, Ch 4 (7P)
Intro to Verilog, Section 2, 5, & 6 (5P)
Intro to Verilog, Section 4 & 6 (2P)
Intro to Verilog, Section 7, 8 & 10 (8P)

- Verilog find the Errors.

```
module find errors
  input a[0:3];
 output [3:0] b;
  input [5:0] c
wire [0:3] aw;
wire [3:0] bw;
 reg [5:0] creg
begin
assign aw = a;
 assign b = bw;
assign creg = c;
always
 begin
```

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Date _____ Page _____

```
if (c_reg = 4'h F)
    bw <= '0101';
end;
end process;
end
```