JAY PRAKASH

Date_____ Page_____

Date:- 11-Sep-2020 Sindhu.S

Course:- Electrodynamics :- An introduction. 4AL18EC049

5th Sem, 'A' Sec

e.g.:- Sindhu - Course.

• Introduction to vector integral Calculus

→ gauss' Theorem, flow and circulation

• Deriving gauss theorm

→ The infinitesimal cube

$$\int_{cube} C \cdot n \, da = \left[\frac{\partial C_x}{\partial x} + \frac{\partial C_y}{\partial y} + \frac{\partial C_z}{\partial z}\right] \Delta x \Delta y \Delta z =$$

$$(\nabla \cdot C) \nabla V$$

The divergence of the vector field at a point function.

→ Flow and circulation :-

Total heat flow outward through

$$S = \int_s h \cdot n \, da = -\frac{dQ}{dt}.$$

The conservation law

$$-\frac{dq}{dt} = \nabla \cdot h$$

2.

Date: 11-Sep-2020
Course: Hardware description language for FPGA design

Sindhu. S
4ALI8EC049
5th sem, A'sec.

- Basic of Verilog:-
  - Verilog for fun and profit (Introduction)
  - Your first Verilog phrase
  - Verilog Rules and syntax
- The history of the Verilog HDL language
- An approach to learning Verilog
- A 'first phrase' design example (a comparator), done 3 diffrent ways

4-bit comparator

```
module COMPARATOR
  (input [3:0] A, B,
   output Y);
integer N;
reg Y;
always @ (A @ B)
  begin : COMPARE
    Y = 0;
  if(A == B)
    Y = 1;
  end
end module //
```