**DAILY ASSESSMENT FORMAT**

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| **Date:** | **27-may-2020** | **Name:** | **Sindhu S** |
| **Course:** | **Logic design** | **USN:** | **4AL18EC049** |
| **Topic:** | 1.boolean equation for digital circuits. Combination circuits:conversion of mux and decoder to logic gates  2.design of 7segment decoder with common anode display | **Semester & Section:** | **4th sem. & ‘A’ sec.** |
| **Github Repository:** | **Sindhu-course** |  |  |

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| **FOR NOON SESSION DETAILS** |
| **Report :** |
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| **Date:** | **27-May-2020** | **Name:** | **Sindhu s** |
| **Course:** | **Python from Udemy** | **USN:** | **4AL18EC049** |
| **Topic:** | **1.application:build a desktop database application** | **Semester & Section:** | **4th sem. & ‘A’ sec.** |
| **AFTERNOON SESSION DETAILS** | | | |
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| **Report :** | | | |