Islamic University of Technology (IUT)
Organisation of Islamic Cooperation (OIC)
Department of Electrical and Electronic Engineering
Electronics Laboratory, Room No.:502

Student ID. : ....

Date & Year: …………………. Working Table No. : ……. Section: …….. Group: ..…….…

Course: EEE 4384
Experiment no.: 03

Name of the experiment: Study of wave shapes of different types of clipping and clamping circuits.

Objective:

- To construct the clipping circuits and observe its corresponding wave shapes.
- To construct the clamping circuits and observe its corresponding wave shapes.

Apparatus required:

| S.NO.	NAME OF THE EQUIPMENT	TYPE	RANGE	QUANTITY (NO.S)
1.	Diode	IN 4007		1
2.	Resistor		10k Ω	1
3.	Capacitor		1μF	1
4.	Oscilloscope			1
5.	Standard Project Board			1
6.	Function/Signal Generator			1
7.	Connecting wires			Required

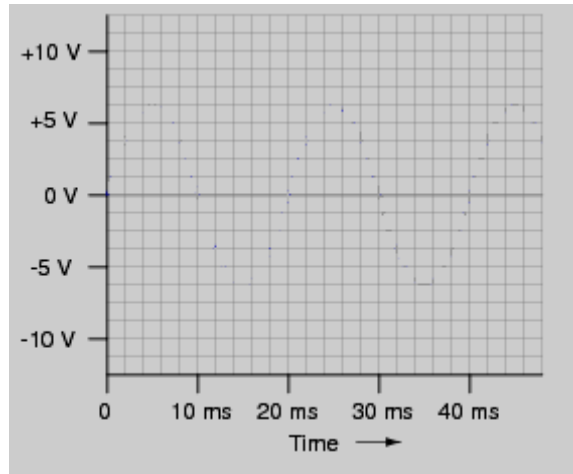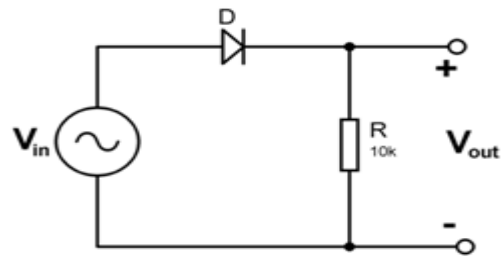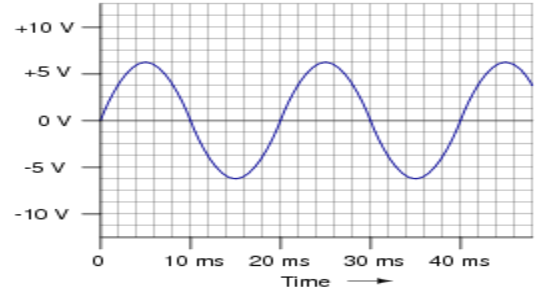**Task 1:** To construct the clipping circuits and observe its corresponding wave shapes.

Theory:
Often it is necessary to modify the shape of a particular waveform. Circuits which perform that function are called clippers. Clipper circuits have the ability to "clip" off a portion of the input signal without distorting the remaining part of the alternating waveform. The half wave rectifier of the previous experiment is an example of the simplest form of diode clipper. Depending on the orientation of the diode, the positive or negative region of the input signal is "clipped" off. There are two general categories of clippers: series and parallel. The series configuration is defined as one where the diode is in series with the load, while the parallel variety has the diode in branch parallel to the load.

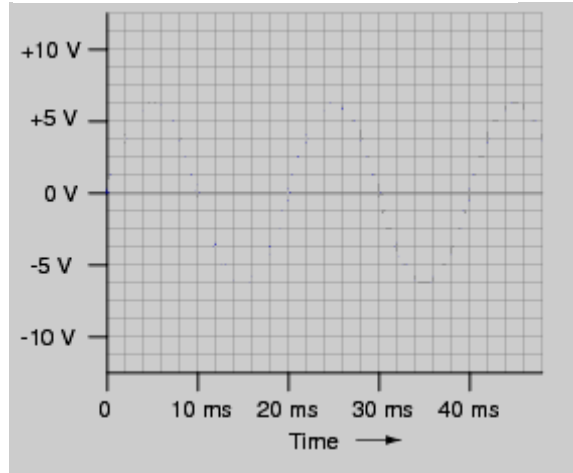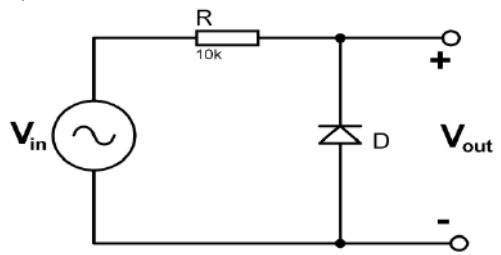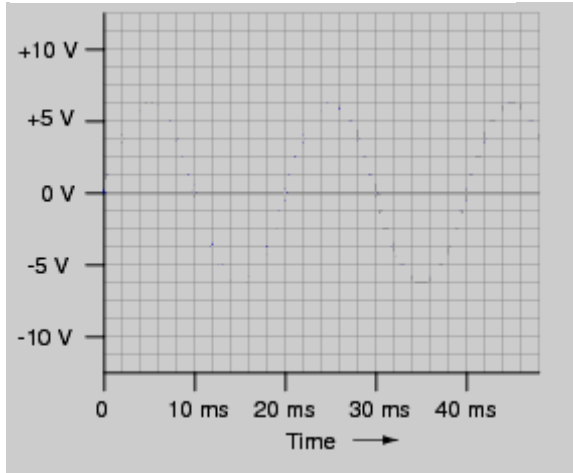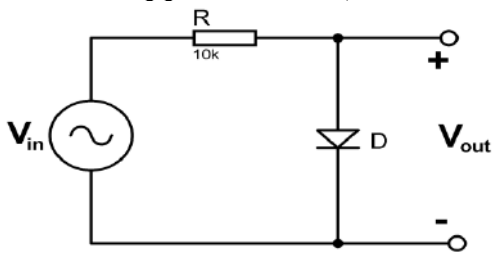# Series Clipper Circuit (with Diode and Resistor):

Positive Clipper
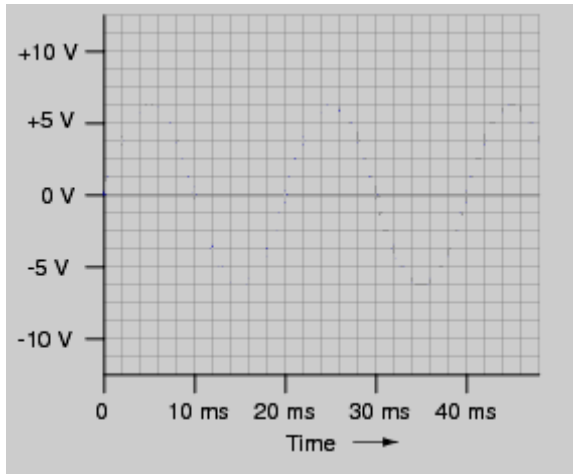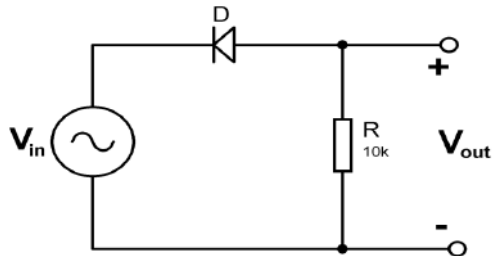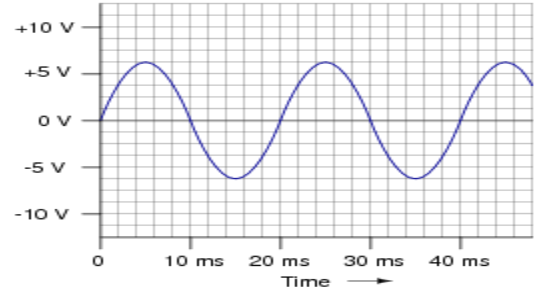
Negative Clipper

Parallel Clipper Circuit (with Diode and Resistor):

Task 2: To construct the clamping circuits and observe its corresponding wave shapes.

Theory:
Sometimes we may want to leave the waveform unchanged, but modify its DC level up or down. To accomplish this, we use a clamper circuit. The beauty of clampers is that they can adjust the DC position of the waveform without knowing what the waveform actually is.
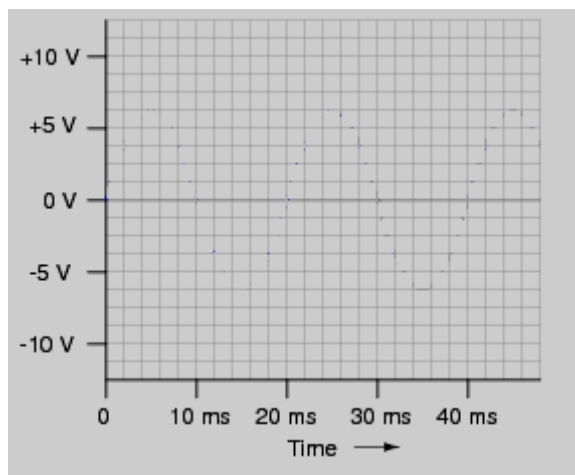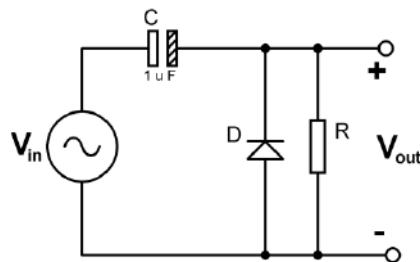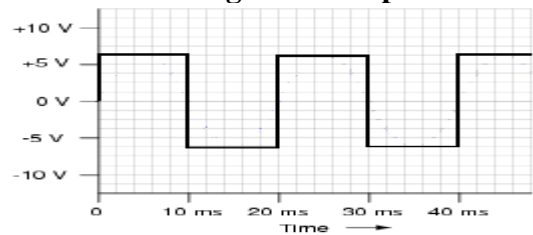
The *clamping* network is one that will "clamp" a signal to a different dc level. The network must have a capacitor, a diode, and a resistive element, but it can also employ an independent dc supply to introduce an additional shift. The magnitude of R and C must be chosen such that the time constant $\tau = RC$ is large enough to ensure that the voltage across the capacitor does not discharge significantly during the interval the diode is non conducting. Throughout the analysis we will assume that for all practical purposes the capacitor will fully charge or discharge in five time constants.

Series clamping Circuit (with Diode and Resistor):
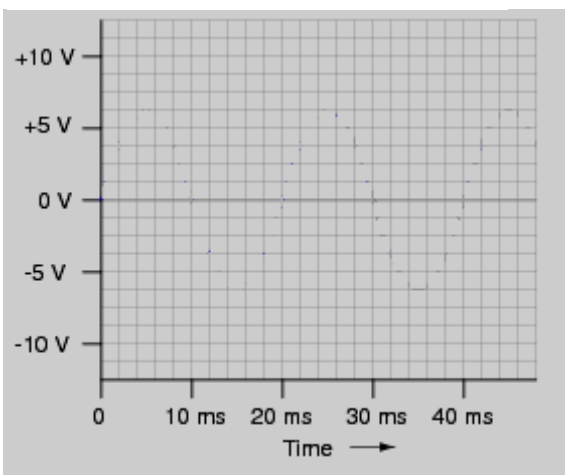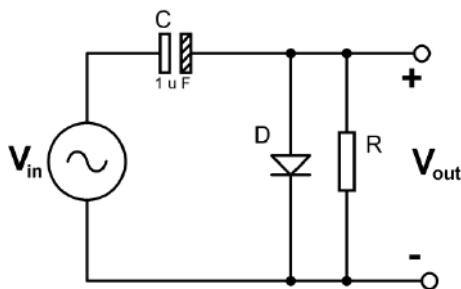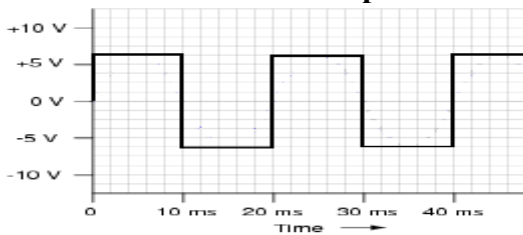
Positive clamper	Negative clamper

Discussion: