



# ALVIN JOSE

**Date of birth:** 17/05/1995 | **Nationality:** Indian | **Gender:** Male | **Phone number:** (+49) 16099880769 (Home) | **Email address:** [alvinjose6@gmail.com](mailto:alvinjose6@gmail.com) | **LinkedIn:** [www.linkedin.com/in/alvin-jose-22089678](https://www.linkedin.com/in/alvin-jose-22089678) | **WhatsApp Messenger:** 9400010851 | **Address:** Vetttersstr. 54\19, 09126, Chemnitz, Germany (Home)

## ABOUT ME

MSc student in Design and Test for Integrated Circuits at Chemnitz University of Technology, with over two years as a Hardware Design Engineer advancing automotive power electronics. Expert in MOSFET testing (up to 1200 V), simulation (LTspice), and test bench development, with proven success in gate driver design and AQC-324 reliability testing (HTRB, HTGB, H3TRB). Skilled in LabVIEW programming and team collaboration, eager to optimize transistor design and process integration.

## WORK EXPERIENCE

### CHEMNITZ POWER LABS – CHEMNITZ, GERMANY

#### **HARDWARE DEVELOPER-WORKING STUDENT-POWER ELECTRONICS** – 12/02/2024 – 30/06/2025

- Skilled in measuring automotive modules (MOSFETs), including pre- and post-measurements.
- Expertise in gate threshold ( $V_{th}$ ), gate leakage (IGSS), drain-source leakage (IDSS), body diode (VSD), and  $R_{Dson}$  measurements.
- Proficient in high-voltage measurements up to 1200 V.
- Experienced in KiCAD PCB design for adapters and gate drivers.
- Used LTspice for project simulations.
- Analyzed gate driver response under various conditions.
- Developed test benches.
- Worked on gate driver development, testing, and calibration.
- Programmed in LabVIEW.
- Knowledge of instruments: power supply, multimeter, oscilloscopes, curve tracer (Keysight).
- Supported reliability tests (load cycle, HTRB, HTGB, H3TRB, etc.).
- Developed HV Zilla for gate leakage measurement.
- Executed test bench operations, including power cycling.
- Documentation Skills: Proficient in Excel for data logging and analysis, PowerPoint for presentations, Microsoft Teams for collaboration, and Planner for task management across all projects.

### RIOD LOGIC – COCHIN, KERALA, INDIA

**Business or Sector** Information and communication | **Website** <https://riod.in/>

#### **HARDWARE DESIGN- ENGINEER** – 12/06/2021 – 10/09/2023

- Extensive knowledge of design tools such as Altium Designer, LT PSpice, etc.
- Knowledge of debugging tools like IDEs and emulators
- Instrument knowledge, multimeters, logic analyzers, and DSO
- DC/DC power supply design, LDOs, battery-operated product design
- Test and debugging—board bring-up, interface testing
- Learned programming languages such as Python, C, and C++
- Multi-layer schematic and PCB layout-design knowledge (4 layers)

### ESTRADO TECHNOLOGIES – TRIVANDRUM, KERALA, INDIA

**Address** CBS, 5th Floor, Techno park Phase III, 695581, Trivandrum, Kerala, India

#### **TECHNICAL SUPPORT ENGINEER** – 25/05/2020 – 08/08/2021

- Overall support for the customers by phone and emails
- SQL server operations

### LIVEWIRE INDIA(CADD CENTRE) THRISSUR. KERALA – THRISSUR, INDIA

- IoT communication and protocol
- cloud and client devices(MQTT)
- Data Analysis

 **REMOMECH ENGINEERING** – PUNE, INDIA

**ENGINEER IN ELECTRICAL SYSTEMS** – 17/05/2017 – 10/02/2019

- Operation of 4MW(4000KW,11KV,5360 Hp,375 Rpm), synchronous, motor(WEG) through DCS(distributed control system)by SIEMENS sierug power SPPA T-1000

## ● **EDUCATION AND TRAINING**

01/10/2023 – CURRENT chemnitz, Germany

**MSC. DESIGN AND TEST FOR INTEGRATED CIRCUITS** Technische Universität (TU) Chemnitz

- Technologies for Micro and Nano Systems
- Integrated Circuit Design - Transistor Level
- Reliability of Micro and Nano Systems
- Micro and Nano Devices
- Smart Sensor Systems
- Advanced Integrated Circuit Technologies

**Address** Technische Universität Chemnitz, Straße der Nationen 62, D-09111, chemnitz, Germany |

**Website** <https://www.tu-chemnitz.de/> | **Field of study** Design and Test for Integrated Circuits | **Number of credits** 34

15/05/2012 – 30/06/2016 thrissur, India

**BACHELOR OF TECHNOLOGY, ELECTRICAL AND ELECTRONICS** sahrdaya collage of engineering

**Address** Kodakara, P.B.No.17, Thrissur (Dt), Pin 680684, Kerala State, India, 680684, thrissur, India |

**Website** <https://www.sahrdaya.ac.in/> | **Field of study** Electrical Engineering

## ● **LANGUAGE SKILLS**

Mother tongue(s): **MALAYALAM**

Other language(s):

	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken production	Spoken interaction	
<b>GERMAN</b>	B1	A2	A2	A2	A2
<b>ENGLISH</b>	C1	C1	C1	C1	C1

*Levels: A1 and A2: Basic user; B1 and B2: Independent user; C1 and C2: Proficient user*

## ● **SKILLS**

altium desginer | Internet of Things (IoT) Knowledge | Microsoft Office | BOM creation | Zoom | Basics Of Python | Organizational and planning skills | Ms. SQL Server | GIT Hub | Kommunikations-Tools (Zoom, Skype, Teams) | Bug tracking Systems : Jira | Version Control System (Git)