

Yu-Che Tsai

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SUMMARY

Experienced Software Engineer, an excellent team member with more than 10 years of work experience.

Pursue an opportunity in FAE or R&D, being cooperative and willing to own a project.

Area of Expertise in:

Linux Kernel Driver

VMware certification

iSCSI protocol

Python

Socket programming

TCP/IP

Embedded System

Storage stack

Multithreading

Routing

C/C++

Rust

WORKING EXPERIENCE:

Western Digital Corporation

2021/12 ~ Now

Staff Engineer (R&D)

Responsibility:

- Refactor legacy code and add unittest cases
- Rust utilities design, implementation and maintenance
- Software packages maintenance
- Kernel driver maintenance
- Debug/Resolve customer reported issues

Accomplishment:

- Refactor legacy C/C++ code with unittest cases
- Reimplement legacy C utilities in Rust
- Port Gplv3 release utility from old product to new one
- Work with team for My Cloud Home/Duo and other NAS products

Latticework Inc., Taiwan branch

2016/01 ~ 2021/11

Staff Engineer (R&D)

Responsibility:

- Storage stack design and implementation
- Management flow design and implementation
- Linux software RAID / LVM / Btrfs kernel driver maintenance
- Relay server design and implementation
- C++/Python RPC library design and implementation

Accomplishment:

- Storage architecture design and module implementation
- Python unittest for storage module and gitlab CI/CD integration
- C++/Python RPC library design with gtest unittest and gitlab CI/CD integration
- Relay service flow design and related modules implementation with unittest and gitlab CI/CD integration
- Work with team for Amber/AmberX NAS product

Promise technology

2011/09 ~ 2015/12

Senior Engineer (R&D)

Responsibility:

- iSCSI target, iSNS client and subsystem management flow implementation and maintenance
- iSCSI performance tuning
- Ethernet device bring up
- Multiple Ethernet devices routing handle

Accomplishment:

- Hack Linux kernel TCP flow and Ethernet driver to decrease CPU utilization and increase iSCSI data out performance.
- Ex50 product integration for 2015 NAB show and 2015 IDF show.
- Fair core assignment for different threads
- Survey Ethernet chips for new product
- Work with TW team for VessRAID R2000 series product
- Work with US & TW team for following products
VTrak Ex30
VTrak A-Class (Ex30 with scale-out design)
VTrak Ex50

EDUCATION

2009-2011 Master degree [Computer Science], National Chung Hsing University, Taichung

2005-2009 Bachelor degree [Computer Science], National Chung Hsing University, Taichung

LANGUAGES:

English: Moderately

Chinese: Native

PERSONAL DOSSIER

Date of Birth: 31th March 1987

Sex: Male