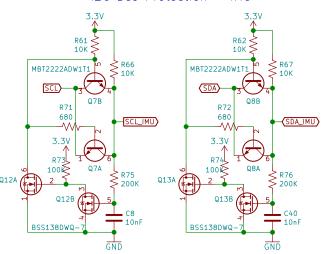
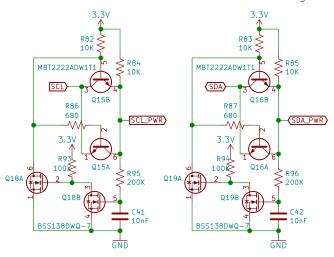


12C Bus Protection - IMU



12C Bus Protection - Power Monitor & USB Charger



NOTE

These novel bus protection circuits prevent traditional I2C/SPI failure modes where a single slave failure can disable the entire bus.

Learn more:

https://doi.org/10.36227/techrxiv.15166620

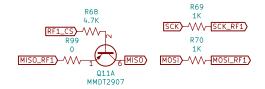
By default, slave clock and/or data lines can be held low and the Master (SAMD51) will still be able to communicate with the remainder of the bus.

They can individually be bypassed by removing the transistor(s) and soldering the Oohm the jumpers below.

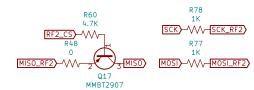
SPI Bus Protection — SD Card and Payloads



SPI Bus Protection - Radio 1



SPI Bus Protection - Radio 2



NOTE: Components labeled "do not install" (DNI) are not populated by default

Bus Protection

Max Holliday

Sheet: /Bus Protection/ File: Bus_Protection.sch

Title: PyCubed Mainboard

 Size: A4
 Date: 2021-06-09
 Rev: v05c

 KiCad E.D.A. kicad (5.1.5)-3
 Id: 7/7

Bus Protection — Bypass Jumpers

SCL) R97 AND SCL_IMU SDA R98 AND SDA_IMU	MISO_RF2
2	(MISO_RF1)
SCL 108 M COL PWR SCL PWR SDA	(MISO 10 MISO_SD