

Figure 1 shows the balancing dependencies in LLVM IR. As the target for this thesis is to balance components of the actual processor, these are included as balancing milestones. The prerequisites for a milestone are all types of variables that comprise it. It is therefore balanced iff all prerequisites are balanced.

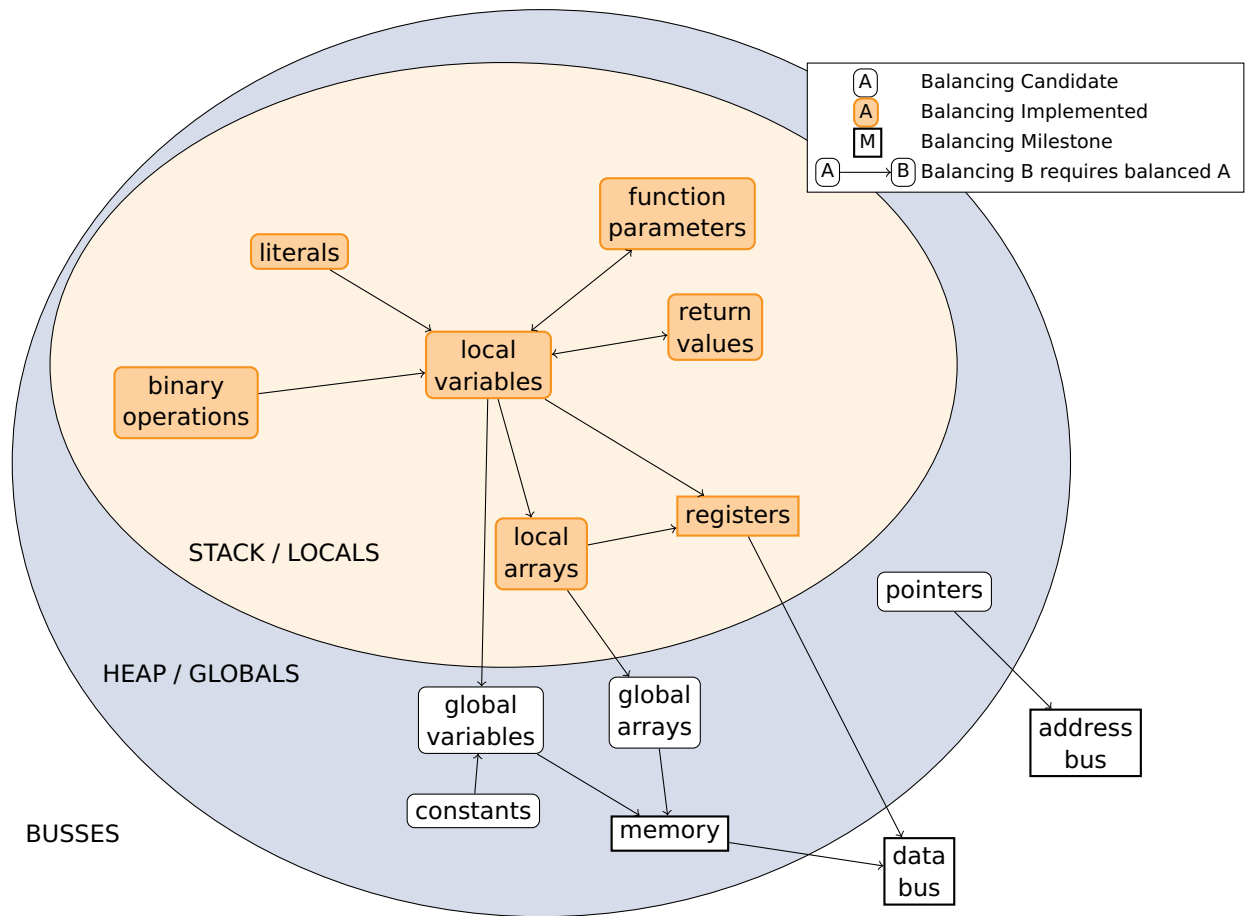


Figure 1: Balancing dependencies in LLVM