
# Porting AI to New Hardware: A Case Study

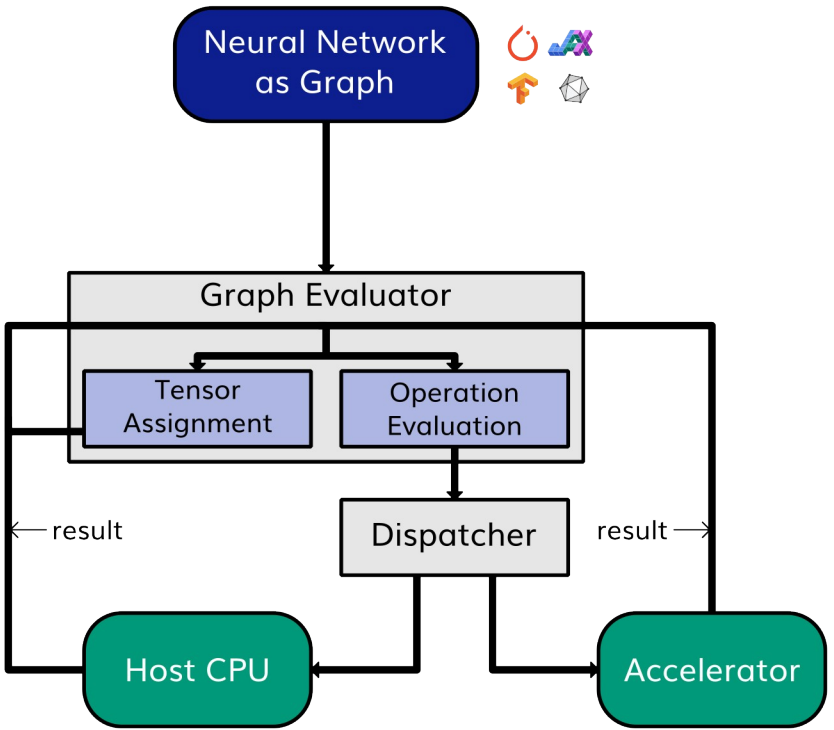
Jeremy Bennett
James Betson
Lorinc Boer
William Jones




# The Context



# AI Tooling Overview



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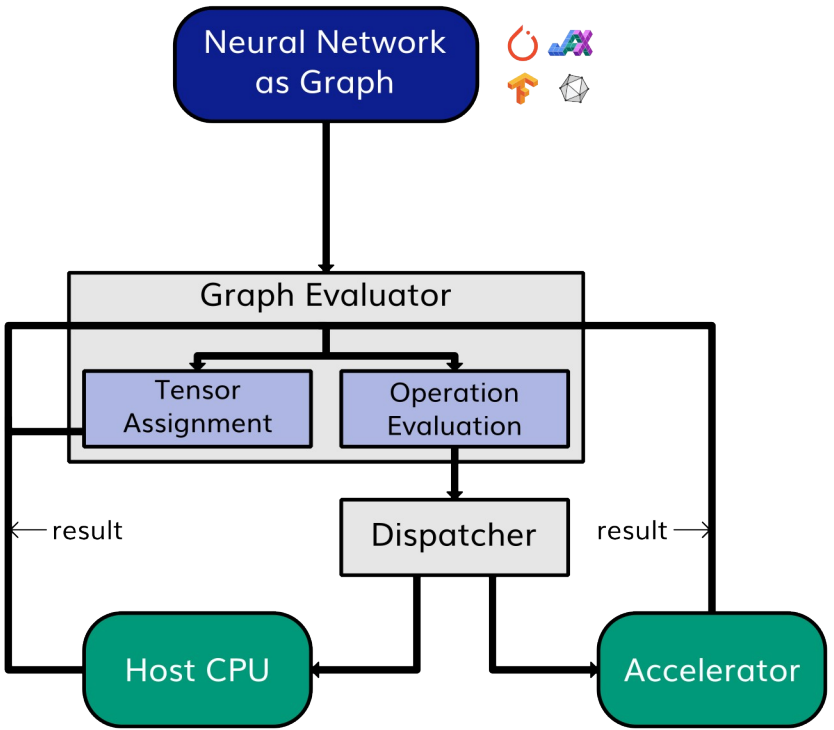
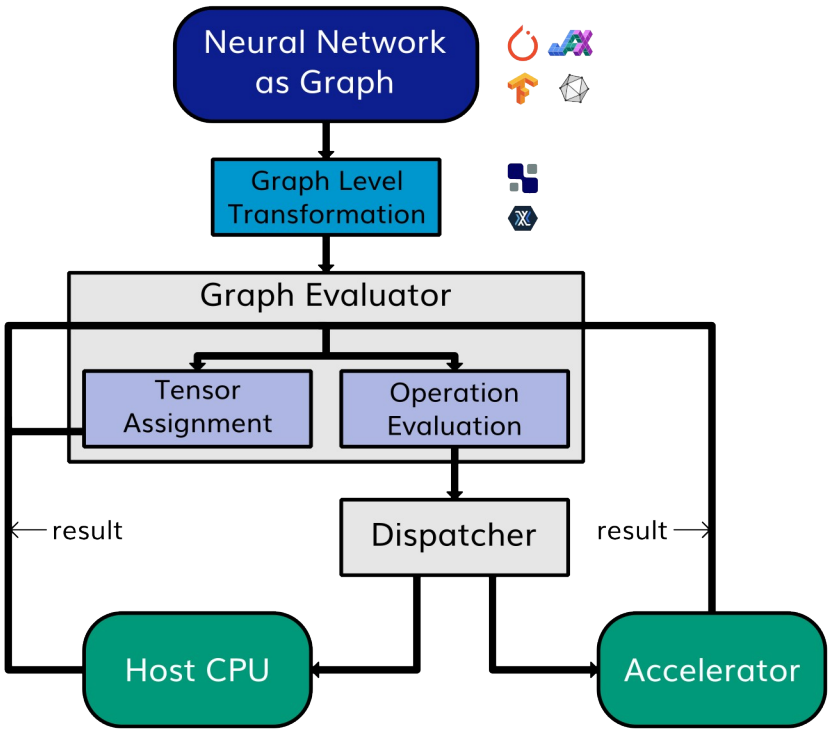
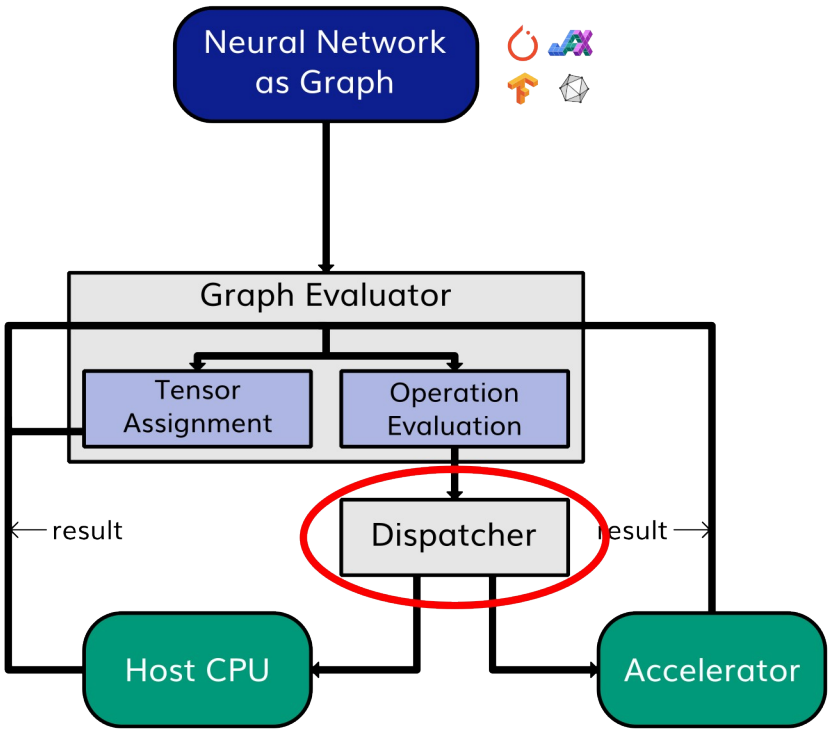
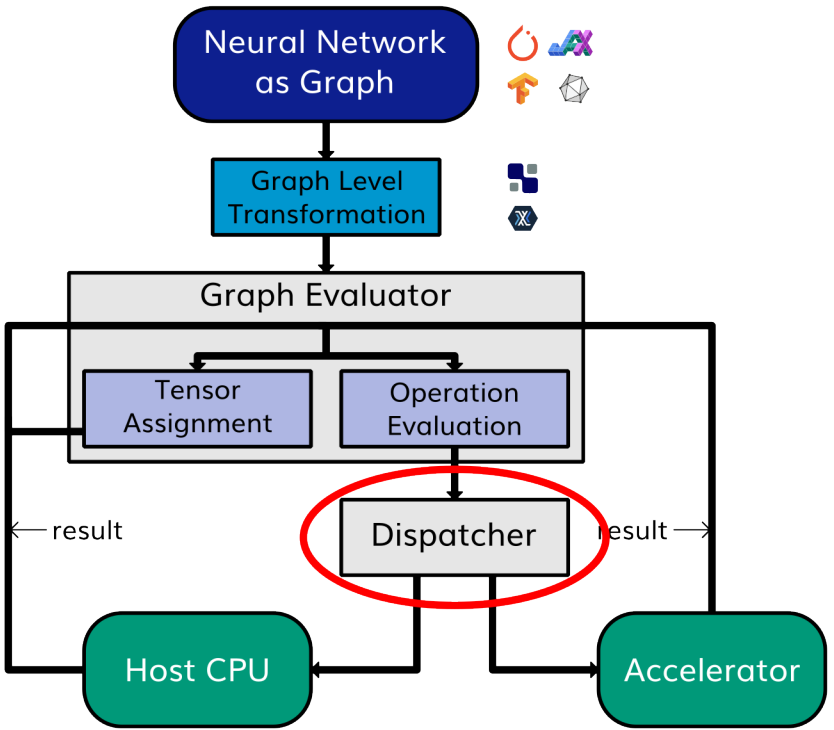


# AI Tooling: Where to Optimize?



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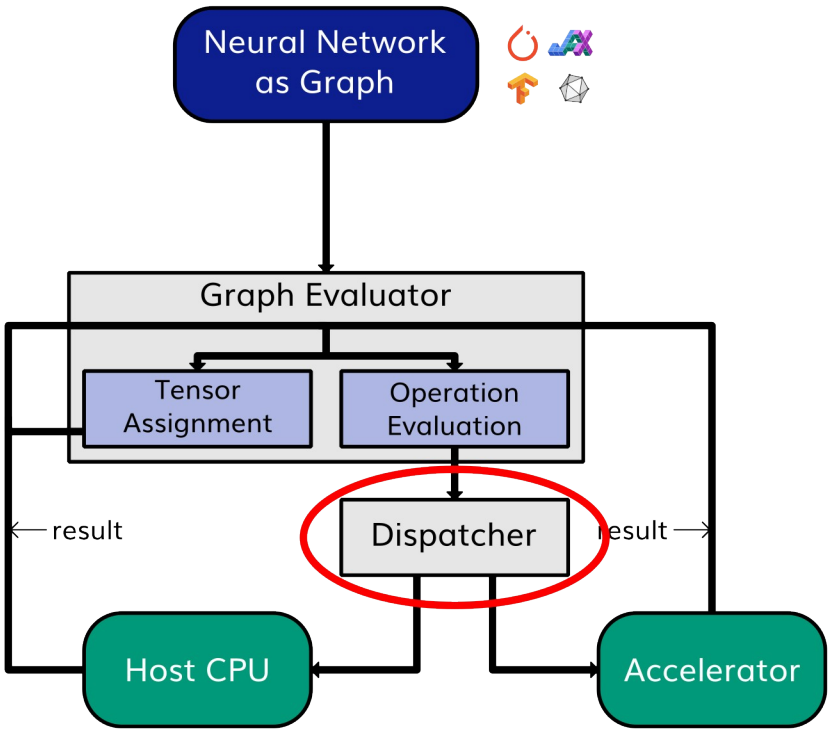
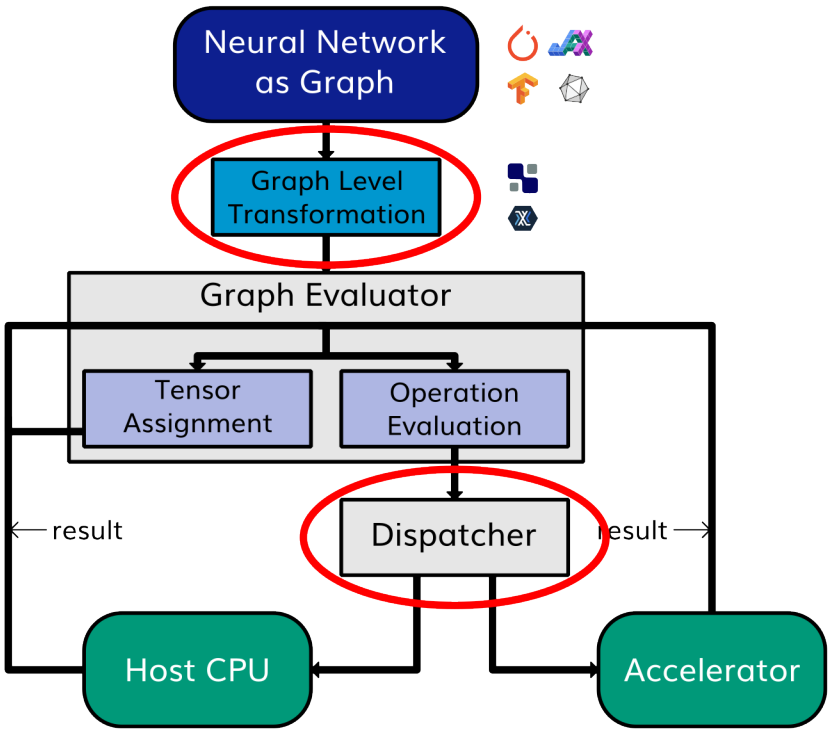
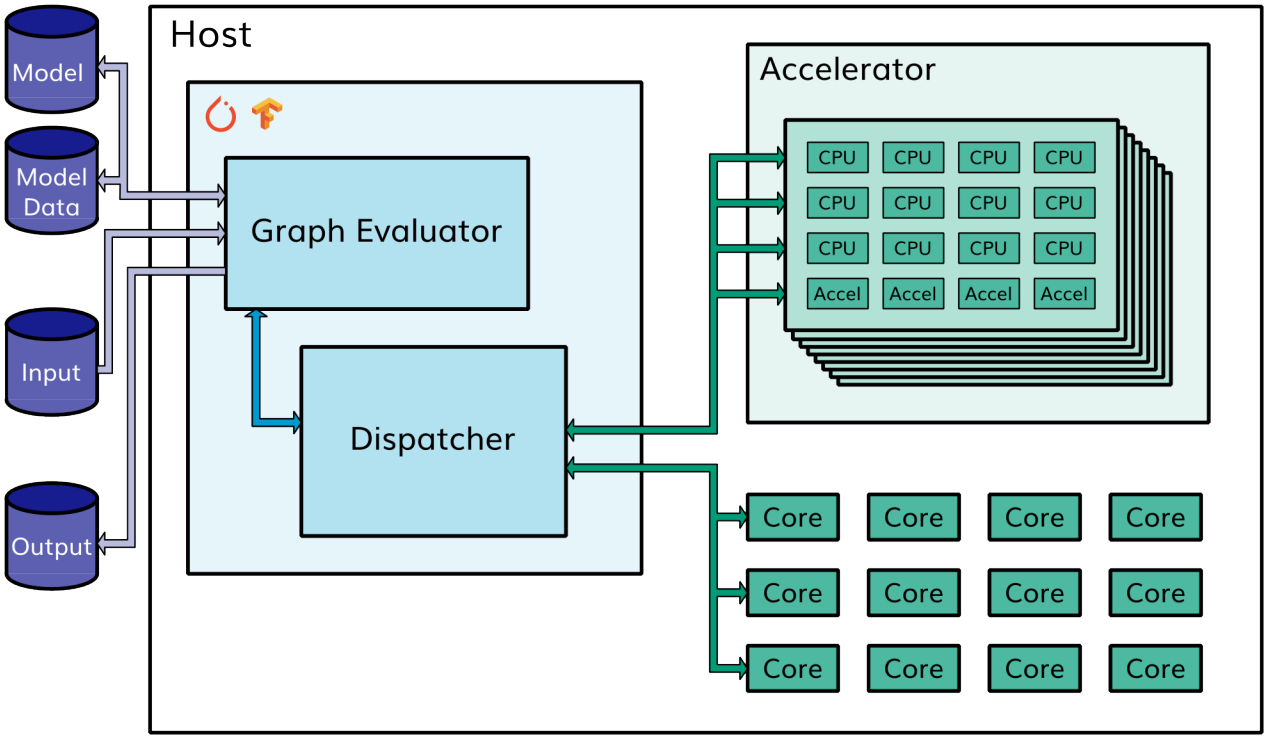
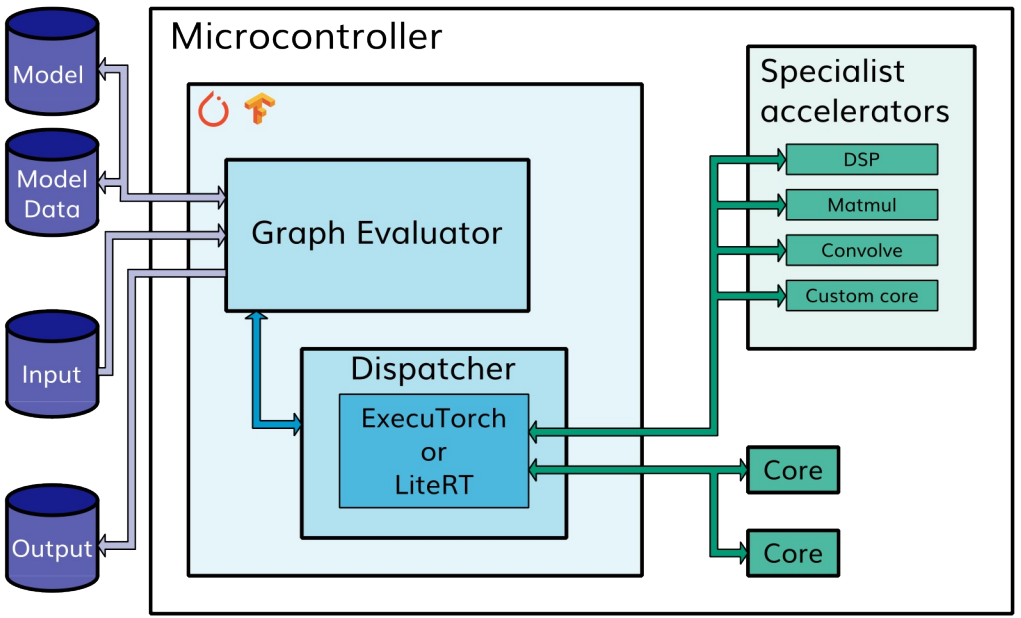


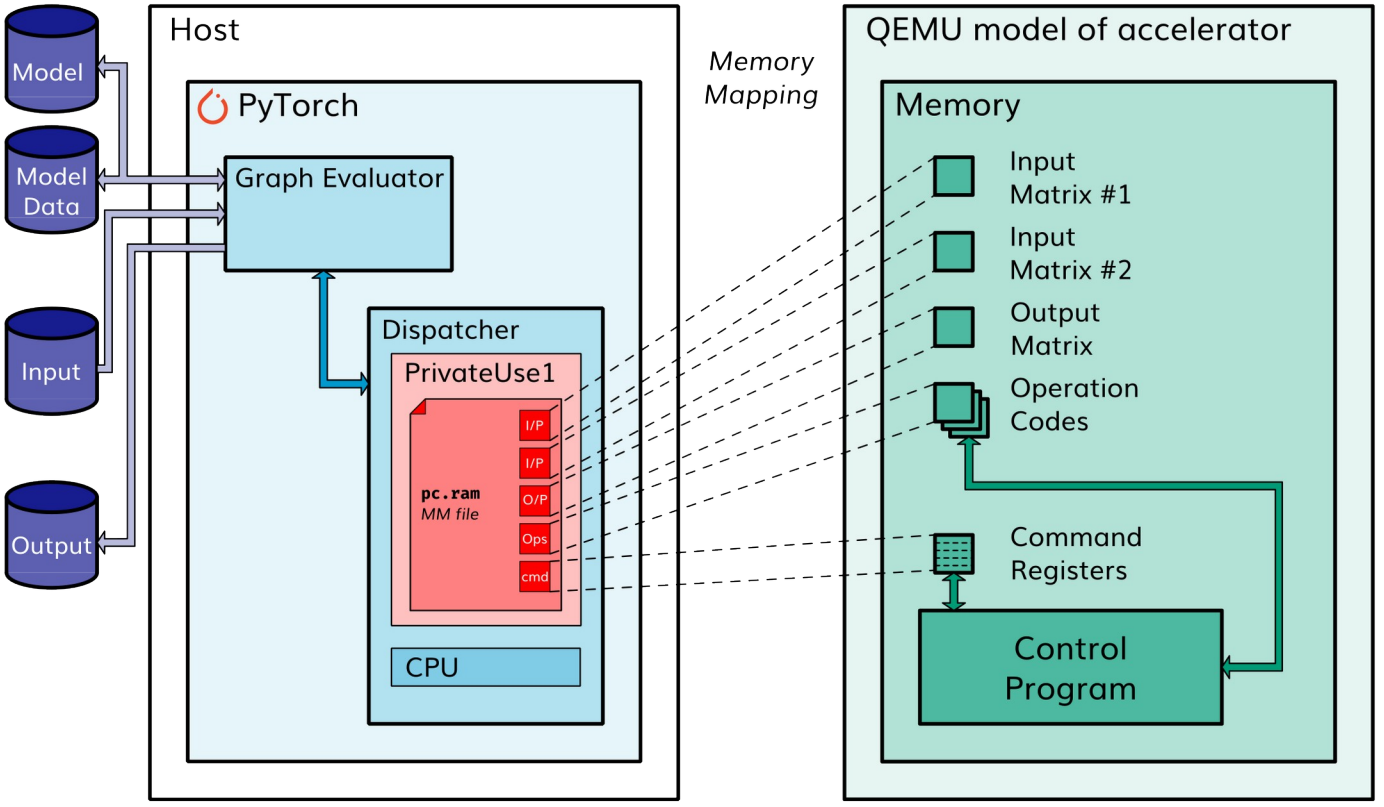
# High Performance Co-Processor for AI



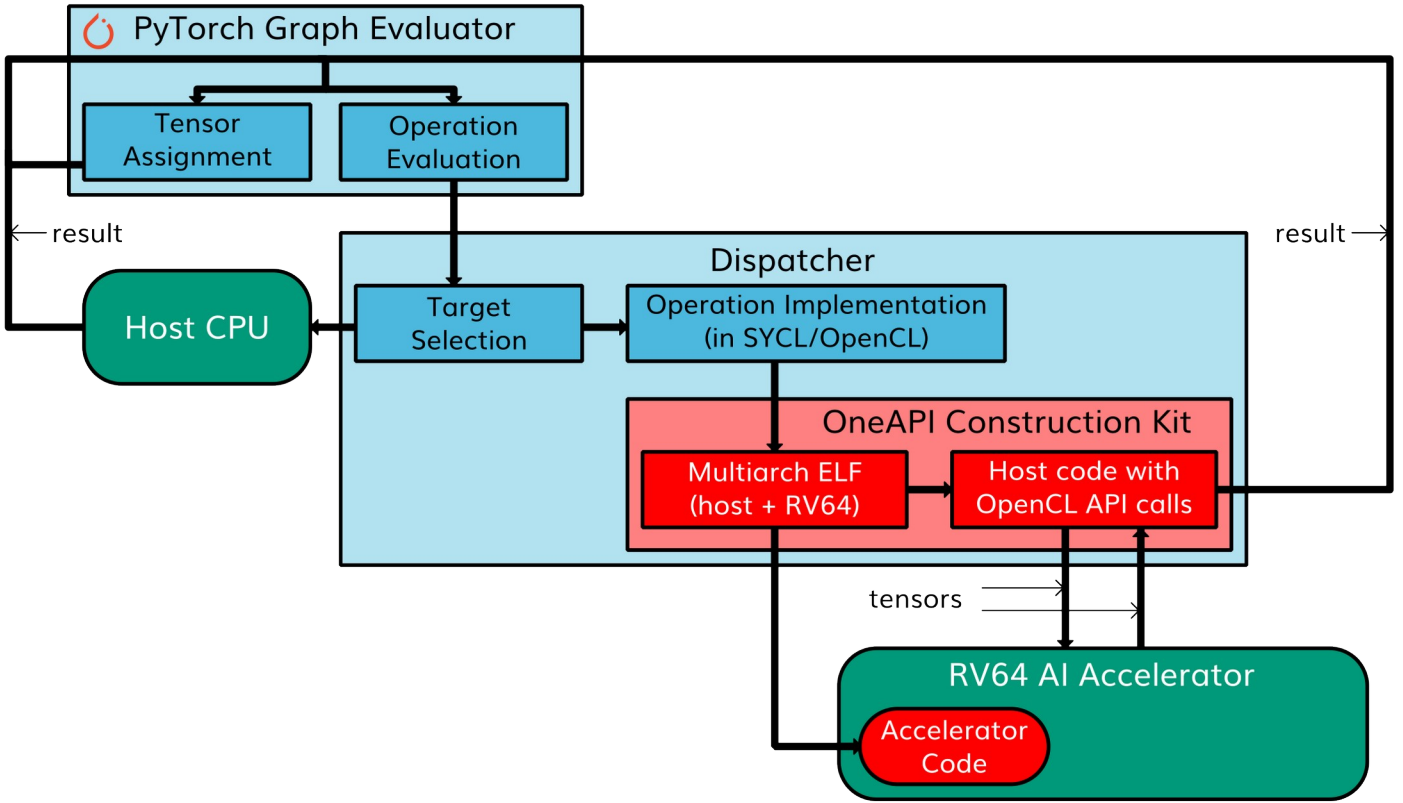
# AI Enabled Microcontrollers



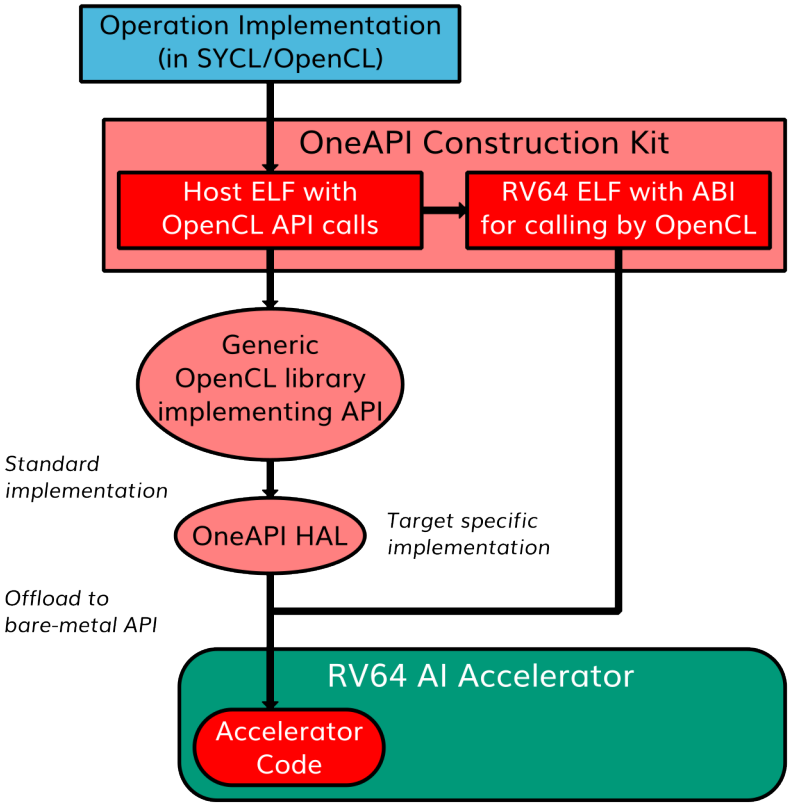
# Developing Pre-Silicon



# High Performance RISC-V for AI



# Implementing OneAPI



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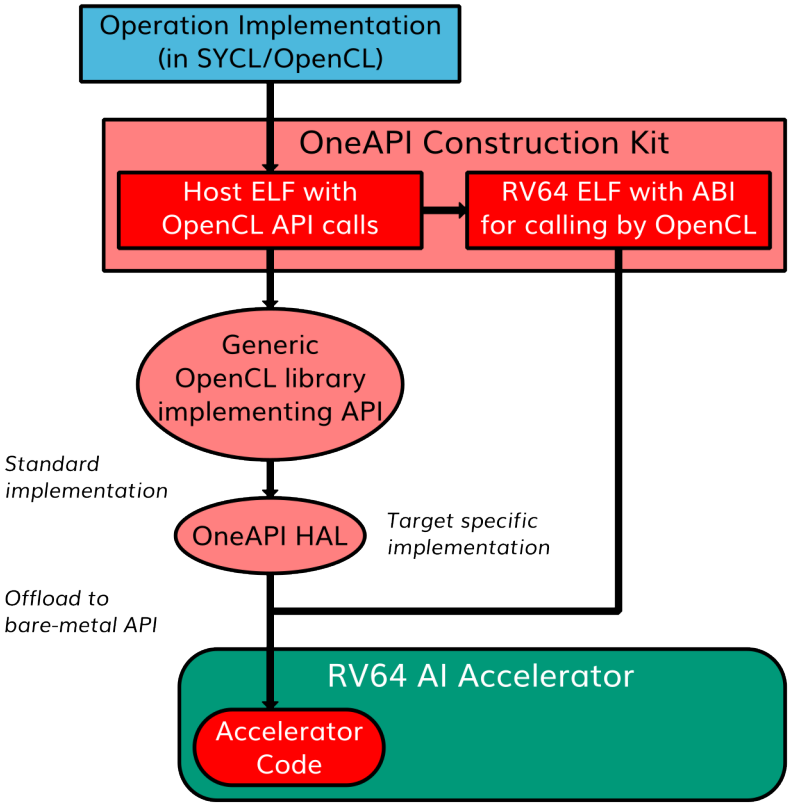


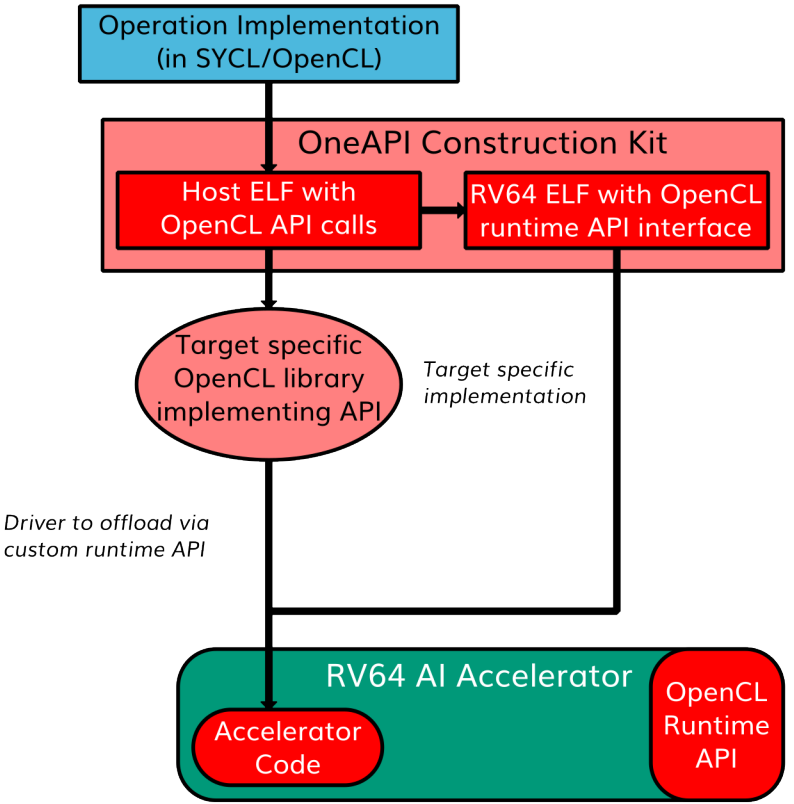

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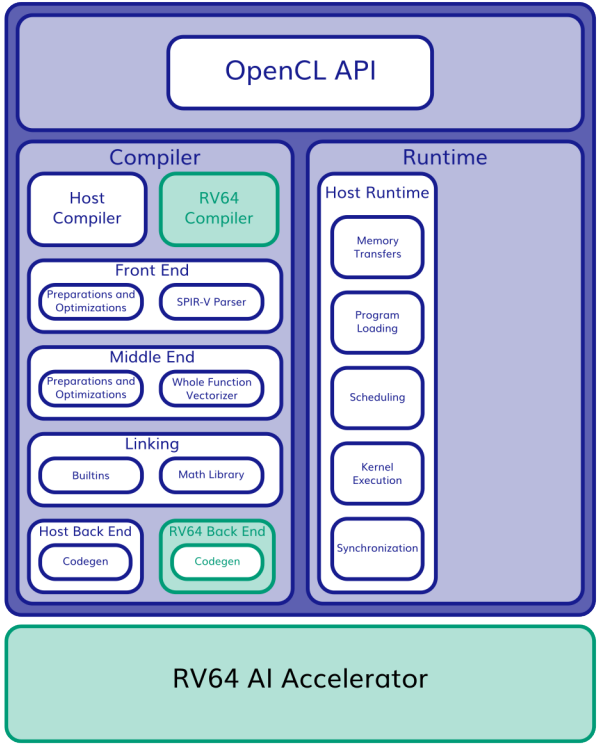

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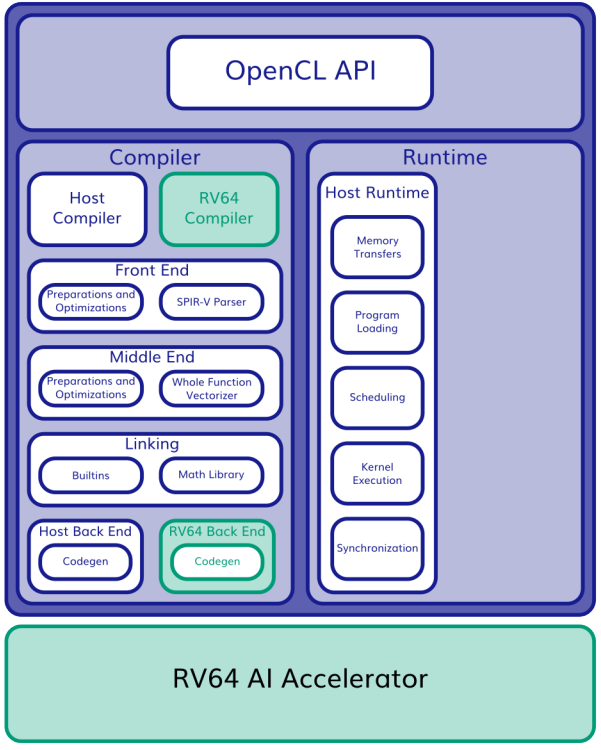


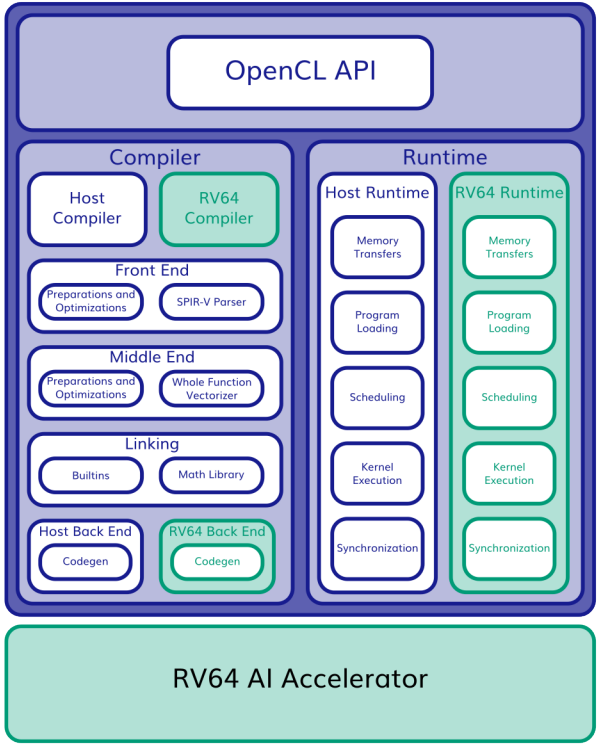


# The Challenge of AI Tooling



# Bringing Up Your Own AI Tooling

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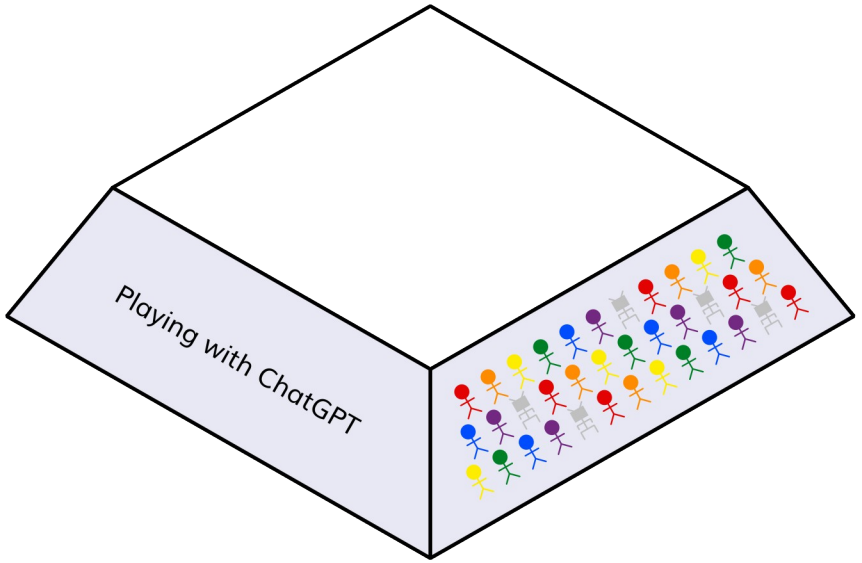

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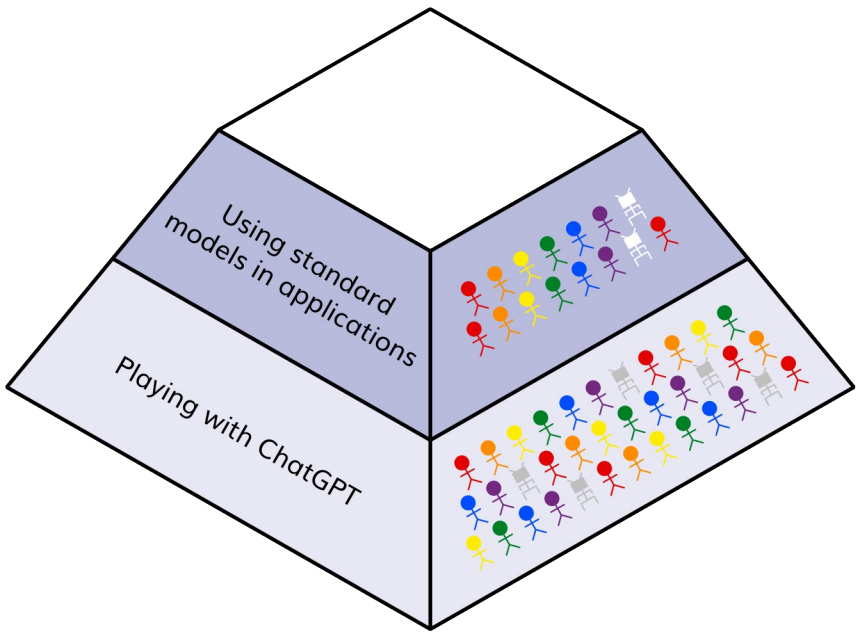

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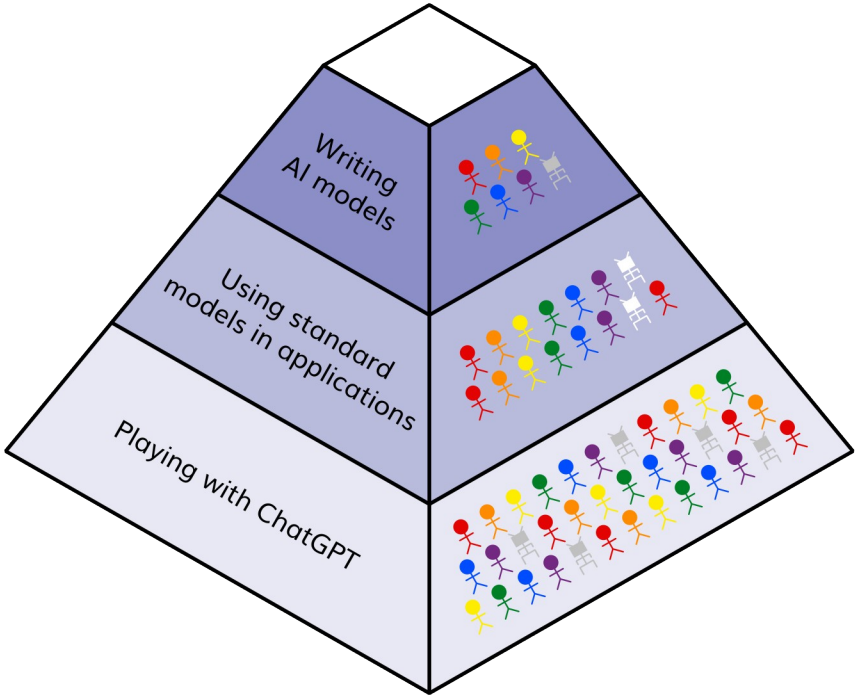

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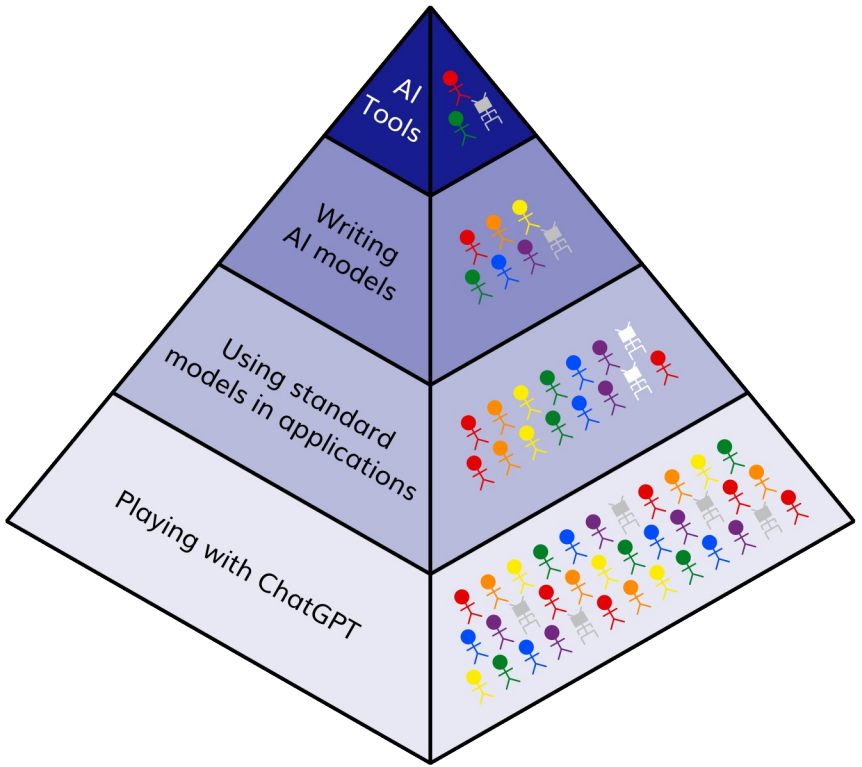

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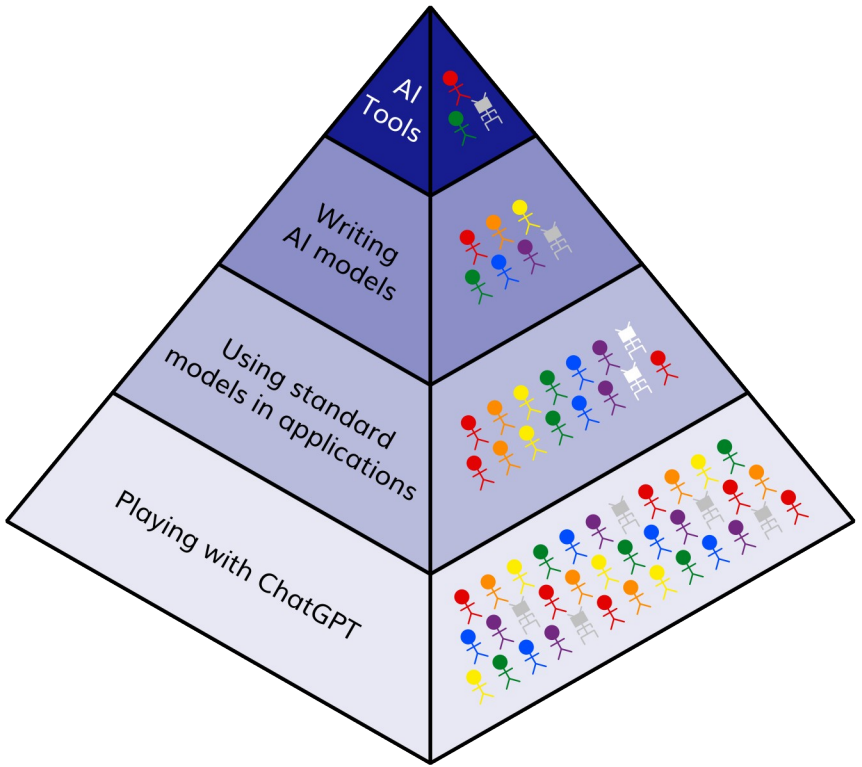


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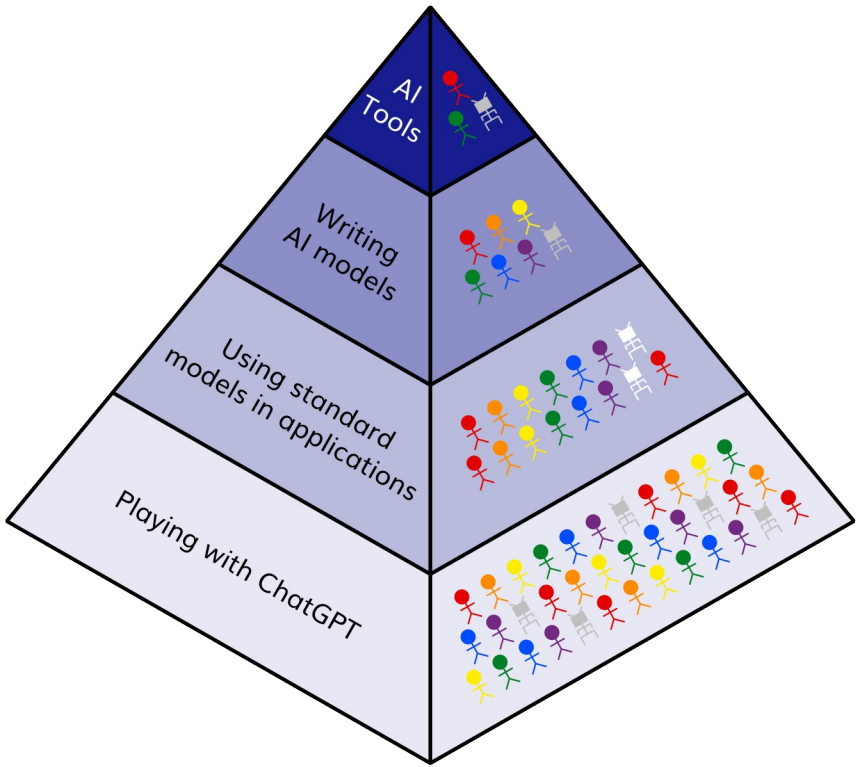


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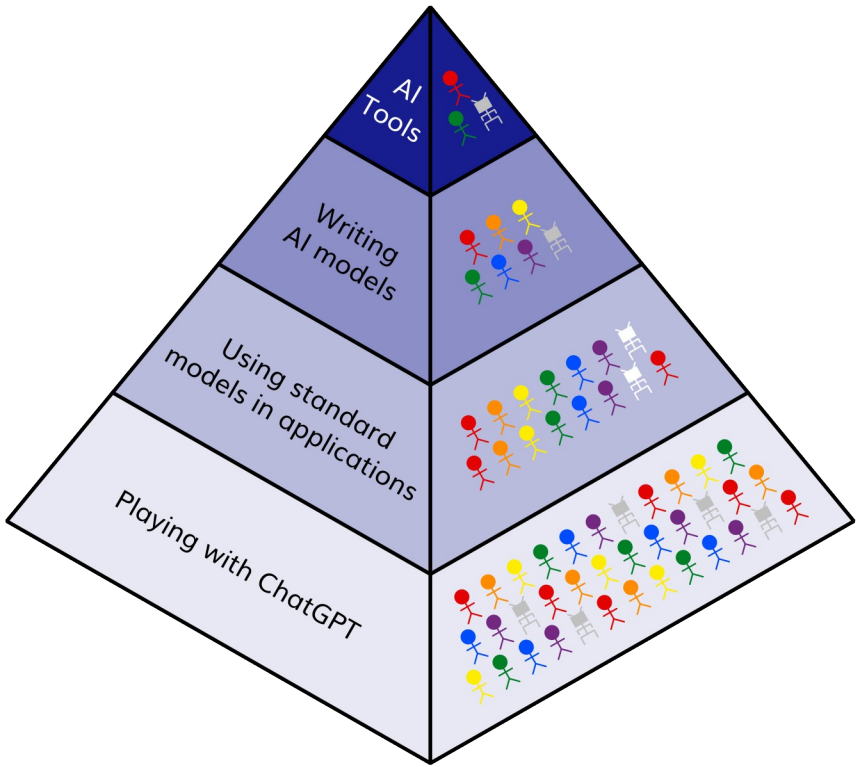


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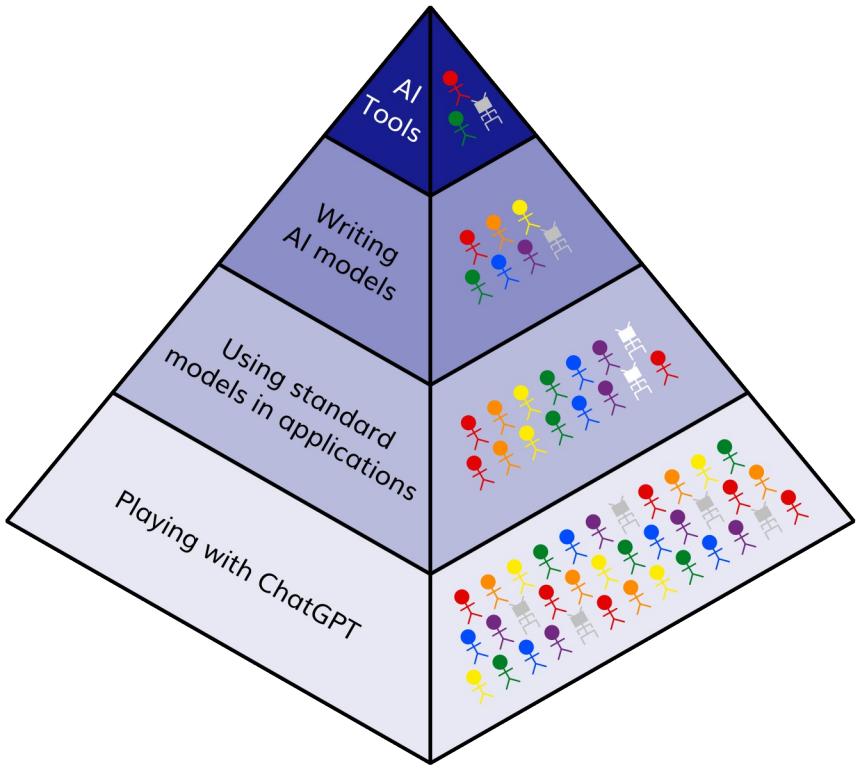


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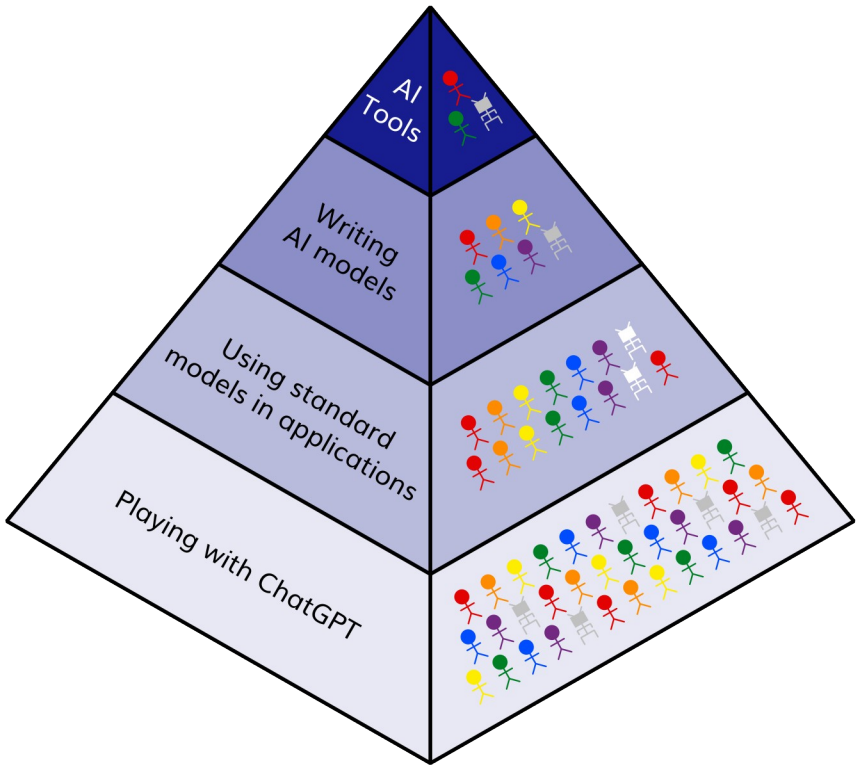


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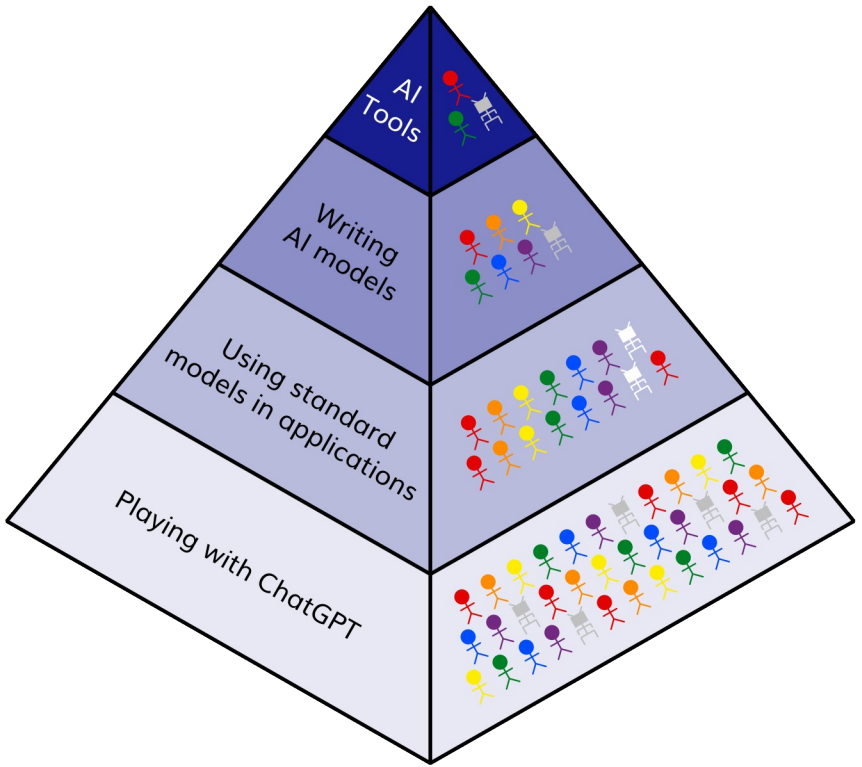


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- Ask Embecosm for help



EMBECOSM®


# Use Case: A Demonstration FPGA AI Accelerator

James Betson
Lorinc Boer




# Thank You

jeremy.bennett@embecosm.com
jb1g21@soton.ac.uk
lab2g20@soton.ac.uk
william.jones@embecosm.com

embecosm.com

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