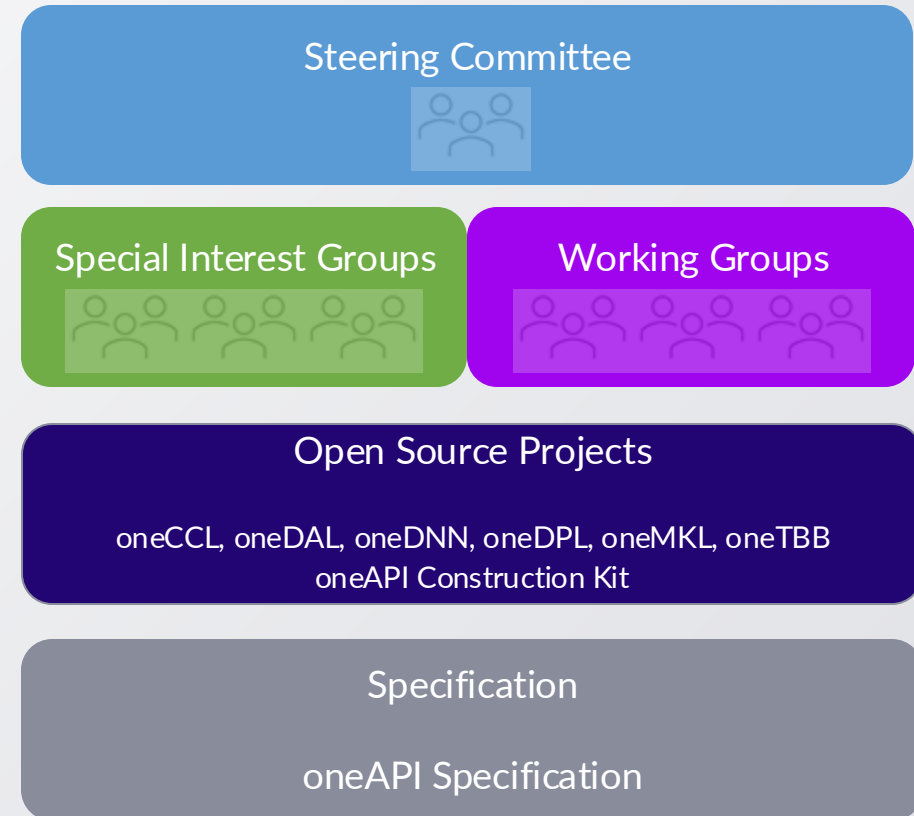# Agenda

- Quick Intro *(Alastair Murray, Codeplay)*
- AI System on FPGA using OneAPI *(Jeremy Bennett, Embecosm and James Betson & , University of Southampton)*

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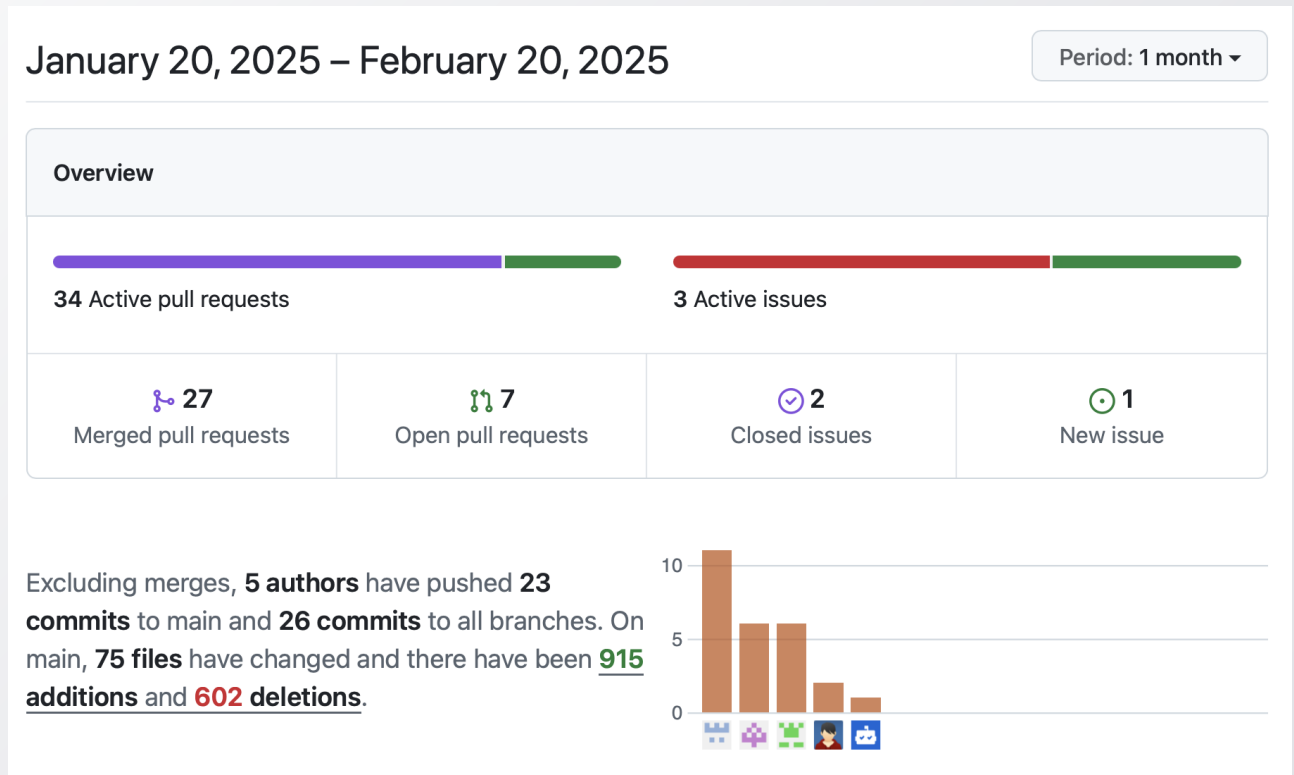Unified Acceleration Foundation

UXL Foundation

Hardware SIG

- Covers topics of interest to supporting hardware in the context of UXL
- A SIG, not a WG, we can cover relevant topics even if they aren't directly managed by the UXL foundation
- Historical topics
  - Level Zero
  - Unified Runtime
  - LLVM Offload Interface
  - oneAPI Construction Kit

Steering Committee

Special Interest Groups

Working Groups

Open Source Projects

oneCCL, oneDAL, oneDNN, oneDPL, oneMKL, oneTBB
oneAPI Construction Kit

Specification

oneAPI Specification

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oneAPI Construction Kit

- Was open sourced Apache 2.0 in February 2023

- Ownership was transferred to UXL foundation November 2024

- Aim is to make it easier for external parties to participate and have ownership via UXL Foundation

- Codeplay engineers continue to work on it

- CI for project is being improved within foundation

January 20, 2025 – February 20, 2025   Period: 1 month ⌄

Overview

34 Active pull requests   **3 Active issues**

| ⌥ **27** Merged pull requests | ⇅ **7** Open pull requests | ⊘ **2** Closed issues | ⊙ **1** New issue |

Excluding merges, **5 authors** have pushed **23 commits** to main and **26 commits** to all branches. On main, **75 files** have changed and there have been **915** additions and **602** deletions.

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Main Topic

Closing

- Next call scheduled May 8th
  - Topic suggestions welcome
- To suggest or volunteer a topic
  - Email: alastair.murray@codeplay.com
  - Or, Email: Hardware-SIG@lists.uxlfoundation.org

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