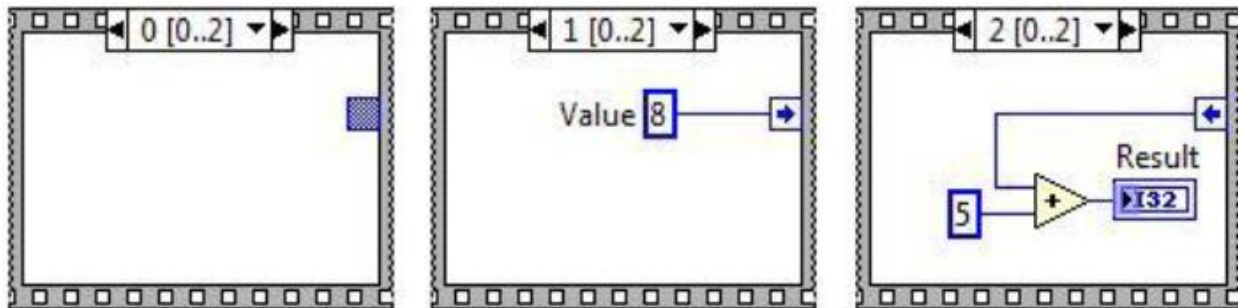


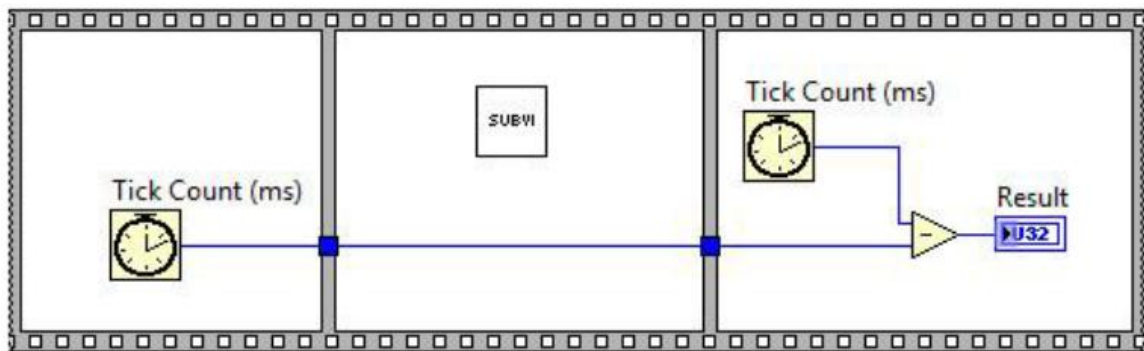
CLAD Homework 4 Questions

1. For implementing state diagrams that allow future application scalability, the best choice for a base structure is?
 - a. Sequence structure
 - b. Case structure
 - c. Formula node
 - d. Object-Oriented structure
2. If possible, a Sequence structure should be replaced with a(n):
 - a. Event structure
 - b. For loop
 - c. State machine
 - d. While loop
3. Why is the sequence local terminal displayed as unassigned in Frame 0 of the stacked sequence structure?

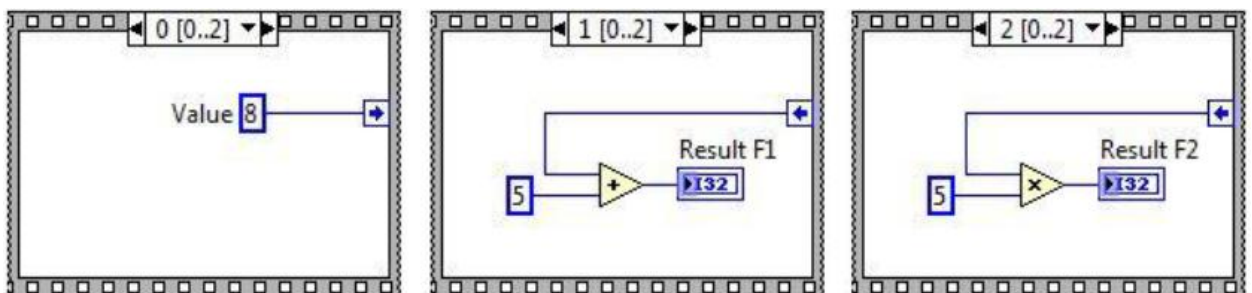


- a. The developer chose not to wire the value to any terminal in this frame
 - b. The value is available only to frames after frame 1
 - c. The data type of the terminal is incompatible with the data type of Value
 - d. The developer disabled the terminal
4. Which of the following cannot be used to transfer data?
 - a. Semaphores
 - b. Queues
 - c. Notifiers
 - d. Local variables

5. Which data synchronization mechanism ensures that no data is lost when an application temporarily provides data faster than it is able to process it?
- Notifier
 - Queue
 - Semaphore
 - Local Variable
6. What value does the Result indicator display after the code snippet executes?



- The time elapsed in milliseconds during the execution of SubVI
 - Zero
 - Number of seconds elapsed since January 1, 1970
 - The time elapsed in milliseconds during the execution of the sequence structure
7. What value does the Result F2 indicator display after the VI containing this Stacked Sequence structure executes?



- 0
- 25
- 40
- 65

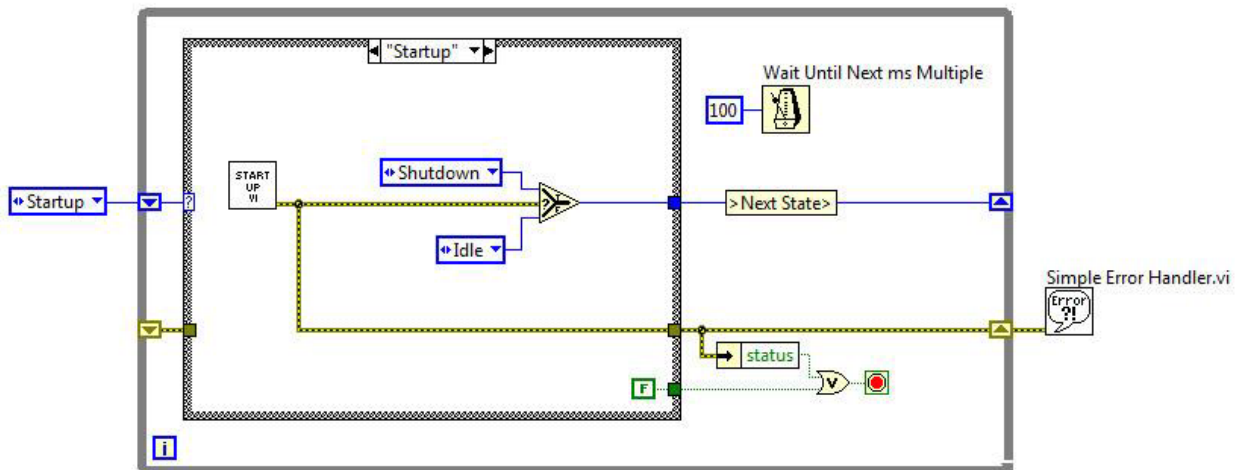
8. Which statement best describes a Notify event, such as the value change of a Boolean control?

- a. Indication that an event occurred and LabVIEW did NOT process the event
- b. Indication that an event occurred and was discarded by the user
- c. Indication that an event occurred and LabVIEW processed the event
- d. Indication that an event did NOT occur but specified Event timeout did occur

9. The Wait function can be added to While Loops:

- a. To free up available memory.
- b. To allocate memory used by the CPU.
- c. To allow the processor time to complete other tasks.
- d. To reserve which processor the code is running on.

10. The following block diagram represents which common type of VI architecture?



- a. Multiple Case Structure VI
- b. General VI
- c. State Machine VI
- d. Parallel Loop VI