

# MAC- Based on Block Ciphers

## CBC-MAC Algorithm

Input: data  $x$ , Block Cipher  $E$ , secret MAC key for  $E$   
 output:  $n$ -bit MAC on  $x$  ( $n$  is the block size of  $E$ )

1. padding & blocking: pad  $x$  if necessary, divide the padded text into  $n$ -bit blocks denoted  $x_1, \dots, x_t$
2. CBC processing.  $E_k$  is our Encryption scheme with  $k$   
 Compute  $H_t$  as follows:

$$H_1 \leftarrow E_k(x_1) \quad IV = 0$$

$$H_i \leftarrow E_k(H_{i-1} \oplus x_i) \quad \text{for } 2 \leq i \leq t$$

optional  $\rightarrow$  3. optional process to increase the strength of MAC.

Second secret key  $\rightarrow k' \neq k$

$$H_t^- \leftarrow E_{k'}^{-1}(H_t)$$

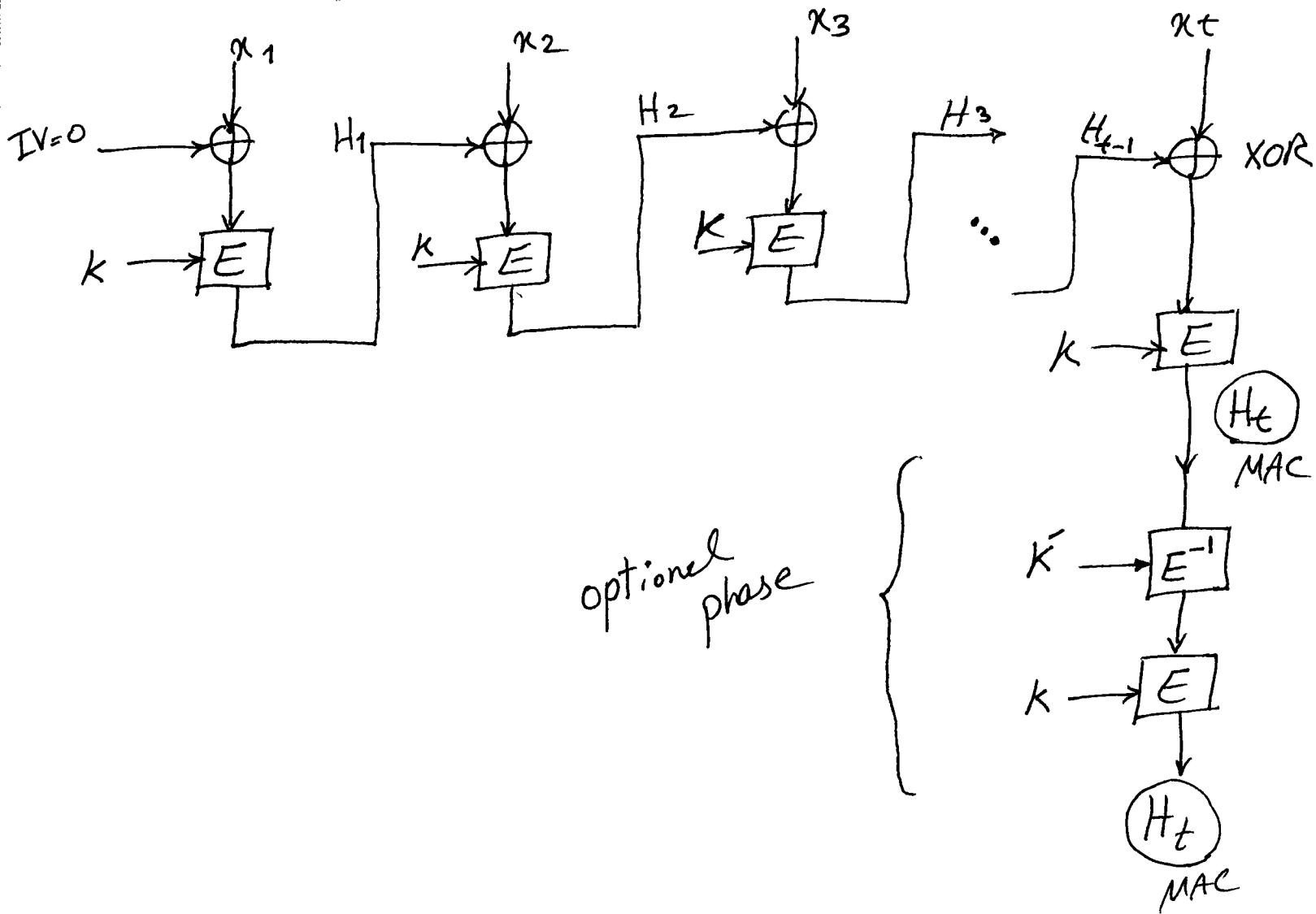
$$H_t \leftarrow E_k(H_t^-)$$

original key

4.  $n$ -bit block  $H_t$  (hash value)

# Architecture

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Note: MAC is different than MDC (Mod Detection Code)

Integrity & Authentication      Integrity

\*secret key      \*unkeyed

# Hash Functions Based on Modular Arith [3]

1995 → MASH: Modular Arithmetic Secure HASH algo.  
 Input: data  $x$  of bitlength  $0 \leq b < 2^{\frac{n}{2}}$   
 output:  $n$ -bit hash of  $x$  ( $n$  is almost the bitlength of modulus  $M$ )

(1) Setup: ⊛ select  $M = pq$  of bitlength " $m$ "  
 $p, q$  are primes & factorization is hard

⊛ the bitlength " $n$ " of the hash-result should be the largest multiple of 16 less than " $m$ "

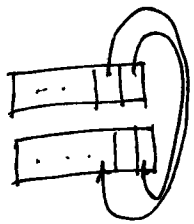
$$n = 16 n' < m \rightarrow \begin{matrix} 16 & 32 & 48 & 64 \dots \end{matrix} \downarrow m = 64\text{-bit}$$

⊛  $H_0 = 0$  as IV

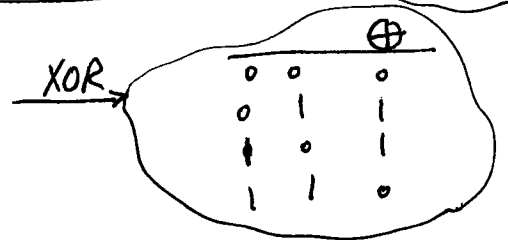
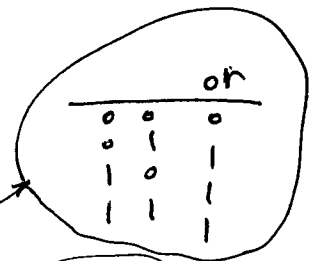
$A = 0 \text{ x } f 0 \dots 0$  Constant value

$\vee \rightarrow$  inclusive or

$\oplus \rightarrow$  exclusive or



bitwise operators



(2) padding & Blocking

pad  $x$  with 0-bits, if necessary to obtain

$t \cdot \frac{n}{2}$ -bit input ← new size for " $x$ "  
 for smallest value of " $t$ "

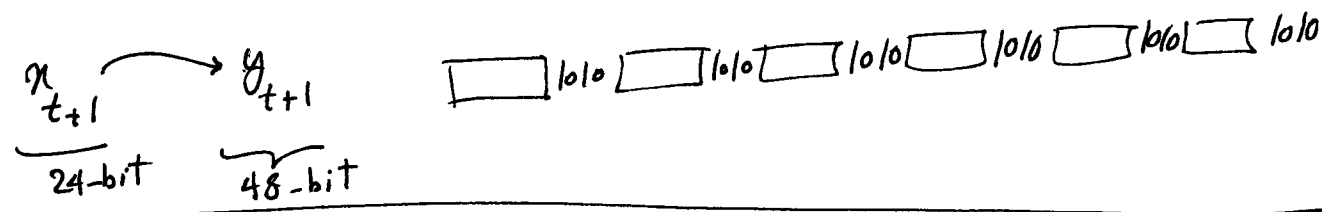
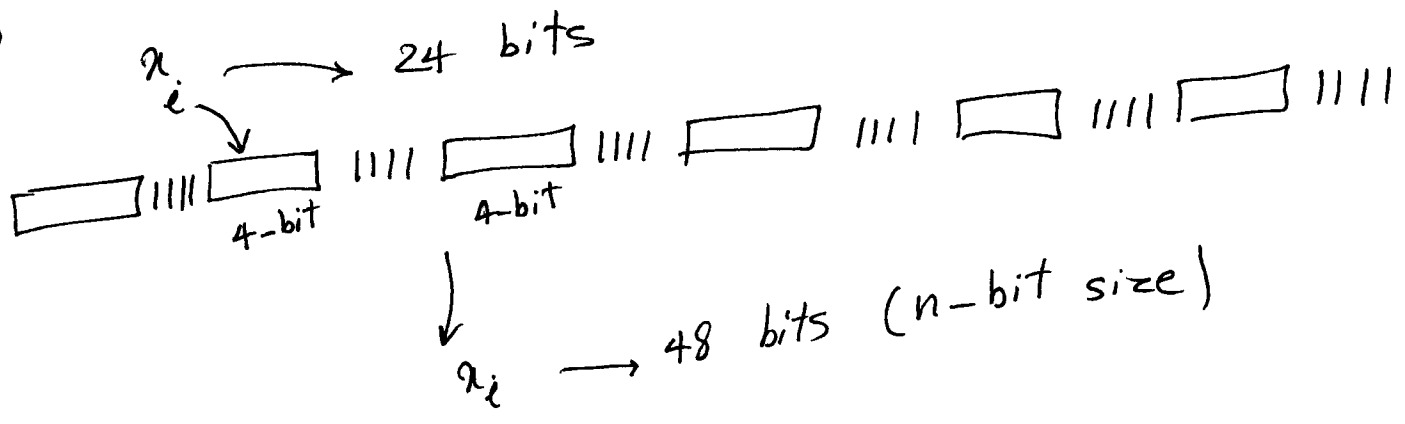
then, divide  $x$  into  $t$  blocks of size  $\frac{n}{2} \rightarrow \overset{24}{x_1} \dots x_t$

Finally,  $x_{t+1}$  denotes  $\frac{n}{2}$ -bit representation of 0

$\downarrow$   
 $\frac{n}{2}$ -bit

3. Expand  $x_i$  to an  $n$ -bit block  $y_i$  by partitioning it into 4-bit nibbles and inserting four 1-bits preceding each, except for  $y_{t+1}$  in which the inserted nibble is 1010 (not 1111).

$\frac{n}{2} = 24$



4. Compression function.  $1 \leq i \leq t+1$   
map 2  $n$ -bit inputs  $(H_{i-1}, y_i)$  to one  $n$ -bit

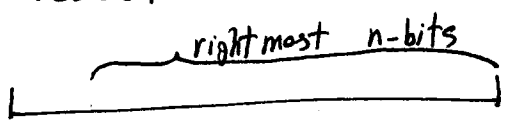
$$H_i \leftarrow \left( \left( (H_{i-1} \oplus y_i) \vee A \right)^2 \bmod M \rightarrow n \right) \oplus H_{i-1}$$

$\oplus$  : Constant value

$\vee$  : n-bit that you created in the previous stage

$\oplus$  :  $IV = H_0 = 0$

$\rightarrow n$  : denotes keeping the rightmost  $n$ -bits of the  $m$ -bit result to its left.



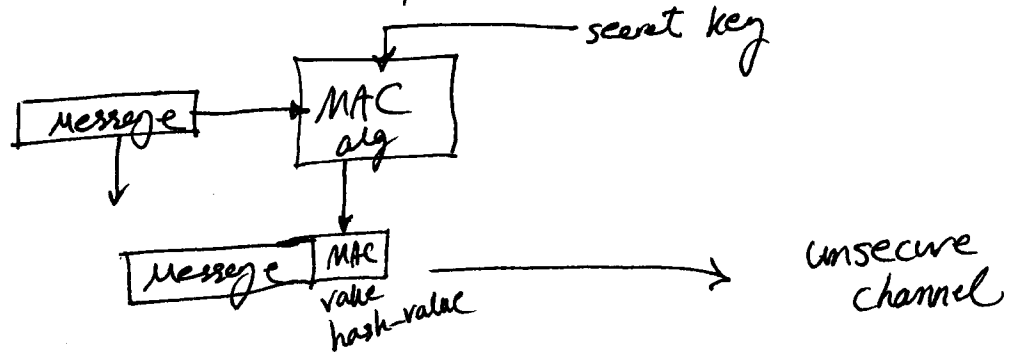
# Three methods

For DI using HF  
Data Integrity

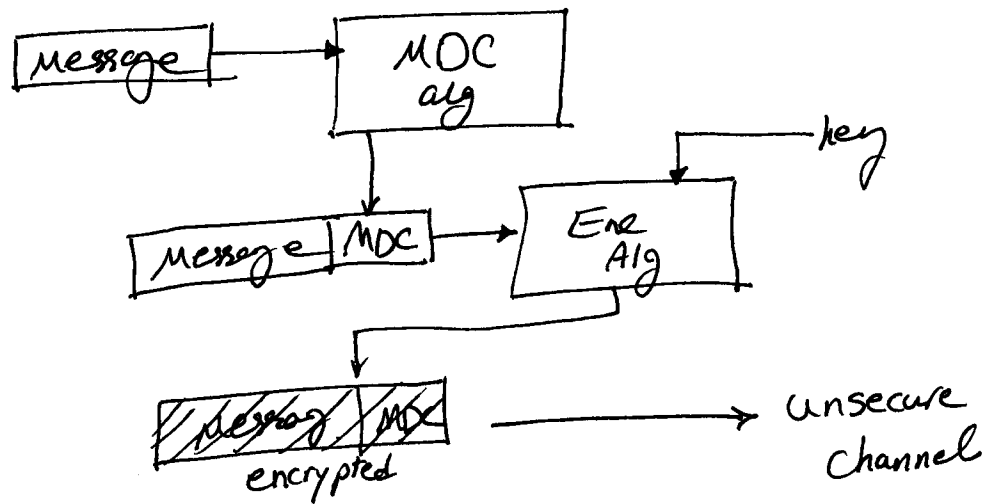
HF  
hash Functions

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(a) MAC only



(b) MDC & encipherment



(c) MDC & Authentic channel

